



Ph.D. DISSERTATION

Optimizing Homomorphic Evaluation Circuits via Search-based Method

프로그램 자동탐색 기술을 이용한 동형암호 회로 최적화에 대한 연구

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Abstract

Optimizing Homomorphic Evaluation Circuits via Search-based Method

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In this dissertation we present a new and general method for optimizing homomorphic evaluation circuits. Although fully homomorphic encryption (FHE) holds the promise of enabling safe and secure third party computation, building FHE applications has been challenging due to their high computational costs. Domain-specific optimizations require a great deal of expertise on the underlying FHE schemes, and FHE compilers that aims to lower the hurdle, generate outcomes that are typically sub-optimal as they rely on manually-developed optimization rules. In this dissertation, based on the prior work of FHE compilers, we propose a method for automatically learning and using optimization rules for FHE circuits. Our method focuses on reducing the maximum multiplicative depth, the decisive performance bottleneck, of FHE circuits by combining program synthesis, term rewriting, and equality saturation. It first uses program synthesis to learn equivalences of small circuits as rewrite rules from a set of training circuits. Then, we perform term rewriting on the input circuit to obtain a new circuit that has lower multiplicative depth. Our rewriting method uses the equational matching with generalized version of the learned rules, and its soundness property is formally proven. Our optimizations also try to explore

every possible alternative order of applying rewrite rules by time-bounded exhaustive search technique called equality saturation. Experimental results show that our method generates circuits that can be homomorphically evaluated 1.08x - 3.17x faster (with the geometric mean of 1.56x) than the state-of-the-art method. Our method is also orthogonal to existing domain-specific optimizations.

Keywords: Homomorphic Evaluation Circuit, Program Synthesis, Term Rewriting, Equality Saturation, Optimization, Search-based Method Student Number: 2015-22908

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Chapter 1

Introduction

1.1 Fully Homomorphic Encryption

Fully Homomorphic Encryption (FHE) [33] enables safe and secure third-party computation with private data because it enables any computation on encrypted data without the decryption key. Because the cloud can perform any computation on encrypted data without learning about the data itself, the clients can safely upload their encrypted data without trusting the software/hardware vendors of the cloud.



Figure 1.1: Secure third-party computation with private data.

Figure 1.1 illustrates this scenario. User encrypts the private data using private key first, and transfer it to the third-party. The third-party gets encrypted result by performing homomorphic evaluation on encrypted data, and transfers it to the user. User can decrypt the result using the same private key that is used for encryption. In this scenario, the third-party can not identify any private data during computation. Even if malicious hackers intercept the data communication, they can not identify any private data since the user and the third-party communicate with encrypted data only.

Problem

However, building FHE applications has been challenging at the moment because of their high computational costs. Though building FHE applications itself does not require much expertise thanks to off-the-shelf libraries of FHE schemes [35, 53, 34], when naively implemented, even with the best FHE schemes [10, 18], FHE applications incur slowdown factors of orders of magnitudes compared to their plaintext version. One key challenge is therefore reducing the costs of FHE applications so that they become amenable to practical use.

Existing Approaches

There have been two approaches – domain-specific optimizations and optimizing FHE compilers – for reducing the costs of FHE applications, but they are still less than desirable.

Various domain-specific FHE optimization techniques have been successfully developed, but developing such techniques requires a great deal of expertise on the underlying FHE schemes. For example, optimizations such as rescaling [26], data movement [44] and batching [46], and heuristics for encryption parameter selection [28, 26] enable several orders of magnitude speedups in a wide range of FHE applications (such as image recognition [26], statistical analysis [44], sorting [17], bioinformatics [19], database [8], and static program analysis [43]).

Yet, such improvement requires a great deal of expertise in cryptography.

Lowering this hurdle of expertise is a goal of FHE compilers, which, equipped with FHE optimization techniques, automatically transform conventional plaintext programs into optimized FHE code. For example, RAMPARTS [4], CINGULATA [16] and ALCHEMY [24] take programs written in a high-level language (e.g., Julia, C++, or a custom DSL) and transform them into arithmetic representations which can be evaluated using a backend FHE scheme.

However, though the users do not have to concern about low-level details of underlying schemes when writing applications, they need to write FHE-friendly algorithms [14, 17, 19, 44] to achieve the desired efficiency.

For example, using well-known sorting algorithms such as quick-sort and merge-sort for homomorphic evaluation leads to a significant performance degradation.

The two main reasons are data dependency and different cost model. First, since we can utilize data dependency to evaluate if-condition statement in plaintext algorithms, there is no additional cost required for evaluating an if-condition statement whose execution result can be varied by input data. According to the evaluation result of conditional expression, it is enough to evaluate only one branch. (i.e. we can utilize data dependency to evaluate if-condition statement). However, in homomorphic algorithms, it requires much higher cost to evaluate the if-condition statement. In homomorphic algorithms, since the decryption values of ciphertexts can not be known in the process of homomorphic evaluation for the condition expression, the if-condition statement must be translated into arithmetized form that includes both branches' homomorphic evaluation result [17]. Furthermore, in the case of homomorphic algorithms, it is necessary to consider a new decisive performance factor multiplicative depth (Section 1.2.2).

Therefore, the performance of the homomorphic algorithm should be measured with a new cost model that takes the number of comparison operations and the multiplicative depth into account. Since the existing efficient sorting algorithms such as quick-sort and mergesort are based on a large number of comparison operations, FHE-friendly sorting algorithms [17] have been proposed that minimizes the number of comparison operations and multiplicative depth.

Furthermore, state-of-the-art compilers rely on hand-written optimization rules whose findings require expertise and are likely to remain sub-optimal. Designing specialized optimization rules for homomorphic compilers requires a great deal of expertise on its code translation process. Furthermore, due to the distinct cost model, it is hard to utilize the traditional compiler optimization methods. Additionally, homomorphic encryption programs are basically boolean circuits which are challenging for humans to reason about at a high level. Thus, it is extremely difficult to overcome these obstacles and design new optimization rules.

1.2 **Problem Definition**

Our Approach

In this dissertation, in the context of optimizing FHE compilation, we propose a novel and general method for optimizing FHE boolean circuits that outperforms existing automatic FHE optimization techniques. Our method focuses on reducing the number of nested multiplications and achieves sizeable optimizations even for existing domain-specific manually optimized results.

Our setting of the optimization problem is simple. Let c be an arithmetic code that can be evaluated using FHE schemes, which can be either generated by a FHE compiler or manually written by a developer. Optimization is to find a new circuit c' of lower computational cost while guaranteeing the same semantics as the original.

Because the decisive performance bottleneck in homomorphic computation is the nested depth of multiplications [4, 16, 17, 59], we set the computation cost to be measured using the maximum multiplicative depth, which is simply the maximum number of sequential multiplications required to perform the computation. For example, the circuit $c(x_1, x_2, x_3, x_4, x_5) = ((x_1x_2)x_3)x_4 + x_5$ has multiplicative depth 3. The lower the multiplicative depth is, the more efficiently a circuit can be evaluated. For example, we can optimize c by replacing it with $c'(x_1, x_2, x_3, x_4, x_5) = (x_1x_2)(x_3x_4) + x_5$ that has depth 2.

Although FHE scheme has been continuously improved from Gentry's first generation FHE scheme [33] to CKKS' fourth generation FHE scheme [18], still it is the same that the multiplicative depth is the decisive performance bottleneck.

In the rest of this section, we formally define the problem of minimizing the multiplicative depth of Boolean circuits. We first provide background on homomorphic encryption (Section 1.2.1) and Boolean circuits (Section 1.2.2). In Section 1.2.3, we formally state the problem.

1.2.1 Homomorphic Encryption

A fully homomorphic encryption (FHE) scheme with binary plaintext space $\mathbb{Z}_2 = \{0, 1\}$ can be described by the interface:

$$\begin{split} \mathsf{Enc}_{pk} &: \mathbb{Z}_2 \to \Omega & \mathsf{Dec}_{sk} : \Omega \to \mathbb{Z}_2 \\ \mathsf{Add}_{pk} &: \Omega \times \Omega \to \Omega & \mathsf{Mul}_{pk} : \Omega \times \Omega \to \Omega \end{split}$$

where pk is a public key, sk is a secret key, Ω is a ciphertext space (e.g. large cardinality set such as \mathbb{Z}_q , i.e., integers modulo an integer q). For all plaintexts $m_1, m_2 \in \mathbb{Z}_2$, the interface should satisfy the following algebraic properties:

$$\mathsf{Dec}_{sk}(\mathsf{Add}_{pk}(\mathsf{Enc}_{pk}(m_1),\mathsf{Enc}_{pk}(m_2))) = m_1 + m_2,$$
$$\mathsf{Dec}_{sk}(\mathsf{Mul}_{pk}(\mathsf{Enc}_{pk}(m_1),\mathsf{Enc}_{pk}(m_2))) = m_1 \times m_2.$$

Note that such a scheme is able to potentially evaluate all Boolean circuits as addition and multiplication in \mathbb{Z}_2 correspond to XOR and AND operations.

As an instance, let us consider a simple symmetric version (where only a secret key is used for both encryption and decryption) of the HE scheme [27] based on approximate common divisor problems [37]:

- The secret key (sk) is a random integer p.
- For a plaintext m, Enc(m) outputs pq + 2r + m, where q and r are randomly chosen integers such that $|r| \ll |p|$. r is a noise for ensuring semantic security [49].
- For a ciphertext \bar{c} , $\mathsf{Dec}(\bar{c})$ outputs $((\bar{c} \mod p) \mod 2)$.
- For ciphertexts \bar{c}_1 and \bar{c}_2 , $\mathsf{Add}(\bar{c}_1, \bar{c}_2)$ outputs $\bar{c}_1 + \bar{c}_2$.
- For ciphertexts \bar{c}_1 and \bar{c}_2 , $\mathsf{Mul}(\bar{c}_1, \bar{c}_2)$ outputs $\bar{c}_1 \times \bar{c}_2$.

For ciphertexts $\bar{c}_1 \leftarrow \text{Enc}(m_1)$ and $\bar{c}_2 \leftarrow \text{Enc}(m_2)$, we know each \bar{c}_i is of the form $\bar{c}_i = pq_i + 2r_i + m_i$ for some integer q_i and noise r_i . Hence $\text{Dec}(\bar{c}_i) = ((\bar{c}_i \mod p) \mod 2) = m_i$, if $|2r_i + m_i| < p/2$. Then, the following equations hold:

$$\bar{c}_1 + \bar{c}_2 = p(q_1 + q_2) + \underbrace{2(r_1 + r_2) + m_1 + m_2}_{\text{noise}_{\text{Add}}}$$

$$\bar{c}_1 \times \bar{c}_2 = p(pq_1q_2 + \dots) + \underbrace{2(2r_1r_2 + r_1m_2 + r_2m_1) + m_1m_2}_{\text{noise}_{\text{Mult}}}$$

Based on these properties, we can show that

$$Dec(\bar{c}_1 + \bar{c}_2) = m_1 + m_2$$
 and $Dec(\bar{c}_1 \times \bar{c}_2) = m_1 \cdot m_2$

if the absolute values of noise_{Add} and noise_{Mult} are less than p/2. Note that the noise in the resulting ciphertext increases during homomorphic addition and multiplication (twice and quadratically as much noise as before respectively). If the noise becomes larger than p/2, the decryption result of the scheme will be spoiled. As long as the noise is managed, the scheme is able to potentially evaluate all Boolean circuits.

Because managing the noise growth is very expensive and the noise growth induced by multiplication is much larger than that by addition, the performance of homomorphic evaluation is often measured by the maximum multiplicative depth of evaluated circuits. The maximum multiplicative depth influences parameters of a HE scheme. Minimizing the multiplicative depth results in not only smaller ciphertexts but also less overall execution time. For example, to support a large number of consecutive multiplications, the secret key p should also be huge in the aforementioned scheme, and it increases overall computational costs. Current FHE schemes are leveled (also called somewhat homomorphic) in that for fixed encryption parameters they only support computation of a particular depth.¹

1.2.2 Boolean Circuit and Multiplicative Depth Boolean Circuit

A Boolean circuit $c \in \mathbb{C}$ is inductively defined as follows:

$$c \hspace{.1in} \rightarrow \hspace{.1in} \wedge (c,c) \hspace{.1in} | \hspace{.1in} \oplus (c,c) \hspace{.1in} | \hspace{.1in} x \hspace{.1in} | \hspace{.1in} 0 \hspace{.1in} | \hspace{.1in} 1$$

where \wedge and \oplus denote AND and XOR respectively, and x denotes an input variable. The grammar is functionally complete because any Boolean functions can be expressed using the grammar. For simplicity, we assume that circuits have a single output value. We will often denote $1 \oplus c$ or $c \oplus 1$ as $\neg c$. In addition, we will use infix notation for \oplus and \wedge .

Multiplicative Depth

Let $\ell : \mathbb{C} \to \mathbb{N}$ be a function that computes the multiplicative depth of a circuit, which is inductively defined as follows:

$$\ell(c) = \begin{cases} 1 + \max_{i \in \{1,2\}} \ell(c_i), & c = \wedge(c_1, c_2) \\ \max_{i \in \{1,2\}} \ell(c_i), & c = \oplus(c_1, c_2) \\ 0, & \text{otherwise} \end{cases}$$

¹A leveled scheme may be turned into a fully homomorphic one by introducing a bootstrapping operation [33], which is computationally heavy.

Critical Path

The input-to-output paths with the maximal number of AND gates are called critical paths. A set of critical paths, denoted $\mathcal{P}(c)$, of a circuit c is a set of strings over the alphabet of positive integers, which is inductively defined as follows:

- If c = x or 0 or 1, $\mathcal{P}(c) \stackrel{\texttt{def}}{=} \{\epsilon\}$, where ϵ is the empty string.
- If $c = f(c_1, c_2)$ where $f \in \{\land, \oplus\}$, then

$$\mathcal{P}(c) \stackrel{\text{def}}{=} \bigcup_{\ell(c_i) = \max_{1 \le j \le 2} \ell(c_j)} \{ ip \mid p \in \mathcal{P}(c_i) \}$$

A set of critical positions $\mathcal{CP}(c)$ consists of all prefixes of strings in $\mathcal{P}(c)$.

Example 1.2.1. Consider a circuit $c(v_1, v_2, v_3, v_4)$ defined as

 $v_1 \wedge (1 \oplus (v_4 \wedge (1 \oplus (v_2 \wedge v_3)))).$

The multiplicative depth $\ell(c)$ of circuit c is 3 because there are three consecutive AND operations performed on v_2 and v_3 . The set $\mathcal{P}(c)$ of critical paths in c is

$$\mathcal{P}(c) = \{2p \mid p \in \mathcal{P}(1 \oplus (v_4 \land (1 \oplus (v_2 \land v_3))))\} \\ = \{22p \mid p \in \mathcal{P}(v_4 \land (1 \oplus (v_2 \land v_3)))\} \\ = \{222p \mid p \in \mathcal{P}(1 \oplus (v_2 \land v_3))\} \\ = \{2222p \mid p \in \mathcal{P}(v_2 \land v_3)\} \\ = \{22221, 22222\}$$

The set CP(c) of critical positions is:

 $\{\epsilon, 2, 22, 222, 2222, 22221, 22222\}.$

Note that in order to decrease the overall multiplicative depth of a Boolean circuit, all critical paths of the circuit must be rewritten. The depth of a critical path can be reduced if we reduce the depth of a sub-circuit at a critical position.

1.2.3 Problem

Given a Boolean circuit $c \in \mathbb{C}$ whose input variables are x_1, \dots, x_n , we aim to find a semantically equivalent circuit $c' \in \mathbb{C}$ whose multiplicative depth is smaller than c. Formally, our goal is to find c' such that

 $\forall x_i. \ c(x_1, \cdots, x_n) \iff c'(x_1, \cdots, x_n), \ell(c) > \ell(c').$ (1.1)

In this dissertation, we propose to solve this problem by combining program synthesis, term rewriting, and equality saturation.

1.3 Search-based Optimization Method

To achieve this critical optimization for homomorphic computation circuits as much as possible, we combine three techniques: program synthesis, term rewriting and equality saturation. Fig. 1.4 illustrates our approach.

1.3.1 Program Synthesis

Our method first automatically learns equivalences of small circuits from a set of training circuits using the program synthesis technique and then uses the learned equivalences to optimize unseen circuits. To learn such equivalences, we partition each training circuit into multiple sub-parts and synthesize their equivalent counterparts of smaller depth. Most of these machine-found optimization patterns are what we can hardly imagine from human-devised optimization techniques. (Section 4.2)

Program synthesis automatically synthesizes a program that satisfies a given high-level specification. The standard formulation syntax-guided synthesis (SyGuS [2]) employs a context-free grammar to describe the space of possible programs (syntactic constraint) and a semantic specification in the form of a logical formula (semantic constraint). A program synthesizer explores candidate programs based on a given context-free-grammar until it finds a program that meets the given semantic constraint. It employs various searching algorithms such as top-down search [42], bottom-up search [3], and bi-directional search [41] algorithm. It also utilize other techniques such as probabilistic models [42] and abstract interpretation [62].

In this dissertation, we utilize a program synthesizer to optimize given boolean circuit. We provide a context-free grammar that constrains an upper bound of the multiplicative depth as a syntactic constraint, and the circuit equivalence condition as a semantic constraint. Figure 1.2 illustrates this scenario. Given these constraints, program synthesizer automatically finds a new circuit that is equivalent to input circuit and has the smaller multiplicative depth,



Figure 1.2: Optimizing synthesis for homomorphic evaluation circuit

which we can call it optimized version of given boolean circuit.

In practice, however, since the search space is enormous, applying above optimizing synthesis technique to large circuits in the real world is infeasible. Instead, we partition each training circuit into multiple sub-parts and synthesize their equivalent counterparts of smaller depth. Each of the found equivalences of small circuits can be utilized as rewrite rules for further optimization process.

1.3.2 Term Rewriting and Equality Saturation

Next, armored with these automatically learned equivalences as rewrite rules, we perform term rewriting on the input circuit to obtain a new circuit that has lower multiplicative depth. Term rewriting [6] has been the most popular approach for compiler optimizations [7, 15, 51]. According to the learned rewrite rules, we repeatedly rewrite sub-parts of the homomorphic evaluation circuit for equals that have lower multiplicative depths until we decrease the overall depth. Rather than syntactic matching, we generalize what have been learned from training circuits by giving flexibility to the rewriting procedure: our method is based on a limited version of the equational matching that takes commutativity into account rather than just the syntactic matching. Our rewriting method is proven to be sound and terminating.

Moreover, by the equality saturation technique [56, 65] that has been widely used in program optimization systems [67, 63, 47], we explore every possible alternative order of applying rewrite rules (i.e. we obtain backtracking effect).

First, given an input program and rewrite rules, we efficiently express all

equivalent programs as a form of program grammar using a data structure called E-graph [65] (saturation process).

E-graph structure represents a large set of expressions (programs). It is defined as a triple of a set of enodes, a set of eclasses, and a set of edges. Each enode contains a non-terminal operator (ex. +, /, \times) or terminal value (ex. 1, 2, a). Eclass is a set of enodes. Edge connects an enode to an eclass. Each enode represents a set of expressions that can be generated recursively by following its children eclasses (detailed generation algorithm is available in Section 3.4). Each eclass represents a set of all expressions that can be generated by enodes inside it.

In saturation process, by repeatedly applying the learned rewrite rules, we expand the E-graph so that it represents all equivalent programs. During this process, expanded E-graph always maintain the invariance that all expressions generated by enodes in the same eclass must be semantically equivalent. Detailed expansion algorithm is available in Section 3.4.2.

We also obtain backtracking effect during the saturation process, since each expansion step always add information to E-graph, whereas the traditional term rewriting destructs the original form of rewritten expression. This allows the saturated E-graph to represent every possible result that can be varied by alternative order of applying rewrite rules.

Note that E-graph also can be interpreted as a program grammar, since the process that each eclass/enode generates a set of expressions are the same with how context-free-grammar generates a set of expressions from a nonterminal symbol/production rule. Each of the eclasses corresponds to a nonterminal symbol and each of enodes in that eclass corresponds to a production rule for the nonterminal symbol. For example, Figure 1.3 shows the semantic equivalence between E-graph and program grammar. Enode 1 and 2 can generate $(a \times 2)/2$ and $(2/2) \times a$, which also can be generated by first and second production rule of S_1 respectively.



Figure 1.3: Semantic equivalence between E-graph and context-free-grammar.

In this context, saturation process is a process of constructing a program grammar that can generate all programs equivalent to the input program.

Next, from the saturated E-graph, we extract the optimal expression according to a given cost function (extraction process). If the cost function is local (the cost of a node is computable only with the costs of its children nodes), it is well known that the least-cost circuit for that cost model can be easily extracted from the E-graph [65].

In this dissertation, given an input boolean circuit and the learned rewrite rules, we start with the initial E-graph that can generate the input circuit only. Then we expand the E-graph by applying the learned rewrite rules (saturation process). After saturation, since the multiplicative depth of the circuit is a local cost function, circuits with the lowest multiplicative depth can be easily extracted.

1.4 Contributions

We implement our method atop CINGULATA [22], a widely-used FHE compiler and evaluate our method on 25 FHE applications from diverse domains (statistical analysis, sorting, medical diagnosis, low-level algorithms, etc). We learn rewrite rules from a set of CINGULATA-generated Boolean circuits and apply the rules into other circuits.² On average, our method generates Boolean circuits

 $^{^{2}}$ Although the dissertation targets Boolean circuits, the method can also be directly applied to arithmetic circuits.



Figure 1.4: Overview of the system.

that can be homomorphically evaluated 1.08x - 3.17x faster (with the geometric mean of 1.56x) than the state-of-the-art method [15].

- A novel general method for optimizing homomorphic evaluation circuits: we first learn rewrite rules from a set of training Boolean circuits using program synthesis and then perform term-rewriting with the equational matching for generalized versions of the learned rewrite rules on a given new circuit. The soundness property of this rewriting system are formally proven. We combine equality saturation [56] with the existing term rewriting system (Section 3.4) to obtain a backtracking effect. This saturation-based term rewriting system outperforms our previous approach [40].
- Confirming the method's effectiveness in a realistic setting : the method outperforms existing automatic FHE optimization techniques, and even shows sizeable optimizations for domain-specific manually optimized results.

Chapter 2

Informal Description

In this section, we illustrate our approach with examples. Our approach consists of offline and online phases (Figure 1.4).

Offline Learning via Program Synthesis

In the offline phase, we use program synthesis to learn a set of rewrite rules from training circuits. Suppose we have the circuit c in Example 1.2.1 in the training set:

$$c \stackrel{\text{def}}{=} v_1 \wedge (\neg (v_4 \wedge (\neg (v_2 \wedge v_3)))).$$

The depth of this circuit is 3 and we would like to find a semantically-equivalent circuit c' with a smaller depth (i.e. $\ell(c') \leq 2$). To do so, we formulate the task as an instance of the syntax-guided synthesis (SyGuS) problem [?]. The formulation comprises a syntactic specification, in the form of a context-free grammar that constrains the space of possible programs, and a semantic specification, in the form of a logical formula that defines a correctness condition. The syntactic

specification for c' is the grammar:

$$S \rightarrow d_3$$

$$d_3 \rightarrow d_2 \wedge d_2 \mid d_3 \oplus d_3 \mid d_2$$

$$d_2 \rightarrow d_1 \wedge d_1 \mid d_2 \oplus d_2 \mid d_1$$

$$d_1 \rightarrow d_0 \wedge d_0 \mid d_1 \oplus d_1 \mid d_0$$

$$d_0 \rightarrow 0 \mid 1 \mid c_1 \mid c_2 \mid c_3 \mid c_4 \mid c_5$$

where S denotes the start symbol, and each non-terminal symbol d_i denotes circuits of multiplicative depth $\leq i$. The semantic specification for c' is given as a logical formula:

$$\forall v_1, v_2, v_3, v_4. \ c(v_1, v_2, v_3, v_4) \iff c'(v_1, v_2, v_3, v_4)$$

which enforces c' to be semantically equivalent to c. Given this SyGuS formulation, an off-the-shelf program synthesizer (e.g. EUSOLVER [3], DUET [41]) is able to find the following circuit c':

$$c' \stackrel{\text{def}}{=} ((\neg (v_3 \land v_2)) \land (v_1 \land v_4)) \oplus v_1$$

which has multiplicative depth 2.

Once we obtain a pair (c, c') of original and optimized circuits, we simplify c and c' by replacing sub-circuits that are equivalent modulo commutativity with a new fresh variable. In this example, $\neg(v_2 \land v_3)$ in c and $\neg(v_3 \land v_2)$ in c' are equivalent modulo commutativity and therefore we replace them by a new variable x, which simplifies c and c' into $v_1 \land (\neg(v_4 \land x))$ and $(x \land (v_1 \land v_4)) \oplus v_1$, respectively. Note that the simplified circuits are still semantically equivalent. We replace sub-circuits with a variable after we check for equivalence using a SAT solver.

The purpose of this simplification step is to generalize the knowledge and maximize the possibility of applying the rewrite rule for optimization in the online phase. However, care is needed not to over-generalize and destroy the syntactic structures of the circuits. For example, if we aim to replace all semantically equivalent sub-circuits with a new fresh variable, we would obtain $x \iff x$, which is useless.

In summary, the offline learning phase produces the following rewrite rule:

$$v_1 \wedge (\neg (v_4 \wedge x)) \to (x \wedge (v_1 \wedge v_4)) \oplus v_1.$$

$$(2.1)$$

Online Optimization via Term Rewriting

In the online phase, we use the learned rewrite rule to optimize unseen circuits. Suppose we want to optimize the following circuit whose multiplicative depth is 4:

$$((v_5 \wedge v_6) \wedge (\neg((v_7 \wedge v_8) \wedge (\neg((v_8 \wedge v_9) \wedge (v_9 \wedge v_7)))))).$$
(2.2)

To optimize the circuit, we first compare it with the left-hand side of the learned rewrite rule (i.e. $v_1 \wedge (\neg(v_4 \wedge x)))$, and find a substitution σ that makes the two circuits equivalent. For example, our matching algorithm in Section 3 is able to find the following substitution:

$$\sigma = \left\{ \begin{array}{ccc} v_1 & \mapsto & v_5 \wedge v_6 \\ v_4 & \mapsto & v_7 \wedge v_8 \\ x & \mapsto & (\neg (v_8 \wedge v_9) \wedge (v_9 \wedge v_7)) \end{array} \right\}$$

Note that $\sigma(v_1 \wedge (\neg(v_4 \wedge x)))$ is equivalent to the circuit in (2.2). Next, we apply the substitution to the right-hand side of the rewrite rule, obtaining the following optimized circuit:

$$(\neg((v_8 \land v_9) \land (v_9 \land v_7)) \land ((v_5 \land v_6) \land (v_7 \land v_8))) \oplus (v_5 \land v_6).$$

whose multiplicative depth is 3. In our approach, the resulting circuit is guaranteed to be semantically equivalent to the original one in (2.2).

Scaling via Divide-and-Conquer

As described from the above, we obtain rewrite rules from a small circuit and apply it into a new small circuit. In practice, however, real circuits are much larger, and the aforementioned method using the SyGuS formulation is not directly applicable. Even state-of-the-art SyGuS tools can only handle small circuits because the search space for synthesis grows exponentially with the maximum depth and number of input variables.

To address this scalability issue, we apply our approach in a divide-andconquer manner; we divide a circuit into pieces, find a replacement for each piece, and finally compose them to form a final circuit. For example, consider the circuit c_{ex} of depth 5, which is depicted in Figure 2.1(a) (the critical path is highlighted in red):

$$c_{ex} \stackrel{\text{def}}{=} ((((a \wedge b) \wedge c) \wedge d) \wedge e) \wedge f.$$
(2.3)

We can divide the circuit into two pieces r_1 and r_2 through which a critical path passes. By introducing two auxiliary variables, c_{ex} can be rewritten as r_2 where

$$r_2 \stackrel{\text{def}}{=} (r_1 \wedge e) \wedge f, \quad r_1 \stackrel{\text{def}}{=} ((a \wedge b) \wedge c) \wedge d.$$

We separately reduce the depths of r_1 and r_2 in order. We first find a replacement for r_1 . We can replace r_1 of depth 3 by the following:

$$r_1' \stackrel{\texttt{def}}{=} (a \wedge b) \wedge (c \wedge d)$$

which has depth 2 and the same semantics as r_1 . Next, we find a replacement for r_2 . We treat r_1 in the definition of r_2 as a special variable that has its own depth 2. Considering the depth of r_1 , we replace r_2 of depth 4 by

$$r_2' \stackrel{\mathrm{def}}{=} r_1' \wedge (e \wedge f)$$

that has depth 3 and the same semantics as r_2 . Combining r'_1 and r'_2 produces the final circuit of depth 3. We use this divide-and-conquer strategy in both of our offline learning and online rewriting phases.

Backtracking via Equality Saturation

In rewriting input circuits, we may miss the global optimality because there can be multiple targets to optimize and multiple rewrite rules to apply to each



Figure 2.1: (a) The circuit c_{ex} of depth 5. (b) A circuit that has depth 3 and the same semantics as c_{ex} .

target. In this situation, optimization results can be varied depending on which target is rewritten first, or which rewrite rule is applied first.

For example, suppose that we want to optimize circuit $c_0 = ((x_1 \land x_2) \land (x_2 \oplus x_3)) \land x_3$ using the following learned rewrite rules.

 $\begin{aligned} \text{rule} (1) : & ((v_1 \wedge v_2) \wedge v_3) \wedge v_4 & \to & ((v_1 \wedge v_2) \wedge v_4) \wedge ((v_2 \oplus v_4) \oplus v_3) \\ \text{rule} (2) : & ((v_1 \wedge v_2) \wedge v_3) \wedge v_4 & \to & (v_1 \wedge v_2) \wedge (v_3 \wedge v_4) \\ \text{rule} (3) : & (v_1 \oplus v_1) & \to & 0 \\ \text{rule} (4) : & (v_1 \wedge 0) & \to & 0 \end{aligned}$

We can apply both rule (1) and rule (2) using substitution $\sigma = \{v_1 \mapsto x_1, v_2 \mapsto x_2, v_3 \mapsto (x_2 \oplus x_3), v_4 \mapsto x_3\}$. If we apply rule (1) first, we can optimize $c_0 \to c_1 = ((x_1 \land x_2) \land x_3) \land ((x_2 \oplus x_3) \oplus (x_2 \oplus x_3))$. In this case, we can subsequently apply rule (3) and rule (4) and optimize $c_0 \to^* 0$. Otherwise, we can optimize $c_0 \to c_2 = (x_1 \land x_2) \land ((x_2 \oplus x_3) \land x_3)$. In this case, we can no longer apply the rewrite rules and miss the opportunity of further optimizing c_0 .

To address this rewrite order issue, we use the equality saturation technique [56] that obtains a fully backtracking effect within a given time limit. By the technique we try to explore every possible rewriting sequences (e.g. we try to explore both $c_0 \rightarrow^* 0$ and $c_0 \rightarrow c_2$).

Equality saturation consists of two processes: saturation process and extraction process. First, in the saturation process, we express all possible result circuits as a form of program grammar using a data structure named E-graph [65] (e.g. we construct program grammar that can generate circuits c_0 , c_1 , c_2 and 0). In the extraction process, we extract an optimal circuit that has the lowest multiplicative depth from a saturated E-graph (e.g. we extract circuit 0 which has the lowest multiplicative depth). Details of this equality saturation process are in Section. 3.4.

Chapter 3

Algorithm

We first review (Section 3.1) key definitions and results borrowed from Baader and Nipkow [6] that will be used in the rest of the dissertation. Then we present the offline learning phase (Section 3.2), online optimization phase (Section 3.3) based on term rewriting and backtracking system(Section 3.4) via equality saturation.

3.1 Preliminaries

Term

A signature Σ is a set of function symbols, where each $f \in \Sigma$ is associated with a non-negative integer n, the arity of f (denoted arity(f)). For $n \geq 0$, we denote the set of all n-ary elements Σ by $\Sigma^{(n)}$. Function symbols of 0-arity are called constants. Let X be a set of variables. The set $T_{\Sigma,X}$ of all Σ -terms over X is inductively defined; $X \subseteq T_{\Sigma,X}$ and $\forall n \geq 0, f \in \Sigma^{(n)}$. $t_1, \dots, t_n \in$ $T_{\Sigma,X}$. $f(t_1, \dots, t_n) \in T_{\Sigma,X}$. We will denote Var(s) for $s \in T_{\Sigma,X}$ as a set of variables in term s. Note the set \mathbb{C} of circuits consists of terms over $\Sigma =$ $\{\wedge, \oplus, 0, 1\}$.

Position

The set of positions of term s is a set Pos(s) of strings over the alphabet of positive integers, which is inductively defined as follows:

- If $s = x \in X$, $Pos(s) \stackrel{\text{def}}{=} \{\epsilon\}$.
- If $s = f(s_1, \cdots, s_n)$, then $Pos(s) \stackrel{\texttt{def}}{=} \{\epsilon\} \cup \bigcup_{i=1}^n \{ip \mid p \in Pos(s_i)\}.$

The position ϵ is called the root position of term s. The size |s| of term s is the cardinality of Pos(s). For $p \in Pos(s)$, the subterm of s at position p, denoted by $s \mid_p$, is defined by induction on the length of p: (i) $s \mid_{\epsilon} \stackrel{\text{def}}{=} s$ and (ii) $f(s_1, \dots, s_n) \mid_{iq} \stackrel{\text{def}}{=} s_i \mid_q$. For $p \in Pos(s)$, we denote by $s[p \leftarrow t]$ the term that is obtained from s by replacing the subterm at position p by t. Formally,

- $s[\epsilon \leftarrow t] \stackrel{\texttt{def}}{=} t$
- $f(s_1, \cdots, s_n)[iq \leftarrow t] \stackrel{\text{def}}{=} f(s_1, \cdots, s_i[q \leftarrow s], \cdots, s_n).$

Substitution

A $T_{\Sigma,X}$ -substitution is a function $X \to T_{\Sigma,X}$. The set of all $T_{\Sigma,X}$ -substitutions is denoted by $Sub(T_{\Sigma,X})$. Any $T_{\Sigma,X}$ -substitution σ can be extended to a mapping $\hat{\sigma}: T_{\Sigma,X} \to T_{\Sigma,X}$ as follows: for $x \in X$, $\hat{\sigma}(x) \stackrel{\text{def}}{=} \sigma(x)$ and for any non-variable term $s = f(s_1, \dots, s_n)$, $\hat{\sigma}(s) \stackrel{\text{def}}{=} f(\hat{\sigma}(s_1), \dots, \hat{\sigma}(s_n))$. With a slight of abuse of notation, we denote $\hat{\sigma}$ as just σ .

Term Rewriting

A Σ -identity (or simply identity) is a pair $\langle s, t \rangle \in T_{\Sigma,X} \times T_{\Sigma,X}$. Identities will be written as $s \approx t$. A term rewrite rule is an identity $\langle l, r \rangle$, written $l \to r$, such that $l \notin X$ and $Var(r) \subseteq Var(l)$. A term rewriting system $\langle \Sigma, E \rangle$ consists of a set Σ of function symbols and a set E of term rewrite rules over $T_{\Sigma,X}$. We will often identify such a system with its rule set E, leaving Σ implicit. The rewrite relation \rightarrow_E on $T_{\Sigma,X}$ induced by a term rewriting system E is defined as follows:

$$s \to_E t \iff \exists l \to r \in E, p \in Pos(s), \sigma \in Sub(T_{\Sigma,X}).$$

 $s \mid_p = \sigma(l), t = s[p \leftarrow \sigma(r)]$

Example 3.1.1. Let $E = \{x \land (y \land z) \approx (x \land y) \land z, 1 \land x \approx x, x \land y \approx y \land x\}$. Then, $1 \land (a \land 1) \rightarrow_E (1 \land a) \land 1 \rightarrow_E a \land 1 \rightarrow_E 1 \land a \rightarrow_E a$.

Equational Theory

Let \leftrightarrow_E^* denote the reflexive-transitive-symmetric closure of \rightarrow_E . The identity $s \approx t$ is a semantic consequence of E (denoted $E \models s \approx t$) iff $s \leftrightarrow_E^* t$. And the relation $\approx_E \stackrel{\text{def}}{=} \{ \langle s, t \rangle \in T_{\Sigma,X} \times T_{\Sigma,X} \mid E \models s \approx t \}$ is called the equational theory induced by E.

Example 3.1.2. Let $C = \{x \land y \approx y \land x, x \oplus y \approx y \oplus x\}$. Then, $x \land (y \oplus z) \rightarrow_{\mathcal{C}} (y \oplus z) \land x \rightarrow_{\mathcal{C}} (z \oplus y) \land x$. The theory of commutativity for circuits is $\approx_{\mathcal{C}} \stackrel{\text{def}}{=} \{\langle s, t \rangle \in \mathbb{C} \times \mathbb{C} \mid \mathcal{C} \models s \approx t\}$ (e.g. $x \land (y \oplus z) \approx_{\mathcal{C}} (z \oplus y) \land x$).

Example 3.1.3. Boolean ring theory is $\approx_{\mathcal{R}} \stackrel{\text{def}}{=} \{ \langle s, t \rangle \in \mathbb{C} \times \mathbb{C} \mid \mathcal{R} \models s \approx t \}$ where

$$\mathcal{R} = \begin{cases} x \oplus y \approx y \oplus x, & x \wedge y \approx y \wedge x, \\ (x \oplus y) \oplus z \approx x \oplus (y \oplus z), \\ (x \wedge y) \wedge z \approx x \wedge (y \wedge z), \\ x \oplus x \approx 0, & x \wedge x \approx x, \\ 0 \oplus x \approx x, & 0 \wedge x \approx 0, \\ x \wedge (y \oplus z) \approx (x \wedge y) \oplus (x \wedge z), \\ 1 \wedge x \approx x \end{cases}$$

Boolean ring theory formalizes digital circuits. For any two circuits c_1, c_2 ,

 $c_1 \leftrightarrow^*_{\mathcal{R}} c_2$ means they are semantically equivalent [6].

Example 3.1.4. The original circuit c and its optimized version c' in Section 2 are semantically equivalent because

$$\begin{aligned} c &= v_1 \wedge (1 \oplus (v_4 \wedge (\neg (v_2 \wedge v_3)))) \\ &\to_{\mathcal{R}} (v_1 \wedge 1) \oplus (v_1 \wedge (v_4 \wedge (\neg (v_2 \wedge v_3)))) \\ &\to_{\mathcal{R}} v_1 \oplus (v_1 \wedge (v_4 \wedge (\neg (v_2 \wedge v_3)))) \\ &\to_{\mathcal{R}} v_1 \oplus ((v_1 \wedge v_4) \wedge (\neg (v_3 \wedge v_2))) \\ &\to_{\mathcal{R}} ((v_1 \wedge v_4) \wedge (\neg (v_3 \wedge v_2))) \oplus v_1 \\ &\to_{\mathcal{R}} ((\neg (v_3 \wedge v_2)) \wedge (v_1 \wedge v_4)) \oplus v_1 = c' \end{aligned} \qquad \begin{aligned} x \wedge y \approx y \wedge x \\ x \wedge y \approx y \wedge x \end{aligned}$$

E-Matching

A substitution σ is a *E*-matcher of two terms *s* and *t* if $\sigma(s) \approx_E t$. Given two terms *s* and *t*, a *E*-matching algorithm computes { $\sigma \in Sub(T_{\Sigma,X}) \mid \sigma(s) \approx_E t$ }. **Example 3.1.5.** Given two terms $s = x \wedge y$ and $t = (a \wedge b) \wedge (b \wedge a)$, *C*-matching algorithm returns two substitutions which are { $x \mapsto a \wedge b, y \mapsto b \wedge a$ } and { $x \mapsto b \wedge a, y \mapsto a \wedge b$ }.

3.2 Learning Rewrite Rules

In this section, we describe how to learn rewrite rules using the divide-andconquer approach described in Section 2. The method is inspired by the prior work [29], which uses syntax-guided synthesis to automatically transform a circuit into an equivalent and provably secure one.

3.2.1 The Overall Algorithm

The pseudocode is shown in Algo. 1. Here, c denotes an original training circuit, θ denotes a threshold value for termination condition, n is an user-provided predefined limit for region selection. The algorithm generates an optimized circuit c', and returns a set E of rewrite rules collected in the process of optimization.

Our algorithm repeatedly identifies a circuit region and synthesizes a replacement. To identify a circuit region, we randomly choose a critical path and traverse the path from input-to-output. If the left and right children at a position have different depths, we include both gates in fan-in and recurse on the child of deeper depth. We repeat this process until the region size reaches a predefined limit. Once we successfully synthesize a replacement, we can decrease the overall depth if a unique critical path passes through the region. Otherwise, we decrease the number of parallel critical paths by one.

Our method first initializes c' to be the original circuit c, E to be the empty set and the worklist w to be a set of critical positions, respectively (lines 1–3). The loop (lines 4 – 13) repeats the process of selecting a region and synthesizing a replacement. First, a critical position *pos* is chosen in the input-to-output

Algorithm 1 Synthesis-based Rule Learning

Input: *c*: input boolean circuit **Input:** θ : threshold for termination condition **Input:** *n*: predefined size limit for chosen regions **Output:** *E*: a set of rewrite rules 1: $c' \leftarrow c$ 2: $E \leftarrow \emptyset$ 3: $w \leftarrow \mathcal{CP}(c')$ 4: while $w \neq \emptyset$ and $\frac{|c'|}{|c|} < \theta$ do remove a pos from w5: $\langle r, \sigma \rangle \leftarrow \text{GetRegion}(c' \mid_{pos}, n)$ 6: 7: $r' \leftarrow \text{Synthesize}(r, \ell(r) - 1, \sigma)$ if $r' \neq \bot$ then 8: 9: $E \leftarrow E \cup \{\text{Normalize}(r \to r')\}$ $c' \leftarrow c'[pos \leftarrow \sigma(r')]$ 10: $w \leftarrow \mathcal{CP}(c')$ 11: end if 12:13: end while 14: return E

order (line 5). Given a subcircuit at *pos*, the GetRegion procedure is invoked to obtain a circuit region r such that $|r| \leq n$ (line 6). The GetRegion procedure substitutes some subterms of a given circuit with fresh variables and returns the result along with the substitution. Section 3.2.2 will detail more on this procedure. Next, we invoke a SyGuS solver to synthesize a replacement for r(line 7). If a solution is found (line 8), we obtain a term rewrite rule $r \to r'$. We generalize the rule by invoking the Normalize procedure (Section 3.2.4), and add it into the set E (line 9). The old region r is replaced with the new region r' (line 10). Because the replacement step may change the overall structure of the current circuit, we recompute critical positions and update the worklist (line 11). This process is repeated as long as there is room for improvement, and the ratio between the sizes of c' and c does not exceed the threshold value θ (line 4). The ratio between the circuit sizes is considered because the depth reduction may not be beneficial if a new circuit c' additionally performs a huge number of AND/XOR operations. Although the multiplicative depth is the dominating factor for homomorphic evaluation performance, the number of operations can

also have a non-trivial impact if it is enormous. The threshold value varies depending on the underlying FHE schemes. In our evaluation, we set θ to be 3. The algorithm eventually returns the set E of rewrite rules (line 14), which include all the transformations occurred while optimizing c into c'.

3.2.2 Region Selection

The GetRegion procedure for the region selection is shown in Algo. 2. The region selection method is a heuristic based on our observation that replacing long and narrow regions covering critical paths often leads to significant optimization effects. If the given region size n is 1 or the given circuit region is a variable of a constant (i.e., |c| = 1) (line 2), we just represent the given circuit region as a fresh variable and return it along with the corresponding substitution (line 3). Otherwise (i.e., |c| > 1), we first let c_1 and c_2 be the left and right child of c, resp. (lines 5 – 6). If the depth of c_1 (c_2 , resp.) is deeper than the other (line 7 (line 10, resp.)), we keep extending the region in c_1 (c_2 , resp.) (line 8 (line 11, resp.)), and substitute c_2 (c_1 , resp.) with a fresh variable (line 9 (line 12, resp.)).

Algorithm 2 GetRegion

```
Input: c: input boolean circuit region
Input: n: predefined size limit for regions
Output: r: a circuit region
Output: \sigma: a substitution from variables to circuits
 1: x \leftarrowa new fresh variable
 2: if n = 1 or |c| = 1 then
 3:
           return \langle x, \{x \mapsto c\} \rangle
 4: end if
 5: c_1 \leftarrow c \mid_1
 6: c_2 \leftarrow c \mid_2
 7: if \ell(c_1) > \ell(c_2) then
           \langle r', \sigma \rangle \leftarrow \text{GetRegion}(c_1, n-1)
 8:
           return \langle c[1 \leftarrow r', 2 \leftarrow x], \sigma \cup \{x \mapsto c_2\} \rangle
 9:
10: else
           \langle r', \sigma \rangle \leftarrow \text{GetRegion}(c_2, n-1)
11:
           return \langle c[1 \leftarrow x, 2 \leftarrow r'], \sigma \cup \{x \mapsto c_1\} \rangle
12:
13: end if
```

Example 3.2.1. Consider the circuit c_{ex} in (2.3). GetRegion(c_{ex} , 5) returns $\langle (r_1 \wedge e) \wedge f, \{r_1 \mapsto ((a \wedge b) \wedge c) \wedge d\} \rangle$ (see Fig. 2.1(a)).
3.2.3 Synthesizing Replacement

Given a circuit region r, an upper bound n of desired multiplicative depths, and a substitution σ , the function Synthesize returns a new semantically equivalent region r' of depth $\leq n$.

For $1 \leq i \leq n$, let x^i denote one of the variables such that $\ell(\sigma(x^i)) = i$. We can formulate a SyGuS instance as follows. The syntactic specification for r' is

$$S \rightarrow d_{n}$$

$$d_{n} \rightarrow d_{n-1} \wedge d_{n-1} \mid d_{n} \oplus d_{n} \mid d_{n-1} \mid x^{n}$$

$$d_{n-1} \rightarrow d_{n-2} \wedge d_{n-2} \mid d_{n-1} \oplus d_{n-1} \mid d_{n-2} \mid x^{n-1}$$

$$\vdots$$

$$d_{0} \rightarrow 0 \mid 1 \mid x^{0}$$

where S denotes the start symbol and each d_i represents circuits of multiplicative depth $\leq i$. The semantics specification for r' enforces the equivalence of r and r':

$$\forall x^0, \cdots, x^{n-1}. \ r(x^0, \cdots, x^{n-1}) \iff r'(x^0, \cdots, x^{n-1}).$$

When Synthesize fails to find a solution, it returns \perp .

Example 3.2.2. After selecting the region r_2 as in Example 3.2.1, we find a replacement for r_2 using the following formulation, hoping to reduce the depth from 5 to 4. The syntactic specification for r'_2 is

$$\begin{array}{rcl} S & \to & d_4 \\ d_4 & \to & d_3 \wedge d_3 \mid d_4 \oplus d_4 \mid d_3 \\ d_3 & \to & d_2 \wedge d_2 \mid d_3 \oplus d_3 \mid d_2 \mid r_1 \\ d_2 & \to & d_1 \wedge d_1 \mid d_2 \oplus d_2 \mid d_1 \\ d_1 & \to & d_0 \wedge d_0 \mid d_1 \oplus d_1 \mid d_0 \\ d_0 & \to & 0 \mid 1 \mid e \mid f \end{array}$$

and the semantics specification is the semantic equivalence with r_2 . Note that r_1 is producible from d_3 because its depth is 3. Given this problem, a SyGuS solver (e.g. EUSOLVER [3], DUET [41]) finds the solution $r_1 \wedge (e \wedge f)$ which has depth 4.

3.2.4 Collecting and Simplifying Rewrite Rules

When we obtain a rewrite rule $l \to r$, we simplify it by invoking the Normalize procedure (line 9 in Algo. 1). We normalize each rewrite rule $l \to r \in E$ as follows:

- Let $\mathcal{S} = \{(l \mid_{p_l}, r \mid_{p_r}) \mid p_l \in Pos(l), p_r \in Pos(r), l \mid_{p_l} \approx_{\mathcal{C}} r \mid_{p_r}\}.$
- For each $(l \mid_{p_l}, r \mid_{p_r}) \in S$, we transform $l \to r$ into $l' \to r'$ where $l' = \sigma(l)$, $r' = \sigma(r), \ \sigma = \{\forall l \mid_{p_i} \mapsto x, \forall r \mid_{p_j} \mapsto x\} \ s.t. \ l \mid_{p_i} \approx_{\mathcal{C}} l \mid_{p_l}, r \mid_{p_j} \approx_{\mathcal{C}} r \mid_{p_r},$ and x is a fresh variable. We transform the rule only if l' is semantically
 equivalent to r'.

We consider a term rewrite rule that cannot be further simplified by this procedure normalized modulo commutativity.

Example 3.2.3. Suppose we want to normalize a rewrite rule:

$$\underbrace{((a \land b) \land (b \land a)) \land (a \land b)}_{l} \to \underbrace{(b \land a)}_{r}.$$

Note that $l \mid_{11} = l \mid_{2} = (a \land b) \approx_{\mathcal{C}} r \mid_{1} = l \mid_{12} = (b \land a)$. If we replace the subterms $l \mid_{11}, l \mid_{12}, l \mid_{2}, and r \mid_{1}$ with a fresh variable x, we obtain normalized rewrite rule $(x \land x) \land x \to x$, which is semantics preserving.

Example 3.2.4. Suppose we want to normalize a rewrite rule:

$$\underbrace{(a \wedge b) \wedge a}_{l} \to \underbrace{(b \wedge a) \wedge b}_{r}.$$

Note that $l \mid_1 = (a \land b) \approx_{\mathcal{C}} r \mid_1 = (b \land a)$. If we replace the subterms $l \mid_1$ and $r \mid_1$ with a fresh variable x, we would obtain $x \land a \to x \land b$, which is undesirably semantics-changing. In this case, we do not replace the subterms.

3.3 Optimization without Backtracking

Next, we describe our algorithm that uses the set E of (normalized) learned rewrite rules to optimize unseen circuits.

3.3.1 Our Term Rewriting System

Our term rewriting system is based on the following relation $\rightarrow_{E,\ell}$ induced by E (learned rewrite rules) and ℓ (the function computing the multiplicative depth).

$$s \to_{E,\ell} t \iff \exists l \to r \in E, p \in \mathcal{CP}(s), \sigma \in Sub(\mathbb{C}).$$
$$s \mid_{p \approx_{\mathcal{C}}} \sigma(l), \ell(\sigma(l)) > \ell(\sigma(r)), t = s[p \leftarrow \sigma(r)]$$

Because our primary goal is to reduce the overall multiplicative depth, the above rewrite relation differs from the ordinary relation in Section 3.1 in three aspects.

First, we rewrite critical paths by considering only critical positions $C\mathcal{P}(s)$ of a given circuit s. Rewriting non-critical paths are not of our interest.

Second, we admit a rewrite step only if it decreases the depth of a critical path. This condition is reflected in $\ell(\sigma(l)) > \ell(\sigma(r))$.

Lastly, we perform rewriting modulo commutativity to provide flexibility to the rewriting procedure. This is for maximizing the possibility of applying the learned rewrite rules for optimization. Instead of syntactically matching a left-hand side of a rule with a subterm as in the ordinary rewrite relation, each rewrite step requires C-matching, which is reflected in $s \mid_{p} \approx_{\mathcal{C}} \sigma(l)$. Here, a complication arises that there may be multiple C-matchers. In such a case, we choose the one that can reduce depth.

Example 3.3.1. Recall the rewrite rule (2.1) in Section 2

$$\underbrace{v_1 \wedge (\neg (v_4 \wedge x))}_l \to \underbrace{(x \wedge (v_1 \wedge v_4)) \oplus v_1}_r.$$

and the target circuit (2.2) of depth 4

$$(v_5 \wedge v_6) \wedge (\neg((v_7 \wedge v_8) \wedge (\neg((v_8 \wedge v_9) \wedge (v_9 \wedge v_7)))))).$$

There are two substitutions that make l match with the target circuit: $\sigma_1 = \{v_1 \mapsto v_5 \land v_6, v_4 \mapsto v_7 \land v_8, x \mapsto (\neg (v_8 \land v_9) \land (v_9 \land v_7))\}$ and $\sigma_2 = \{v_1 \mapsto v_5 \land v_6, v_4 \mapsto (\neg (v_8 \land v_9) \land (v_9 \land v_7)), x \mapsto v_7 \land v_8\}$. Applying the substitutions into r gives us two candidates for the replacement, which are

$$\sigma_1(r) = (\neg((v_8 \land v_9) \land (v_9 \land v_7)) \land ((v_5 \land v_6) \land (v_7 \land v_8))) \oplus (v_5 \land v_6),$$

$$\sigma_2(r) = ((v_7 \land v_8) \land ((v_5 \land v_6) \land (\neg(v_8 \land v_9) \land (v_9 \land v_7)))) \oplus (v_5 \land v_6).$$

Note that $\sigma_1(r)$ has depth 3 whereas $\sigma_2(r)$ has depth 4. Because only σ_1 can reduce the depth, we choose σ_1 .

The following theorem ensures that our term rewriting system is semanticspreserving and terminating.

Theorem 3.3.2 (Soundness). $\forall c, c' \in \mathbb{C}$. $c \to_{E,\ell} c' \Rightarrow c \approx_{\mathcal{R}} c'$.

Theorem 3.3.3 (Termination). $\rightarrow_{E,\ell}$ is a terminating relation.

Before describing the proofs of Theorem 3.3.2 and 3.3.3, we begin with preliminary concepts from Baader and Nipkow [6].

Definition 3.3.4. Let \equiv be a binary relation on $T_{\Sigma,X}$.

- 1. The relation \equiv is closed under substitutions iff $s \equiv t$ implies $\sigma(s) \equiv \sigma(t)$ for all $s, t \in T_{\Sigma,X}$ and substitutions σ .
- 2. The relation \equiv is compatible with Σ -context iff $s \equiv s'$ implies $t[p \leftarrow s] \equiv t[p \leftarrow s']$ for all $t \in T_{\Sigma,X}$ and positions $p \in Pos(t)$.

Lemma 3.3.5. $\approx_{\mathcal{R}}$ is closed under substitutions and compatible with Σ -context.

Proof. By Theorem 3.1.12 in Baader and Nipkow [6], for any set E of Σ -identities, the relation \leftrightarrow_E^* is closed under substitutions. By Lemma 3.1.11 in Baader and Nipkow [6], the relation \leftrightarrow_E^* is also compatible with Σ -context. Setting $E = \mathcal{R}$ finishes the proof.

Lemma 3.3.6. For all $s, t \in T_{\Sigma,X}$,

$$s \approx_{\mathcal{C}} t \implies s \approx_{\mathcal{R}} t.$$

Proof. Straightforward from the fact that $\mathcal{C} \subseteq \mathcal{R}$.

Now we are ready to prove Theorem 3.3.2.

Proof. By the definition of $s \to_E t$ and E, there exist $(l \approx_{\mathcal{R}} r) \in E, p \in Pos(s), \sigma$. such that $s \mid_{p} \approx_C \sigma(l), t = s[p \leftarrow \sigma(r)]$.

$$\begin{split} s \mid_{p} \approx_{\mathcal{R}} \sigma(l) & (\text{By Lemma 3.3.6 and } s \mid_{p} \approx_{C} \sigma(l)) \\ s[p \leftarrow s \mid_{p}] \approx_{\mathcal{R}} s[p \leftarrow \sigma(l)] & (\text{By Lemma 3.3.5}) \\ \sigma(l) \approx_{\mathcal{R}} \sigma(r) & (\text{By Lemma 3.3.5 and } l \approx_{\mathcal{R}} r) \\ s[p \leftarrow \sigma(l)] \approx_{\mathcal{R}} s[p \leftarrow \sigma(r)] & (\text{By Lemma 3.3.5}) \end{split}$$

Therefore, $s \approx_{\mathcal{R}} t$.

Next, before describing the proof of termination, we define the following strict orders between terms.

Definition 3.3.7. For all $s, t \in T_{\Sigma,X}$,

$$s \succ t \iff (\ell(s) > \ell(t)) \lor (\ell(s) = \ell(t) \land |\mathcal{CP}(s)| > |\mathcal{CP}(t)|).$$

Recall that by replacing an old circuit region compassing a critical path with a new circuit region of smaller depth, we can either i) decrease the overall depth if the critical path is unique, or ii) decrease the number of parallel critical paths, which is reflected on the definition of \succ .

The following lemma tells us that the order \succ is compatible with Σ -context.

Lemma 3.3.8. Suppose we have $s, t_1, t_2 \in T_{\Sigma,X}$ such that $s \mid_p = t_1$ and $\ell(t_1) > \ell(t_2)$. Then,

$$\forall p \in \mathcal{CP}(s). \ s[p \leftarrow t_1] \succ s[p \leftarrow t_2].$$

Proof. • Case $p = \epsilon$: $s[p \leftarrow t_1] = t_1 \succ t_2 = s[p \leftarrow t_2]$.

• Case p = p'.1:

Suppose $s \mid_{p'} = \wedge(t_1, s_2)$ which makes $s \mid_p = t_1$. By the fact p = p'.1 and the definition of \mathcal{CP} , $\ell(t_1) \geq \ell(s_2)$. There are two cases.

Case (1) $\ell(t_1) > \ell(s_2)$:

$$\ell(\wedge(t_1, s_2)) = 1 + \ell(t_1)$$

$$\ell(\wedge(t_2, s_2)) = 1 + \max(\ell(t_2), \ell(s_2)) \qquad (By \text{ Def. of } \ell)$$

$$\ell(t_1) > \ell(t_2) \qquad (By \text{ the premise})$$

$$\ell(t_1) > \ell(s_2) \qquad (By \text{ the case assumption})$$

$$\therefore \ell(s[p \leftarrow t_1]) > \ell(s[p \leftarrow t_2])$$

Case (2) $\ell(t_1) = \ell(s_2)$:

$$\begin{split} \ell(\wedge(t_1, s_2)) &= 1 + \ell(s_2) \\ \ell(\wedge(t_2, s_2)) &= 1 + \ell(s_2) \\ |\mathcal{CP}(\wedge(t_1, s_2))| &= |\mathcal{CP}(t_1) \uplus \mathcal{CP}(s_2)| \times 2 \\ |\mathcal{CP}(\wedge(t_2, s_2))| &= |\mathcal{CP}(s_2)| \times 2 \\ \mathcal{CP}(t_1) \neq \emptyset \\ |\mathcal{CP}(\wedge(t_1, s_2))| &> |\mathcal{CP}(\wedge(t_2, s_2))| \end{split}$$

$$(\ell(s_2) = \ell(t_1) > \ell(t_2)) \\ (By \text{ Def. of } \mathcal{CP}) \\ (By \text{ Def. of }$$

Note that $\mathcal{CP}(t_1) \neq \emptyset$ because otherwise, $\ell(t_1) = 0 > \ell(t_2) \ge 0$ which leads to a contradiction.

Therefore, $\ell(s[p \leftarrow t_1]) = \ell(s[p \leftarrow t_2]) \land |\mathcal{CP}(s[p \leftarrow t_1])| > |\mathcal{CP}(s[p \leftarrow t_2])|.$ The other case where $s \mid_{p'} = \oplus(t_1, s_2)$ can be proven similarly.

• Case p = p'.2: Similar to the above case.

Now we are ready to prove Theorem 3.3.3.

Proof. Straightforward from Lemma 3.3.8 and the fact that \succ is a strict order.

Intuitively, termination is enforced because every rewrite step decreases the depth of a critical path. If the rewritten critical path is unique, we reduce the overall multiplicative depth of the circuit. Otherwise, we reduce the number of parallel critical paths. Because every circuit has at least one critical path of non-negative depth, the rewriting procedure eventually terminates.

Using the rewrite relation $\rightarrow_{E,\ell}$, given a circuit c, we perform term rewriting on c to obtain an optimized circuit c' such that $c \stackrel{*}{\rightarrow}_{E,\ell} c'$. At each rewrite step, we randomly choose a critical path and traverse the path to find a target region to be replaced. The traversal order is randomly chosen between the input-to-output and output-to-input orders. Similarly to Algo. 1, we stop the rewriting procedure when there are so many additional AND/XOR gates in c'that the depth reduction may not be beneficial.

3.3.2 Optimizations

In practice, we apply the following optimization techniques to the rewriting procedure.

Prioritizing Large Rewrite Rules

In the case where multiple rewrite rules are applicable, we choose the largest rule. The size of a rule $l \rightarrow r$ is simply measured by |l|. This heuristic is based on our observation that large rules are applicable less often than small rules, but they expedite transformation by modifying a wider area.

Term Graph Rewriting

So far, we have presented our method as if circuits are represented as functional expressions for ease of presentation. In practice, we cannot directly implement this kind of conventional term rewriting based on strings or trees because of an efficiency issue. For example, term rewrite rules such as (2.1) containing some variable more often on its right-hand side than on its left-hand side can increase the size of a term by a non-constant amount. This problem can be overcome by creating several pointers to a subterm instead of copying it.

For efficiency, we conduct term graph rewriting [50] on circuits. Term graph rewriting is a model for computing with graphs representing functional expressions. Graphs allow sharing common subterms, which improves the efficiency of conventional term rewriting in space and time. Thus, we represent circuits as graphs and perform rewriting on the graphs by translating term rewrite rules into suitable graph transformation rules. Term graph rewriting is sound with respect to term rewriting in that every graph transformation step corresponds to a sequence of applications of term rewrite rules. The interested reader is referred to Plump [50] for more details about the soundness proof and the translation method.

Bounded C-matching

From a performance perspective, the main weakness of our rewriting system is that each rewrite step requires C-matching, which is known to be NPcomplete [39]. We limit the search space of C-matching algorithm by limiting the number of applications of commutativity rules (see below for details).

E-Matching Algorithm Solving a matching problem for two terms *s* and *t* is represented by $S = \{s \approx_E^2 t\}$. A conventional *E*-matching algorithm derives a set of equations in solved form:

$$\{x_1 \approx_E t_1, \cdots, x_n \approx_E t_n\}$$

where all x_i 's are pairwise distinct.

Matching System The symbol \perp or a pair P; S where

• *P* is a set of matching problems,

$\frac{\{s \doteq^? s\} \cup P; S}{P; S}$	[Trivial]
$\frac{\{f(s_1, s_2) \doteq^? f(t_1, t_2)\} \cup P; S}{\{s_1 \doteq^? t_1, s_2 \doteq^? t_2\} \cup P; S}$	[Decomposition]
$\frac{\{f(s_1, s_2) \doteq^? f(t_1, t_2)\} \cup P; S}{\{s_2 \doteq^? t_1, s_1 \doteq^? t_2\} \cup P; S}$	[C-Decomposition]
$\frac{\{f(s_1, s_2) \doteq^? g(t_1, t_2)\} \cup P; S}{\bot} f \neq g$	[Symbol Clash]
$\frac{\{f(s_1, s_2) \stackrel{=}{=}^? x\} \cup P; S}{\bot}$	[Symbol-Variable Clash]
$\frac{\{x \stackrel{i}{\stackrel{\circ}{=}} t_1\} \cup P; \{x \stackrel{i}{=} t_2\} \cup S}{\bot} t_1 \neq t_2$	[Merging Clash]
$\frac{\{x \doteq {}^? t\} \cup P; S}{P; \{x \doteq t\} \cup S} \ x \doteq t' \notin S \text{ where } t \neq t'$	[Variable Elimination]

Table 3.1: Rules for C-matching

- S is a set of equations in matched form.
- \perp represents failure (i.e., no matchers).

A matcher (or a solution) of a system P; S returns a matcher that solves each of the matching equations in P and S.

Table. 3.1 depicts an example of the matching rules when E = C.

The following algorithm matchs s to t.

- 1. Create an initial system is $\{s \doteq^? t\}; \emptyset$.
- 2. Apply successively the matching rules.
- 3. If the final system is $\emptyset; S$, return S.
- 4. If the final system is \perp , then fail.

C-Matching Algorithm

Example 3.3.9. The followings show the process of finding C-matchers of f(x, f(a, x)) and f(g(a), f(a, g(a))) where f and g are function symbols, and the others are variables.

$$\begin{cases} f(x,y) \doteq_{C}^{?} f(f(a,b), f(b,a)) \}; \emptyset \Longrightarrow_{\mathsf{Decomposition}} \\ \{x \doteq_{C}^{?} f(a,b); y \doteq_{C}^{?} f(b,a) \}; \emptyset \Longrightarrow_{\mathsf{V}.\mathsf{Elim}} \\ \{y \doteq_{C}^{?} f(b,a) \}; \{x \doteq_{C} f(a,b) \} \Longrightarrow_{\mathsf{V}.\mathsf{Elim}} \\ \emptyset; \{x \doteq_{C} f(a,b), y \doteq_{C} f(b,a) \} \Longrightarrow_{\mathsf{V}.\mathsf{Elim}} \end{cases}$$

The other way is

$$\begin{cases} f(x,y) \doteq_{C}^{?} f(f(a,b), f(b,a)) \}; \emptyset \Longrightarrow_{\mathsf{C-Decomposition}} \\ \{x \doteq_{C}^{?} f(b,a); y \doteq_{C}^{?} f(a,b) \}; \emptyset \Longrightarrow_{\mathsf{V.Elim}} \\ \{y \doteq_{C}^{?} f(a,b) \}; \{x \doteq_{C} f(b,a) \} \Longrightarrow_{\mathsf{V.Elim}} \\ \emptyset; \{x \doteq_{C} f(b,a), y \doteq_{C} f(a,b) \} \Longrightarrow_{\mathsf{V.Elim}} \end{cases}$$

Note that C-Decomposition and Decomposition transform the same system in different ways. There may exist multiple matchers, and C-matching algorithm is NP-complete [39].

To avoid the exponential complexity of C-matching algorithm, we bound the number of applications of the C-Decomposition rule. This lead to incompleteness, but does not harm the correctness of the matching algorithm.

3.4 Optimization with Backtracking Based on Equality Saturation

3.4.1 E-graph Structure

E-graph structure is defined as a triple of a set of endes, a set of eclasses, and a set of edges. Each enode contains a boolean operator (\land, \oplus) or boolean value (0, 1, x). Eclass is a set of enodes. Edge connects an enode to an eclass.

The meaning of the E-graph is as follows. Each enode represents a set of all expressions that can be generated in the following manner, and each eclass represents a set of all expressions that can be generated by enodes inside it. All E-graphs must have invariant that all expressions generated by enodes in the same eclass must be semantically equivalent. If an enode is a boolean value, it generates the constant expression itself. If an enode is a boolean operator, it generates all expressions that can be made by choosing each child boolean expression among the expressions that the corresponding child eclass represents.



Figure 3.1: Simple example of E-graph. Each box means enode, and dotted box means eclass.

Figure 3.1 shows the concept of an E-graph. Let us illustrate how to generate various expressions from eclass or enode, and show that the above invariant of E-graph holds. Let EC_i and EN_i be the sets of expressions that can be generated by eclass ec_i and enode *i* respectively. Since enode 1–3 respectively contain a boolean value x_1-x_3 , $EC_i = EN_i = \{x_i\}(1 \le i \le 3)$. Since enode 4 contains a boolean operator \wedge and has two children eclasses ec_1 and ec_2 , it can generate $x_1 \wedge x_2$ by choosing $x_1 \in EC_1$ and $x_2 \in EC_2$ as two children expressions. Similarly, we get $EC_5 = EN_5 = \{(x_1 \wedge x_2) \wedge x_3\}, EC_6 = EN_6 = \{(x_2 \oplus x_3) \oplus (x_2 \oplus x_3)\}$, and $EN_7 = \{((x_1 \wedge x_2) \wedge (x_2 \oplus x_3)) \wedge (x_2 \oplus x_3) \oplus (x_2 \oplus x_3))\}$. In the same way, we get $EC_{root} = EN_7 \cup EN_8 = \{((x_1 \wedge x_2) \wedge x_3) \wedge ((x_2 \oplus x_3) \wedge ((x_2 \oplus x_3) \oplus (x_2 \oplus x_3))), ((x_1 \wedge x_2) \wedge (x_2 \oplus x_3)) \wedge x_3\}$. Recall that two boolean expressions in EC_{root} are semantically equivalent.

Note that E-graph also can be interpreted as a program grammar. Each of the eclasses corresponds to a nonterminal symbol and each of enodes in that eclass corresponds to a production rule for the nonterminal symbol. Every E-graph corresponds to a particular context-free grammar.

In this context, equality saturation is a process of constructing a program

grammar that can generate all boolean circuits equivalent to the input circuit.

3.4.2 Equality Saturation Process

Equality saturation consists of two processes: saturation process and extraction process. First, in the saturation process, we continually expand E-graph by finding all circuits which are semantically equivalent to an initial circuit. We call the E-graph saturated if we can not find any other equivalent circuits. In the extraction process, we extract a circuit that has the lowest multiplicative depth from a saturated E-graph.

Figure 3.2 and Figure 3.3 illustrates the saturation process. Same as Section 2, we start with a term graph of the input boolean circuit $((x_1 \wedge x_2) \wedge (x_2 \oplus x_3)) \wedge x_3$ and following rewrite rules.

$$\begin{aligned} \text{rule} (1): & ((v_1 \wedge v_2) \wedge v_3) \wedge v_4 & \to & ((v_1 \wedge v_2) \wedge v_4) \wedge ((v_2 \oplus v_4) \oplus v_3) \\ \text{rule} (2): & ((v_1 \wedge v_2) \wedge v_3) \wedge v_4 & \to & (v_1 \wedge v_2) \wedge (v_3 \wedge v_4) \\ \text{rule} (3): & (v_1 \oplus v_1) & \to & 0 \\ \text{rule} (4): & (v_1 \wedge 0) & \to & 0 \end{aligned}$$

Then we explore optimized circuits that can be generated based on the rewrite rules by an iteration of three steps: ematch, add, and merge. Figure 3.2 illustrates the first iteration. In the ematch step, we find all enodes that can be rewritten by a certain rule. An enode is said rewritable by a certain rule if it can generate a circuit which can be rewritten by the rule. As the root enode can generate circuit $((x_1 \wedge x_2) \wedge (x_2 \oplus x_3)) \wedge x_3$ that can be rewritten by rule (1) and rule (2), we get two rewritten circuits as a result of ematch step for the root enode (Figure 3.2.(a)). In the add step, we add all rewritten circuits to the E-graph in a recursive manner from bottom to top (Figure 3.2.(b)). Newly added enodes are colored gray in Figure 3.2 and Figure 3.3. Note that each added rewritten circuit corresponds to an enode. In the merge step, for each pair of rewritable enode



Figure 3.2: Change of E-graph during a single iteration. Dotted box means eclass. (a) ematch result for root enode. (b) add subcircuit c_1 and c_2 to E-graph. (c) merge root node and result enodes $(c_1 \text{ and } c_2)$ of add step.

and rewritten circuit, we merge the rewritable enode and newly added enode (that corresponds to the rewritten circuit) as the same eclass (Figure 3.2.(c)). In the second and third iteration, we expand E-graph by applying rewrite rule (3) and rule (4) respectively (Figure 3.3.(c), Figure 3.3.(d)).¹ As the root enode is merged with the enode that contains 0, we can figure out that the initial circuit is semantically equivalent to 0. More details of these three steps are described in Willsey et al. [65].

We repeat the above three steps until no further changes are made (i.e. E-graph is saturated). Since it is not guaranteed that an E-graph will end up saturating, we give an appropriate amount of time limit (12 hours).

¹More enodes are rewritable by rule (2), but we ruled out them in Figure 3.3 for clarity.



Figure 3.3: Change of E-graph during iterations. Dotted box means eclass. (a) initial E-graph. (b) after 1 iteration. (c) after 2 iterations. (d) saturated E-graph.

In the extraction process, we extract the least-cost circuit from the saturated E-graph for the given cost model. If the cost function is local (the cost of a node is computable only with the costs of its children nodes), it is known that the least-cost circuit for that cost model can be easily extracted from the E-graph [65]. In our case, since the multiplicative depth of the circuit is a local function, circuits with the lowest multiplication depth can be easily extracted.

3.4.3 Tradeoff between Optimality and Cost

In our single-path (i.e. without backtracking) term rewriting system defined in Section 3.3, we can only explore limited optimization space due to termination property and efficiency. To ensure termination, we selectively rewrite a target circuit only when its multiplicative depth is reduced. For efficiency, we apply rewrite rules only to a target circuit lying on critical paths. For these reasons, we have to give up the guarantee to find a globally optimal circuit for efficient and terminating rewriting procedures. In a saturation-based rewriting system, the possibility of finding the globally optimal circuit is enlarged. As we efficiently compress all possible result circuits as a form of program grammar, we can explore an expansive space within a practical time budget.

Although equality saturation is a time-consuming method in general, we can successfully introduce it for our term rewriting system since most of the homomorphic evaluation circuits have a relatively small scale.

Chapter 4

Evaluation

We implemented our method as a tool named LOBSTER¹. This section evaluates our LOBSTER system to answer the following questions:

- **Q1:** How effective is LOBSTER for optimizing FHE applications from various domains?
- **Q2:** How does LOBSTER compare with existing general-purpose FHE optimization techniques?
- Q3: What is the benefit of reusing pre-learned rewrite rules?
- **Q4:** What is the benefit of using equality saturation technique?
- Q5: What is the benefit of the rule normalization and equational matching?
- Q6: How long does LOBSTER take to obtain optimized circuits?
- **Q7:** How sensitive is LOBSTER to changes in the training set for learning rewrite rules?

 $^{^1\}mathbf{L}\mathbf{e}\mathrm{arning}$ to $\mathbf{O}\mathrm{ptimize}\ \mathbf{B}\mathrm{oolean}$ circuits using $\mathbf{S}\mathrm{ynthesis}\ \&\ \mathbf{T}\mathbf{E}\mathrm{rm}\ \mathbf{R}\mathrm{e}\mathrm{writing}$

All of our experiments were conducted on Linux machines with Intel Xeon 2.6GHz CPUs and 256G of memory.

4.1 Experimental Setup

Implementation

LOBSTER comprises three pieces: (i) an offline rule learner, (ii) an online circuit optimizer, and (iii) a homomorphic circuit evaluator. LOBSTER is written in OCaml and RUST, and consists of about 3K lines of code.

The offline rule learner collects rewrite rules for online optimization. For each of the benchmarks, we performed the offline learning algorithm (Algo. 1) with a timeout of 1 week before online optimization. We use EUSOLVER $[3]^2$ and DUET $[41]^3$, which are state-of-the-art open-source search-based synthesizers, for the offline learning task. We use a timeout of one hour for synthesizing each rewrite rule.

The online circuit optimizer transforms Boolean circuits generated by CINGULATA [16], an open-source FHE compiler, into depth-optimized ones. CINGULATA first directly translates a given FHE application written in C++ into a Boolean circuit representation, and then heuristically minimizes the circuit area by removing redundancy using the ABC tool [12], which has been widely used for hardware synthesis. Then, our optimizer performs the saturation-based rewriting procedure on the resulting circuit. We used EGG [65] library to implement saturationbased rewriting system. We used learned rewrite rules and commutativity as equality rules, and did not use any eclass analysis.

Circuits optimized by the online optimizer are evaluated by our homomorphic circuit evaluator built using HElib [35].⁴ When homomorphically evaluating

 $^{^{2}}$ In our previous work [40], we chose EUSOLVER among the general-purpose synthesizers that participated in the 2019 SyGuS competition [55] since the tool performs best for our optimization tasks.

³We could learn new rewrite rules using DUET that outperforms EUSOLVER for our optimization tasks in most cases.

 $^{^{4}}$ We could not use the homomorphic circuit evaluator provided by CINGULATA because it crashed for some of our evaluation benchmarks, which are fairly sizeable circuits.

circuits, we set the security parameter to 128 which is usually considered large enough. It means a ciphertext can be broken in the worst case time proportional to 2^{128} .

Benchmarks

Our benchmarks comprise 25 FHE applications written using the CINGULATA APIs shown in Table 4.1. We had initially collected 64 benchmarks from the following four sources.

- CINGULATA benchmarks 9 FHE-friendly algorithms from diverse domains (medical diagnosis, stream cipher, search, sort) [22].
- Sorting benchmarks 4 privacy-preserving sorting algorithms (merge, insertion, bubble, and odd-even) [17]. All the sorting algorithms can take up to 6 encrypted 8-bit integers as input.
- Hacker's Delight benchmarks 26 homomorphic bitwise operations adapted from Hacker's Delight [64]⁵, a collection of bit-twiddling hacks. We include these benchmarks because they can be potentially used as building blocks for efficient FHE applications that perform computations over fixed-width integers.
- EPFL benchmarks 25 circuits from EPFL combinational benchmark suite [1]. The circuits are intendedly suboptimal to test the ability of circuit optimization tools.

Among these 64 candidate benchmarks, we ruled out 39 benchmarks that are out of reach for homomorphic evaluation even with the state-of-the-art FHE scheme [35], or which are likely depth-optimal based on empirical evidence.

Among the excluded 39 benchmarks, 18 benchmarks have the number of AND/XORs greater than 10,000, or the multiplicative depth is larger than 100.

 $^{^{5}22}$ benchmarks used for program synthesis [38] + 4 excerpted from Hacker's Delight [64]

Table 4.1: Characteristics of benchmarks from {medical [14], sorting [17], bitvector evaluation [38, 64], circuit [1]} algorithm. \times **Depth** denotes the multiplicative depth. **#AND** and **Size** give the number of AND operations and the circuit size, respectively.

Name	Description	×Depth	#AND	Size
cardio	medical diagnostic algorithm	10	109	318
dsort	FHE-friendly direct sort	9	708	1464
msort	merge sort	45	810	1525
isort	insertion sort	45	810	1525
bsort	bubble sort	45	810	1525
osort	oddeven sort	25	702	1343
hd-01	isolate the rightmost 1-bit	6	87	118
hd-02	absolute value	6	76	229
hd-03	floor of average of two integers (a clever impl.)	5	27	64
hd-04	floor of average of two integers (a naive impl.)	10	75	159
hd-05	max of two integers	7	121	295
hd-06	min of two integers	7	121	295
hd-07	turn off the rightmost contiguous string of 1-bits	5	17	32
hd-08	determine if an integer is a power of 2	6	18	37
hd-09	round up to the next highest power of 2	14	134	236
hd-10	find first 0-byte	6	35	73
hd-11	the longest length of contiguous string of 1-bits	18	391	652
hd-12	number of leading 0-bits	16	116	232
bar	barrel shifter	12	3141	5710
cavlc	coding-cavlc	16	655	1219
ctrl	ALU control unit	8	107	180
dec	decoder	3	304	312
i2c	i2c controller	15	1157	1987
int2float	int to float converter	15	213	386
router	lookahead XY router	19	170	277

Our homomorphic circuit evaluator runs out of memory for these circuits. The rest 21 benchmarks are such that (i) baseline optimizer [15] fails to reduce the multiplicative depth, and (ii) we could not mine any rules from their circuit representations even after 7 days of running the offline learner, because this means that even the state-of-the-art synthesizer with practically unlimited time cannot find any improvement.

Baseline

We compare LOBSTER to Carpov et al. [15], which also aims at minimizing the multiplicative depth of circuits for homomorphic evaluation. The work is also based on term rewriting, but only with two hand-written rewrite rules. The first rule is based on AND associativity: $(x \wedge y) \wedge z \rightarrow x \wedge (y \wedge z)$. In a given circuit c, a substitution σ such that $\sigma((x \wedge y) \wedge z)$ is syntactically matched with a sub-circuit of c is found. The matched part $\sigma((x \wedge y) \wedge z)$ is replaced with $\sigma(x \wedge (y \wedge z))$ if $\ell(y) < \ell(x)$ and $\ell(z) < \ell(x)$. This rewrite rule, when applied into a critical path, reduces the depth by one from $\ell(\sigma(x)) + 2$ to $\ell(\sigma(x)) + 1$. The second rewrite rule is based on XOR distributivity: $(x \oplus y) \wedge z \rightarrow (x \wedge z) \oplus (y \wedge z)$. This rule does not affect the depth, but it can make the first rule applicable by clearing XOR operators away. The two rewrite rules repeatedly rewrite critical paths until a heuristic termination condition is satisfied. As the tool is not publicly available, we reimplemented their algorithm.⁶

4.2 Effectiveness of Lobster

Optimization Effect

We evaluate LOBSTER on the benchmarks and compare it with Carpov et al. [15]. Both of the tools are provided circuits initially generated by CINGULATA. We aim to determine whether LOBSTER can learn rewrite rules from training circuits and effectively generalize them for optimizing other unseen circuits. To this end, we conduct **leave-one-out cross validation**; for each benchmark, we use rewrite rules learned from the other remaining 24 benchmarks. Both of the tools are given the timeout limit of 12 hours for the optimization tasks; in case of exceeding the limit, we use the best intermediate results computed so far.

We measure LOBSTER's reduction ratios of the multiplicative depth and speedups in overall homomorphic evaluation time against the initial CINGULATA-

⁶We use the "random" priority function because it slightly outperforms the "non-random" heuristics according to the results in Carpov et al. [15].



Figure 4.1: Main results comparing the optimization performance of LOBSTER and Carpov et al. [15] – Speedups in overall homomorphic evaluation time (left) and depth reduction ratios (right).



Figure 4.2: Correlation plot of multiplicative depth and homomorphic evaluation time

generated circuits, and compared them with Carpov et al. [15]. The results are summarized in Fig. 4.1. More detailed information can be found in Table 4.2. LOBSTER is able to optimize 22 out of 25 benchmarks within the timeout limit. LOBSTER achieves 1.08x - 5.43x speedups with the geometric mean of 2.05x. The number of AND gates increases up to 1.9x more with the geometric mean of 1.31x. The depth reduction ratios range from 12.5% to 53.3% with the geometric mean of 25.1%.

We next study the results in detail. Most notably, LOBSTER achieves 2.62x

Table 4.2: Detailed main results (comparison to Carpov etl al. [15]). The timeout for optimization is set to 12 hours. $\#AND \uparrow$ shows the ratio between the number of AND gates of the optimized circuit and the original one. **Eval.** Time shows homomorphic evaluation time (where '-' means that the depth and evaluation time is the same as the original).

	Original			Carpov et al.			Lobster			
Name	×Depth	Eval. Time	×Depth	#AND ↑	Eval. Time	×Depth	#AND ↑	Eval. Time		
cardio	10	17m 14s	9	x1.07	10m 08s	8	x1.12	6m 34s		
dsort	9	10m 52s	8	x1.08	8m 29s	7	x1.33	6m 47s		
msort	45	5h 20m 59s	41	x1.02	5h 00m 06s	36	x1.88	2h 40m 23s		
isort	45	5h 20m 16s	-	-	-	36	x1.88	2h 38m 53s		
bsort	45	5h 21m 46s	41	x1.02	5h 06m 09s	36	x1.88	2h 32m 38s		
osort	25	2h 16m 58s	-	-	-	20	x1.91	43m 12s		
hd01	6	4m 36s	-	-	-	-	-	-		
hd02	6	4m 50s	-	-	-	-	-	-		
hd03	5	1m 08s	-	-	-	4	x1.44	1m 03s		
hd04	10	9m 06s	9	x1.00	7m 36s	7	x1.31	2m 25s		
hd05	7	6m 08s	-	-	-	6	x1.52	4m 16s		
hd06	7	6m 14s	-	-	-	6	x1.54	4m 12s		
hd07	5	1m 02s	-	-	-	3	x1.12	24s		
hd08	6	2m 18s	5	x1.00	1m 03s	5	x1.00	57s		
hd09	14	13m 03s	12	x1.10	9m 34s	11	x1.37	8m 48s		
hd10	6	4m 24s	5	x1.03	2m 07s	5	x1.00	1m 20s		
hd11	18	33m 31s	17	x1.00	28m 30s	14	x1.08	17m 42s		
hd12	16	22m 31s	15	x1.00	18m 01s	14	x1.12	12m 26s		
bar	12	56m 55s	-	-	-	10	x0.89	37m 47s		
cavlc	16	26m 35s	10	x1.20	15m 01s	9	x1.18	9m 37s		
ctrl	8	3m 06s	6	x1.02	2m 44s	4	x1.19	1m 14s		
dec	3	38s	-	-	-	-	-	-		
i2c	15	51m 00s	9	x1.08	21m 38s	8	x1.21	15m 45s		
int2float	15	15m 23s	9	x1.13	6m 30s	7	x1.21	2m 50s		
router	19	37m 26s	10	x1.31	12m 34s	10	x1.38	11m 39s		

and 1.60x speedups for the two CINGULATA benchmarks cardio and dsort, respectively. Recall that they are already carefully hand-tuned to be depth optimized. This result shows that our method provides significant performance gains that are complementary to those achieved by domain-specific optimizations. The four sorting benchmarks also observe significant performance improvements. For the four sorting benchmarks, we used single-path term rewriting of previous LOBSTER [40], since EGG library failed to perform saturation task for circuits that has multiplicative depth over 25. LOBSTER reduces the depth by 20% for each of them. The osort benchmark shows a 3.17x speedup, and the other three benchmarks show 2.0x speedups. As of the Hacker's Delight benchmarks, 10 out of 12 observe improvements. The speedups for hd-04, hd-07, hd-08 and hd-10 are remarkable (3.8x, 2.6x, 2.4x and 3.3x, respectively). For the other benchmarks, we observe 1.08x - 1.89x speedups. However, both of the two optimizers fail to optimize the other 2 benchmarks, which are relatively simple. Based on the fact that these small and tricky algorithms are designed to efficiently perform computations on plaintexts, we suspect most of these benchmarks to be depth-optimal.

As of the EPFL benchmarks, 6 out of 7 observe improvements. Both optimizers fail to optimize dec, which is relatively simple. For bar, we observe 1.51x speedup. For the other benchmarks (cavlc, ctrl, i2c, int2float and router), LOBSTER achieves remarkable speedups (2.5x - 5.4x).

The number of AND gates increases 1.31x more on average. For the 4 sorting benchmarks ($\{m,i,b,o\}$ sort), we observe nearly 2x increases. For the other benchmarks, we observe up to 1.5x increases. These increases are acceptable considering depth reduction ratio and speedup. The increases in the number of XOR gates is similar, with the geometric mean of 1.2x.

In terms of time spent for the optimization, LOBSTER successfully optimizes circuits better than Carpov et al. [15] within given time limit (12 hours).

Note that Fig. 4.2 shows that the depth reduction ratios are generally proportional to performance improvements (but not exactly proportional since the number of AND operations also influences the performance). This shows that multiplicative depth reduction is a good proxy for speedup, and thus we only measured depth reduction ratio rather than speedup in sub-experiments (Section 4.4 – Section 4.8).

Learning Capability

We investigate the learned rewrite rules. From all the benchmarks, our rule learner mines 502 rewrite rules. The rule sizes (the size of a rule $l \rightarrow r$ is measured by |l|) range from 4 to 38. The average and median sizes are 14 and 13, respectively. Fig. 4.3 shows how often these rules were applied to reduce the multiplicative depth during our single-path term rewriting. Relatively small-sized rules (size 5 - 15) are most frequently used, but also the large-sized rules are sometimes applied and optimize wide areas of the input boolean circuits.



Figure 4.3: distribution of rule sizes and how often they were used during optimization

The machine-found optimization patterns are surprisingly aggressive. For example, the following intricate rules enable to reduce the depth of a rewritten path by 1 when applied once (we denote $1 \oplus c$ as $\neg c$).

$$(v_1 \land (v_2 \land ((v_3 \oplus (v_4 \land v_5)) \oplus (v_6 \land v_5)))) \rightarrow ((((v_6 \oplus v_4) \land v_5) \oplus v_3) \land (v_2 \land v_1)) ((\neg((v_1 \land (\neg(v_2 \oplus v_3))) \oplus (v_2 \oplus v_3))) \land v_4) \rightarrow ((((\neg v_2) \oplus v_3) \land ((\neg v_1) \land v_4)) (\neg((((((v_1 \oplus v_2) \land v_3) \land v_4) \land v_5)) \oplus v_2)) \rightarrow ((((v_2 \oplus v_1) \land v_4) \land (v_3 \land v_5)) \oplus v_2) (((\neg((v_1 \oplus (v_2 \land v_3)) \oplus (v_4 \land v_3))) \land v_5) \rightarrow ((((v_2 \oplus v_4) \land (v_5 \land v_3)) \oplus ((\neg v_1) \land v_5)) (((((v_1 \oplus v_2) \oplus v_3) \land (((v_1 \oplus v_2) \land v_3) \oplus (v_1 \land v_2))) \land ((((v_1 \oplus v_2) \land v_3) \oplus (v_1 \land v_2)) \land (((v_1 \oplus v_2) \land v_3)) \oplus ((\neg((v_1 \oplus v_2) \land v_3)))) (((((v_1 \oplus v_2) \land v_3) \oplus (v_1 \land v_2)) \land ((v_1 \oplus v_2) \land v_3)) \oplus ((\neg((v_1 \oplus v_2) \land v_3))))) \rightarrow ((v_3 \land v_1) \land v_2)$$

Next, we investigate how long it takes to learn rewrite rules. The offline learning algorithm (Algo. 1) is time consuming. The timeout limit for the offline learning is set to 168 hours (i.e., 1 week), and we use intermediate results (rules collected so far) when the budget expires. On average, the offline learning phase for each benchmark takes 125 hours. For dsort, hd01, hd02, hd03, hd10, ctrl and dec, the learning takes 1 - 46 hours. For router, it takes 129 hours. The other benchmarks takes 168 hours (i.e., the learner is forced to stop when the time budget expires).

Answer to Q1: LOBSTER can optimize 22 out of 25 realistic FHE applications. (x2.26 speedup, 25.1% depth reduction).

4.3 Comparison to the Baseline

Fig. 4.1 shows that LOBSTER significantly outperforms the existing state-ofthe-art homomorphic circuit optimizer [15] in terms of both depth reduction ratio and homomorphic evaluation time. Only 15 out of 25 benchmarks can be optimized by Carpov et al. [15], whereas LOBSTER is able to optimize 22. Compared to Carpov et al. [15], LOBSTER's speedup is increased by up to 3.17x with the geometric mean of 1.56x. The depth reduction ratio is increased by up to 40.0% with the geometric mean of 13.8%.

We observe that Carpov et al. [15] needs relatively small amount of optimization time than LOBSTER. It took 1 second – 25 minutes to optimize benchmarks with the average of 2 minutes, whereas LOBSTER took 12 hours to optimize each benchmark. This is because Carpov et al. [15] uses single-path rewriting with two simple rewrite rules, whereas LOBSTER uses saturation-based rewriting with total 502 rewrite rules.

We empirically observe that Carpov et al. [15] often falls into the basin of local minima because its two rewrite rules can modify only a small area at a time. On the contrary, LOBSTER often applies large rewrite rules and escapes local optima.

Answer to Q2: LOBSTER outperforms existing FHE optimizer. (x1.56 speedup, 13.8% depth reduction ratio)

4.4 Efficacy of Reusing Pre-Learned Rewrite Rules

We observe that reusing pre-learned rewrite rules significantly enhanced LOBSTER's scalability and exploration power.

To investigate the benefit for scalability, we compare LOBSTER to a simple method that uses the offline rule learner as an on-the-fly optimizing synthesizer. Since it does not use any pre-learned rewrite rules, it can not use saturationbased rewriting. While performing single-path rewriting, it finds an optimized version of sub-circuits using a program synthesizer rather than matching it with pre-learned rewrite rules. The timeout limit for optimization is again set to 12 hours, and we use the best intermediate results when the budget expires.



Figure 4.4: Comparison between on-the-fly synthesis and equality saturation with learned rules

Fig. 4.4 summarizes the results. The synthesis-based optimizer can optimize only 14 benchmarks within the timeout limit. Furthermore, in all the 14 benchmarks, the depth reduction ratio is less than that of LOBSTER that reuses pre-learned rewrite rules (geometric mean of 8.2% vs 25.1%). That is mainly due to its limited scalability; if the synthesis-based optimizer is given 7 days, it can achieve optimization effects similar to LOBSTER's. Such enormous optimization costs are mainly due to the inability to prove unrealizability (i.e., no solution) of attempts of optimizing already depth-optimal circuit regions. In such cases, the synthesizer wastes the timeout limit of 1 hour. On the contrary, LOBSTER can avoid such situations by giving up cases beyond the reach of previously learned rules.

To investigate the benefit for exploration power, we compare LOBSTER to a simple version that only uses boolean ring theory (Example. 3.1.3) as rewrite rules. Fig. 4.5 summarizes the results. The simple version of LOBSTER can optimize only 6 benchmarks within the timeout limit. Furthermore, in all the 6



Figure 4.5: Impact of changing rewrite rules

benchmarks, the depth reduction ratio is less than that of LOBSTER that uses pre-learned aggressive rewrite rules. This shows that LOBSTER can efficiently escape local optima by applying pre-learned rewrite rules even though they can be induced by boolean ring theory.

We also investigate that adding new rewrite rules can enhance exploration power. We compare LOBSTER to a simple version that only uses 188 rewrite rules learned by EUSOLVER rather than the whole 502 rewrite rules learned by EUSOLVER and DUET. Fig. 4.5 summarizes the results. In five benchmarks, the simple version's depth reduction ratio is less than that of LOBSTER that uses all rewrite rules. In the exceptional case of hd 01, the simple version can reduce multiplicative depth by 1, whereas LOBSTER can not optimize it within time limit. This is because full version of LOBSTER needs much more time to perform each iteration step for equality saturation, since it uses nearly 3 times more rewrite rules than the simple version. If the LOBSTER is given sufficient amount of time limit, it can also optimize hd 01.

Table 4.3: Detailed comparison results of single-path rewriting and saturationbased rewriting. The timeout for optimization is set to 12 hours. $\#AND \uparrow$ shows the ratio between the number of AND gates of the optimized circuit and the original one. **Eval. Time** shows homomorphic evaluation time (where '-' means that the evaluation time is the same as the original).

	Original		Single-path			Saturation-based			
Name	×Depth	Eval. Time	×Depth	#AND ↑	Eval. Time	×Depth	#AND ↑	Eval. Time	
cardio	10	17m 14s	8	x1.06	7m 02s	8	x1.12	6m 34s	
dsort	9	10m 52s	8	x1.12	8m 08s	7	x1.33	6m 47s	
msort	45	5h 20m 59s	36	x1.88	2h 40m 23s	-	-	-	
isort	45	5h 20m 16s	36	x1.88	2h 38m 53s	-	-	-	
bsort	45	5h 21m 46s	36	x1.88	2h 32m 38s	-	-	-	
osort	25	2h 16m 58s	20	x1.91	43m 12s	-	-	-	
hd01	6	4m 36s	-	-	-	-	-	-	
hd02	6	4m 50s	-	-	-	-	-	-	
hd03	5	1m 08s	-	-	-	4	x1.44	1m 03s	
hd04	10	9m 06s	8	x1.04	3m 20s	7	x1.31	2m 25s	
hd05	7	6m 08s	-	-	-	6	x1.52	4m 16s	
hd06	7	6m 14s	-	-	-	6	x1.54	4m 12s	
hd07	5	1m 02s	3	x0.76	24s	3	x1.12	24s	
hd08	6	2m 18s	5	x1.00	1m 00s	5	x1.00	57s	
hd09	14	13m 03s	10	x1.32	7m 56s	11	x1.37	8m 48s	
hd10	6	4m 24s	5	x1.03	1m 25s	5	x1.00	1m 20s	
hd11	18	33m 31s	15	x1.00	21m 40s	14	x1.08	17m 42s	
hd12	16	22m 31s	15	x1.00	17m 04s	14	x1.12	12m 26s	
bar	12	56m 55s	11	x0.96	48m 19s	10	x0.89	37m 47s	
cavlc	16	26m 35s	10	x1.02	13m 06s	9	x1.18	9m 37s	
ctrl	8	3m 06s	5	x1.12	1m 18s	4	x1.19	1m 14s	
dec	3	38s	-	-	-	-	-	-	
i2c	15	51m 00s	8	x1.05	15m 59s	8	x1.21	15m 45s	
int2float	15	15m 23s	8	x1.10	4m 09s	7	x1.21	2m 50s	
router	19	37m 26s	10	x1.12	12m 31s	10	x1.38	11m 39s	

Answer to Q3: Reusing learned rules enhances LOBSTER's scalability and exploration power.

(1 week vs 12 hour opt. time, 2.6% vs 23.7% vs 25.1% depth reduction)

4.5 Efficacy of Equality Saturation

We now evaluate the effectiveness of saturation-based rewriting, which we used for online optimization. We compare LOBSTER to a previous version [40] that uses single-path rewriting only.

We measure both version of the LOBSTER's reduction ratios of the multiplica-



Figure 4.6: Efficacy of Equality Saturation

tive depth and speedups in overall homomorphic evaluation time. The results are summarized in Fig. 4.6. More detailed information can be found in Table 4.3. As we mentioned before, the four sorting benchmarks can only be optimized by the single-path rewriting method. Except for the four sorting benchmarks, the saturation-based rewriting method is able to optimize 3 benchmarks (hd03, hd05, hd06) which can not be optimized by the single-path rewriting method. For the 8 benchmarks(dsort, hd04, hd11, hd12, bar, cavlc, ctrl, int2float), the saturationbased rewriting method outperforms the single-path rewriting method in terms of depth reduction ratio. This shows that the saturation-based rewriting method can explore wider area of optimization results because it can store every possible rewriting sequences (i.e. saturation-based rewriting obtains backtracking effect). By contrary, for hd09 benchmark, the single-path rewriting method outperforms the saturation-based rewriting method. This is because the single-path rewriting method can explore deeper single rewriting path within given time limit, since each iteration step for equality saturation needs much more time than single critical path rewriting. If the saturation-based method is given sufficient amount

of time limit and memory storage, it can also optimize hd09 same with the single-path rewriting method.

Answer to Q4: Equality saturation enhances LOBSTER's exploration power via backtracking. (20.2% vs 25.1% depth reduction)

4.6 Efficacy of Equational Rewriting

We now evaluate the effectiveness of design choices made in LOBSTER– the rule normalization and equational term rewriting. We compare LOBSTER with its variant without the two techniques. In other words, the variant uses syntactic matching instead of equational matching when conducting term rewriting and applies the learned rules without the normalization process.



Figure 4.7: Efficacy of equational rewriting

Fig. 4.7 summarizes the results. The variant can optimize 16 benchmarks (LOBSTER can optimize 22), and its depth reduction ratio is less than that of LOBSTER in 12 benchmarks. In the exceptional case of hd 01, the variant

can reduce multiplicative depth by 1, whereas LOBSTER can not optimize it within time limit. This is because of the difference of time cost for each iteration step for equality saturation. Same as 4.4, LOBSTER can also optimize hd 01 if it is given sufficient amount of time limit. We conclude that overall, the rule normalization and equational term rewriting play crucial roles in giving flexibility to the rewriting procedure.

Answer to Q5: Equational matching enables flexible rewriting and enhances exploration power.
(16 vs 22 optimized benchmarks, 17.6% vs 25.1% depth reduction)

4.7 Sensitivity to Changes in a Time Limit



Benchmarks

Figure 4.8: Comparison between the optimization results with 1h and 12h of time limit.

We now investigate the effects of changing the time limit of online optimization.

We compared LOBSTER with its variant that is given 1 hour of time limit.

Fig. 4.8 summarizes the results. Both of the tools can optimize 22 benchmarks within time limit. In 4 sorting benchmarks $(\{m,i,b,o\}sort)$ that use single-path rewriting, LOBSTER significantly outperforms the variant. In the other benchmarks that use saturation-based rewriting, LOBSTER slightly outperforms the variant. This shows that most of the effective iteration steps for equality saturation are performed within 1 hour, since each iteration step needs much more time as the number of iteration grows. We conclude that the most appropriate time limit for LOBSTER is 12 hours, but we can also get similar optimization result with 1 hour of time limit in saturation-based rewriting.

Answer to Q6: LOBSTER takes less than 12 hour to obtain practically saturated circuit.

4.8 Sensitivity to Changes in a Training Set

We now investigate the effects of changing the number of training programs. We have conducted 2-fold cross validation; for each of four benchmark categories (Cingulata, Sorting, HD, EPFL), we used rules learned from the smaller half and applied them to the other larger half, and compare with the result of leave-one-out cross validation. The 14 benchmarks on the x-axis in Fig. 4.9 are testing benchmarks, and the other 11 benchmarks are training benchmarks.

As can be seen in Fig. 4.9 that summarizes the results, the smaller set of training programs does not lead to significant performance degradation. The cardio, hd05, hd06, hd09, hd12, cavlc, and int2float benchmarks observe optimization effects less powerful than before, but the other benchmarks remain the same. We conclude that overall, the performance of LOBSTER is not much sensitive to changes in a given set of training programs.

Answer to Q7: LOBSTER is not critically sensitive to changes in a given set of training circuits.

(11 vs 14 optimized benchmarks, 23.6% vs 29.0% depth reduction)



Figure 4.9: Comparison between the optimization results with two-fold cross validation and leave-one-out cross validation.

Chapter 5

Related Work

Existing FHE Compilers in comparison

Existing FHE compilers [16, 4, 24, 26, 45, 58, 25, 21, 23] use fixed, hand-tuned optimization methods. These compilers allow programmers to easily write FHE applications without detailed knowledge of the underlying FHE schemes. These compilers also provide optimizations for reducing the multiplicative depth of the compiled circuits. However, the optimization methods are hand-tuned, which requires manual effort and is likely to be sub-optimal. In this dissertation, we aimed to automatically generate optimization rules that can be used by existing compilers.

Cingulata [16] is an open-source compiler that translates high-level programs written in C++ into boolean circuits. It supports optimization of circuits for reducing multiplicative depth based on hand-written rules. Cingulata uses ABC [12], an open-source boolean circuit optimizer, but it does not directly address our optimization problem [16]. Cingulata also uses more advanced, yet hand-written, circuit optimization techniques specially designed for minimizing multiplicative depth [15, 5]. In partic-

ular, the multi-start heuristic by Carpov et al. [15], which we used for comparison with LOBSTER in Section 4, shows a significant reduction in multiplicative depths for their benchmarks. However, we note that the benchmark circuits used in Carpov et al. [15] are "intendedly suboptimal to test the ability of optimization tools" [1]. By contrast, the benchmarks used in this dissertation include circuits that are already carefully optimized in terms of FHE evaluation as explained in Section 4.1, thereby leaving relatively small room for depth reduction. We observe the heuristic in Carpov et al. [15] does not perform very well for such a hard optimization task.

- RAMPARTS [4] is an optimizing compiler for translating programs written in Julia into circuits for homomorphic evaluation. It optimizes the size and multiplicative depth of the circuits using symbolic execution and hand-written rules. It automatically selects the parameters of FHE schemes and the plain text encoding for input values and uses a number of hand-written circuit optimization rules for reducing multiplicative depth.
- ALCHEMY [24] is a system that provides domain-specific languages and a compiler for translating high-level plaintext programs into low-level ones for homomorphic evaluation. The compiler automatically controls the ciphertext size and noise by choosing FHE parameters, generating keys and hints, and scheduling maintenance operations. The domain-specific languages are statically typed and are able to check the safety conditions that parameters should satisfy.
- CHET [26] is a domain-specific optimizing compiler for FHE applications on neural network inference. It enables a number of optimizations automatically, but they are hand-tuned and specific to tensor circuits, e.g.,

determining efficient tensor layout, selecting good encryption parameters, etc. By contrast, our technique is domain-unaware and does not rely on a limited set of hand-written rules.

- COPSE [45] is a domain-specific optimizing compiler for FHE applications on decision forest inference. It vectorizes decision-forest inference models (i.e. parallelizes operations performed during inference) to exploit ciphertext packing technique and optimize FHE applications. This vectorizing process also minimizes the multiplicative depth growth, but its method is hand-tuned and specific to decision forest inference.
- HECO [58] is an optimizing compiler for translating programs written in C-like domain specific language into low-level SEAL language for homomorphic evaluation. It optimizes the runtime and memory usage of the SEAL programs using automatic SIMD batching algorithm. It exploits potential SIMD parallelism by applying automatic batching rules.
- EVA [25] is an optimizing compiler for arithmetic FHE applications. It enables a hand-tuned optimization specific to the CKKS FHE scheme by lowering the cost overhead caused by crypto operations (e.g. linearize, rescale, relevel) that ensure the safety of homomorphic evaluations. EVA_{improved} enhances usability of EVA by making its Python front-end more natural. It also enables two new optimization rules, but still they are hand-tuned. By contrast, our optimization framework is scheme-unaware and has a larger potential for speedup since it aims to reduce the multiplicative depth of FHE applications.
• Porcupine [23] is an optimizing compiler for arithmetic FHE applications. Similar to ours, it uses program synthesis to optimize FHE applications. However, it targets specific DSL named Quill rather than boolean circuits and user has to provide hand-written sketch to successfully optimize target applications.

Superoptimization

Similar to ours, existing superoptimizers [7, 13, 51, 52, 36] for traditional programs are able to learn rewrite rules automatically. The major technical difference, however, is that we use equational matching, rather than syntactic matching, to maximize generalization.

Bansal and Aiken [7] present a technique for automatically constructing peephole optimizers. Given a set of training programs, the technique learns a set of replacement rules (i.e. peephole optimizers) using exhaustive enumeration. The correctness of the learned rules is ensured by a SAT solver. The learned rules are stored in an optimization database and used for other unseen programs via syntactic pattern matching. Optgen [13] is also based on enumeration for generating peephole optimization rules that are sound and complete up to a certain size by generating all rules up to the size and checking the equivalence by an SMT solver. Souper [51] is similar to Bansal and Aiken [7] but is based on a constraint-based synthesis technique and targets a subset of LLVM IR. STOKE [52, 36] uses a stochastic search based on MCMC to explore the space of all possible program transformations for the x86-64 instruction set.

Program Synthesis

Over the last few years, inductive program synthesis has been widely used in various application domains [29, 31, 66, 61, 54, 30, 32]. In this work, we use inductive synthesis to minimize multiplicative depth of boolean circuits. To our knowledge, this is the first application of program synthesis for efficient homomorphic evaluation. Our work has been inspired by the prior work by

Eldib et al. [29], where syntax-guided synthesis and static analysis are used to automatically transform a circuit into an equivalent and provably secure one that is resistant to a side-channel attack.

Term Rewriting and Equality Saturation

Term rewriting [20, 9, 11, 57, 60] and equality saturation [56, 67, 65, 63, 47] has been widely used in program transformation systems. The previous rewrite techniques rely on hand-written rules that require domain expertise, whereas this work uses automatically synthesized rewrite rules. For example, Chiba et al. [20] presented a framework of applying code-transforming templates based on term rewriting, where programs are represented by term rewriting systems and transformed by a set of given rewrite rules (called templates). Visser et al. [60] used term rewriting in ML compilers and presented a language for writing rewriting strategies. Tate et al. [56] and Yang et al. [67] used equality saturation(i.e. saturation-based term rewriting) with hand-tuned rewrite rules to optimize C-like languages and trained DNN models respectively. In this work, we focus on a different application domain of term rewriting (i.e. homomorphic evaluation) and provide a novel idea of learning and using rewrite rules automatically.

Similar to ours, Ruler [48] used equality saturation to automatically infer rewrite rules for a given user-defined domain. Although Ruler found 35 rewrite rules for the boolean circuit domain, we observed that the saturation-based term rewriting with these 35 rewrite rules shows little optimization effect for homomorphic evaluation. It just slightly outperforms an ablation of LOBSTER used in Section 4.4 that uses boolean ring theory as rewrite rules (2.6% vs 3.3%). This is because rewrite rules inferred by Ruler has no objective in mind to reduce the multiplicative depth.

Chapter 6

Conculsion

In this dissertation, we presented a new method for optimizing FHE boolean circuits that does not require any domain expertise and manual effort. Our method first uses program synthesis to automatically discover a set of optimization rules from training circuits. Then, it performs equational term rewriting on the new, unseen circuit based on the equality saturation to maximally leveraging the learned rules. We demonstrated the effectiveness of our method with 25 FHE applications from diverse domains. The results show that our method achieves sizeable optimizations that are complementary to existing domain-specific optimization techniques.

Though we target a specific kind of optimization tasks for homomorphic evaluation in this dissertation, we believe our approach is potentially applicable to other optimization tasks. Our method of synthesizing optimization rules and exhaustively applying the combinations of the learned optimization rules in a cost-effective way by the time-bounded equality saturation technique can be beneficial to a broader class of optimization tasks.

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Appendices

Appendix A

Learned Rewrite Rules

 $(\land (\neg 0) norm_2)$ $\rightarrow norm_2$ $(\land (\land norm_2 y_{14}) y_{15})$ $\rightarrow (\wedge norm_2 (\wedge y_{15} y_{14}))$ $(\land (\neg (\land n_{231} \ n_{223})) \ y_{14})$ $\rightarrow (\oplus (\land (\land y_{14} \ n_{223}) \ n_{231}) \ y_{14})$ $(\neg (\land (\land norm_3 norm_7) n_{379}))$ $\rightarrow (\neg (\land (\land norm_7 \ n_{379}) \ norm_3))$ $(\lor norm_1 (\lor n_{174} (\neg n_{92})))$ $\rightarrow (\lor (\oplus n_{92} (\neg n_{174})) (\lor (\neg n_{92}) norm_1))$ $(\neg (\land n_{171} (\land n_{165} n_{134})))$ $\rightarrow (\lor (\neg n_{165}) (\neg (\land n_{134} n_{171})))$ $(\land (\land (\neg n_{237}) n_{109}) n_{235})$ $\rightarrow (\land (\oplus n_{235} n_{237}) (\land n_{235} n_{109}))$ $(\land (\land (\land n_{301} \ n_{297}) \ n_{290}) \ n_{284})$ $\rightarrow (\land (\land n_{284} \ n_{297}) \ (\land n_{301} \ n_{290}))$ $(\neg (\land (\neg (\land n_{236} n_{222})) y_{17}))$

$$\begin{array}{l} \rightarrow (\oplus (\neg (\land (\land y_{17} n_{222}) n_{236})) y_{17}) \\ (\land (\land n_{481} (\land n_{462} n_{457})) n_{476}) \\ \rightarrow (\land (\land (\land n_{441} n_{440}) n_{1206}) n_{444}) \\ \rightarrow (\land (\land (\land n_{440} n_{449}) n_{1206}) n_{444}) \\ \rightarrow (\land (\land (\land n_{444} n_{440}) n_{449}) n_{1206}) \\ (\land (\land (\land n_{325} norm_3) n_{322}) i_{25}) \\ \rightarrow (\land (\land norm_3 (\land i_{25} n_{322})) n_{325}) \\ (\oplus (\land (\land n_{554} n_{390}) i_9) n_{618}) \\ \rightarrow (\oplus (\land (\land n_{554} n_{390}) i_9) n_{554}) n_{618}) \\ (\land (\oplus (\land n_{107} i_{13}) n_{107}) (\land n_{117} n_{118})) \\ (\land (\oplus (\land n_{117} i_{13}) n_{107}) (\land n_{117} n_{118})) \\ (\land (\oplus (\land n_{117} i_{13}) n_{107}) (\land n_{117} n_{118})) \\ (\land (\neg (\land norm_3 i_7)) (\neg i_4)) \\ \rightarrow (\oplus (\lor (\neg norm_3) (\lor (\neg i_7) i_4)) i_4) \\ (\land n_{121} (\neg (\land n_{116} (\neg n_{114})))) \\ \rightarrow (\oplus (\land n_{114} (\land n_{116} n_{121})) (\land n_{121} (\neg n_{116}))) \\ (\land n_{215} (\lor (n_{174} (\lor norm_3 norm_7))) \\ (\land (\land n_{215} (\lor (\lor n_{174} norm_3) norm_7)) \\ (\land (\land n_{449} (\land n_{440} n_{440}) n_{1206}) n_{444}) \\ \rightarrow (\land (\land n_{449} (\land n_{444} n_{440})) (\oplus n_{444} (\oplus n_{440} n_{1206}))) \\ (\land (\neg n_{290}) (\land norm_3 (\neg n_{278}))) \\ (\land (\land n_{297} (\land (\land n_{121} n_{120} n_{267})) \\ \rightarrow (\land (\land n_{155} (\land n_{98} n_{157}) (\land n_{155} n_{150})) \\ \rightarrow (\land (\land n_{155} (\land n_{98} n_{157}) (\oplus n_{150} (\oplus n_{155} n_{98}))) \\ (\land (\neg (\land norm_2 n_{893}) (\neg (\land n_{734}) p_{1020}) \\ (\land (\land norm_2 n_{893}) (\neg (\land n_{876} n_{824}))) \\ \rightarrow (\land (\oplus (\oplus (\land (\land n_{824} n_{893}) n_{876}) n_{893}) norm_2) \\ \end{cases}$$

 $(\land (\neg (\land n_{481} (\land n_{462} n_{457}))) pi_{082})$ $\rightarrow (\oplus (\land (\land pi_{082} n_{457}) n_{481}) n_{462}) pi_{082})$ $(\land (\land n_{379} (\neg (\oplus n_{371} n_{373}))) n_{376})$ $\rightarrow (\land (\oplus (\neg n_{371}) n_{373}) (\land n_{376} n_{379}))$ $(\land (\land n_{441} (\oplus norm_3 (\neg n_{218}))) n_{225})$ $\rightarrow (\land (\oplus (\neg norm_3) n_{218}) (\land n_{225} n_{441}))$ $(\oplus (\land n_{107} (\neg (\land n_{78} n_{63}))) n_{125})$ $\rightarrow (\oplus (\oplus (\land (\land n_{107} \ n_{63}) \ n_{78}) \ n_{125}) \ n_{107})$ $(\land n_{261} (\neg (\oplus n_{215} (\land n_{177} i_{23}))))$ $\rightarrow (\oplus (\land (\land i_{23} \ n_{261}) \ n_{177}) (\land (\neg n_{215}) \ n_{261}))$ $(\wedge n_{199} (\neg (\oplus (\wedge n_{195} norm_4) norm_4))))$ $\rightarrow (\oplus (\wedge norm_4 (\wedge (\neg n_{195}) n_{199})) n_{199})$ $(\land n_{101} (\neg (\land (\land n_{85} n_{78}) n_{74})))$ $\rightarrow (\oplus (\land (\land n_{74} n_{85}) (\land n_{78} n_{101})) n_{101})$ $(\land (\land (\neg n_{94}) (\neg norm_4)) (\neg n_{63}))$ $\rightarrow (\neg (\lor norm_4 (\lor n_{63} n_{94})))$ $(\land (\neg (\land n_{277} (\land n_{271} n_{232}))) i_{30})$ $\rightarrow (\oplus (\land (\land (\land i_{30} \ n_{277}) \ n_{271}) \ n_{232}) \ i_{30})$ $(\land (\land (\land n_{1344} n_{384}) (\neg norm_4)) norm_9)$ $\rightarrow (\land (\land norm_9 (\land n_{384} n_{1344})) (\oplus n_{1344} norm_4))$ $(\land norm_1 (\neg (\land (\land n_{158} n_{150}) n_{98})))$ $\rightarrow (\land norm_1 (\neg (\land n_{150} (\land n_{98} n_{158}))))$ $(\land (\land n_{177} (\land n_{171} n_{166})) (\neg n_{160}))$ $\rightarrow (\land (\oplus n_{160} (\lor n_{177} n_{171})) (\land n_{166} (\land n_{177} n_{171})))$ $(\land n_{131} (\land (\neg (\land n_{114} n_{107})) norm_4))$ $\rightarrow (\land (\oplus (\land (\land n_{107} \ n_{131}) \ n_{114}) \ n_{131}) \ norm_4)$ $(\land n_{327} (\oplus n_{325} (\neg (\land n_{254} n_{207}))))$ $\rightarrow (\oplus (\land (\land n_{327} \ n_{254}) \ n_{207}) (\land (\neg n_{325}) \ n_{327}))$ $(\land (\neg (\land n_{104} (\neg (\land n_{99} x_{11})))) x_{14})$

 $\rightarrow (\lor (\land (\land x_{14} x_{11}) n_{99}) (\land (\neg n_{104}) x_{14}))$ $(\land (\land (\neg (\land n_{104} (\neg norm_5))) x_{14}) x_{15})$ $\rightarrow (\land (\lor norm_5 (\neg n_{104})) (\land x_{15} x_{14}))$ $(\land n_{261} (\neg (\land (\neg (\land n_{254} n_{220})) n_{219})))$ $\rightarrow (\land (\oplus (\neg (\land (\land n_{220} \ n_{219}) \ n_{254})) \ n_{219}) \ n_{261})$ $(\land (\oplus (\land (\land y_{17} \ n_{222}) \ n_{236}) \ y_{17}) \ n_{273})$ $\rightarrow (\oplus (\land (\land (\land n_{273} \ n_{222}) \ y_{17}) \ n_{236}) (\land n_{273} \ y_{17}))$ $(\land (\land (\land n_{731} n_{447}) n_{445}) (\land n_{659} n_{440}))$ $\rightarrow (\land (\land (\land (\land n_{447} n_{445}) n_{440}) n_{659}) n_{731})$ $(\land (\land (\land n_{440} \ n_{459}) \ n_{658}) \ (\land n_{457} \ n_{458}))$ $\rightarrow (\land (\land (\land n_{458} \ n_{459}) \ n_{457}) (\land n_{440} \ n_{658}))$ $(\land (\land (\land n_{461} n_{478}) (\land n_{457} n_{458})) n_{447})$ $\rightarrow (\land (\land (\land n_{447} \ n_{478}) \ n_{458}) (\land n_{461} \ n_{457}))$ $(\land (\land (\land n_{480} n_{478}) (\land n_{462} n_{457})) n_{477})$ $\rightarrow (\land (\land (\land n_{478} \ n_{457}) \ n_{477}) (\land n_{480} \ n_{462}))$ $(\land (\land (\land (\land n_{437} n_{443}) n_{440}) n_{450}) n_{436})$ $\rightarrow (\land (\land (\land n_{436} n_{437}) n_{440}) n_{443}) n_{450})$ $(\land (\land (\land (\neg op_4) op_3) (\neg op_1)) op_0)$ $\rightarrow (\land (\neg (\lor op_4 op_1)) (\land op_0 op_3))$ $(\oplus (\oplus n_{610} n_{609}) (\land (\land n_{547} n_{322}) i_{24}))$ $\rightarrow (\oplus (\oplus (\land (\land i_{24} \ n_{322}) \ n_{547}) \ n_{610}) \ n_{609})$ $(\land (\neg (\oplus i_{15} (\neg i_7))) (\land norm_5 norm_{11}))$ $\rightarrow (\land (\land norm_{11} (\oplus i_{15} i_7)) norm_5)$ $(\land (\oplus (\oplus n_{286} (\land n_{269} n_{213})) n_{249}) n_{193})$ $\rightarrow (\oplus (\land (\oplus n_{286} n_{249}) n_{193}) (\land (\land n_{193} n_{213}) n_{269}))$ $(\land (\oplus n_{333} (\land (\land n_{329} norm_4) n_{317})) n_{151})$ $\rightarrow (\land (\oplus (\land norm_4 (\land n_{329} n_{317})) n_{333}) n_{151})$ $(\land (\land n_{158} (\land (\neg n_{148}) (\neg n_{136}))) n_{98})$ $\rightarrow (\land (\neg (\lor n_{148} \ n_{136})) (\land n_{98} \ n_{158}))$

 $(\neg (\land (\neg (\land n_{250} (\land n_{205} n_{162}))) n_{247}))$ $\rightarrow (\oplus (\neg (\land (\land n_{247} n_{205}) n_{250}) n_{162})) n_{247})$ $(\land (\land n_{230} \ n_{209}) \ (\land n_{193} \ (\land n_{152} \ n_{117})))$ $\rightarrow (\land (\land (\land n_{152} \ n_{230}) \ n_{117}) \ (\land n_{193} \ n_{209}))$ $(\lor (\land n_{177} \ norm_2) \ (\land n_{211} \ (\land n_{177} \ n_{182})))$ $\rightarrow (\lor (\land n_{177} \ norm_2) \ (\land n_{177} \ (\land n_{182} \ n_{211})))$ $(\land (\neg (\land (\neg (\land n_{356} n_{312})) n_{215})) n_{177})$ $\rightarrow (\oplus (\land (\land n_{177} \ n_{312}) (\land n_{356} \ n_{215})) (\land n_{177} \ (\neg n_{215})))$ $(\land (\land (\land (\land norm_4 n_{277}) n_{272}) n_{268}) n_{263})$ $\rightarrow (\land (\land n_{272} \ norm_4) \ (\land n_{263} \ (\land n_{277} \ n_{268})))$ $(\land (\neg (\land (\land n_{94} n_{76}) (\neg n_{72}))) n_{19})$ $\rightarrow (\oplus (\land n_{72} (\land n_{94} (\land n_{19} n_{76}))) (\land n_{19} (\neg (\land n_{76} n_{94}))))$ $(\land n_{69} (\neg (\land (\neg (\land n_{41} n_{23})) n_{21})))$ $\rightarrow (\land (\land n_{69} (\lor n_{23} (\neg n_{21}))) (\lor n_{41} (\neg n_{21})))$ $(\land (\land n_{149} \ n_{146}) \ (\neg (\land (\neg n_{141}) \ n_{36})))$ $\rightarrow (\oplus (\land n_{141} (\land n_{149} (\land n_{36} n_{146}))) (\land n_{149} (\land n_{146} (\neg n_{36}))))$ $(\land (\land (\land n_{461} \ n_{457}) \ (\land n_{730} \ n_{733})) \ pi_{002})$ $\rightarrow (\land (\land n_{730} (\land n_{457} n_{461})) (\land n_{733} (\land pi_{002} n_{461})))$ $(\land (\neg (\land n_{1596} (\neg (\land n_{1590} n_{1587})))) n_{825})$ $\rightarrow (\oplus (\land n_{1590} (\land n_{1587} (\land n_{825} n_{1596}))) (\land n_{825} (\neg n_{1596})))$ $(\land n_{958} (\neg (\land (\neg (\land n_{939} n_{936})) pi_{028})))$ $\to (\land (\land n_{958} (\lor n_{939} (\neg pi_{028}))) (\neg (\land pi_{028} (\oplus n_{939} n_{936}))))$ $(\wedge n_{305} (\oplus (\wedge (\oplus n_{264} norm_4) n_{255}) n_{255}))$ $\rightarrow (\land (\land n_{255} n_{305}) (\oplus n_{264} (\neg norm_4)))$ $(\land (\land i_{12} i_{11}) (\land (\land i_{11} i_{10}) i_{9}))$ $\rightarrow (\land (\land i_9 i_{11}) (\land i_{10} i_{12}))$ $(\land (\land (\land i_{10} i_9) (\land i_9 i_8)) i_{11})$ $\rightarrow (\land (\land i_{11} \ i_{10}) (\land i_9 \ i_8))$ $(\land (\land (\land (\land n_{108} n_{99}) n_{103}) norm_4) n_{110})$

 $\rightarrow (\land (\land (\land (\land n_{110} \ n_{108}) \ n_{99}) \ n_{103}) \ norm_4)$ $(\land (\land n_{65} (\land n_{157} n_{98})) (\land n_{155} n_{150}))$ $\rightarrow (\land (\land n_{155} (\land n_{157} (\land n_{65} n_{98}))) (\oplus n_{150} (\oplus n_{157} (\land n_{155} n_{65}))))$ $(\land norm_1 (\land (\land (\land n_{158} n_{150}) n_{98}) n_{65}))$ $\rightarrow (\land (\land n_{65} (\land n_{158} n_{98})) (\land n_{150} norm_1))$ $(\wedge n_{178} (\wedge n_{165} (\wedge n_{152} (\wedge n_{116} n_{103}))))$ $\rightarrow (\land (\land (\land n_{178} \ n_{103}) \ (\land n_{116} \ n_{152})) \ n_{165})$ $(\wedge n_{199} (\wedge (\wedge n_{192} n_{165}) (\wedge n_{152} n_{117})))$ $\rightarrow (\land (\land (\land n_{199} \ n_{192}) \ n_{152}) \ n_{117}) \ n_{165})$ $(\land n_{202} (\land norm_2 (\land n_{114} (\land n_{86} n_{74}))))$ $\rightarrow (\land (\land n_{86} (\land n_{114} n_{202})) (\land n_{74} norm_2))$ $(\land n_{140} (\land n_{136} (\land norm_3 (\land n_{89} n_{75}))))$ $\rightarrow (\land (\land n_{89} (\land n_{136} n_{140})) (\land n_{75} norm_3))$ $(\land (\oplus n_{333} (\land (\land n_{329} n_{326}) n_{317})) n_{151})$ $\rightarrow (\oplus (\land n_{151} (\land n_{326} (\land n_{329} n_{317}))) (\land n_{151} n_{333}))$ $(\oplus norm_1 (\land (\land (\land n_{251} n_{248}) n_{218}) n_{215}))$ $\rightarrow (\oplus (\land n_{215} (\land n_{248} (\land n_{251} n_{218}))) norm_1)$ $(\oplus (\land (\land n_{93} n_{92}) (\neg (\land n_{125} x_{25}))) n_{92})$ $\rightarrow (\oplus (\land (\land (\land n_{93} x_{25}) n_{92}) n_{125}) (\land (\neg n_{93}) n_{92}))$ $(\land (\oplus (\lor (\neg (\land n_{121} \ n_{116})) \ n_{114}) \ n_{121}) \ x_{23})$ $\rightarrow (\oplus (\land (\land x_{23} \ n_{116}) \ n_{121}) \ (\neg \ n_{114})) \ (\land (\neg \ n_{121}) \ x_{23}))$ $(\land (\neg (\land n_{121} (\neg (\land n_{116} (\neg n_{114}))))) x_{23})$ $\rightarrow (\land (\oplus (\lor (\neg (\land n_{121} \ n_{116})) \ n_{114}) \ n_{121}) \ x_{23})$ $(\land (\neg (\oplus (\land (\land x_{17} \ n_{95}) \ norm_5) \ x_{17})) \ n_{94})$ $\rightarrow (\oplus (\land (\land (\land n_{94} n_{95}) x_{17}) norm_5) (\land (\neg x_{17}) n_{94}))$ $(\land (\land (\land n_{169} \ n_{149}) \ n_{142}) \ (\neg (\land n_{108} \ x_{16})))$ $\rightarrow (\land (\oplus (\land (\land x_{16} \ n_{149}) \ n_{108}) \ n_{149}) (\land n_{169} \ n_{142}))$ $(\oplus (\land (\neg (\lor n_{175} n_{153})) (\land n_{19} n_{182})) n_{19})$ $\rightarrow (\oplus (\land (\land n_{19} \ n_{182}) \ (\neg \ n_{175})) \ (\neg \ n_{153})) \ n_{19})$

 $\rightarrow (\oplus (\lor (\oplus (\neg n_{187}) n_{199}) norm_3) n_{199})$ $(\land n_{218} (\neg (\land (\land n_{213} n_{212}) (\land n_{177} n_{85}))))$ $\rightarrow (\oplus (\land (\land (\land n_{212} \ n_{218}) \ n_{85}) \ (\land n_{177} \ n_{213})) \ n_{218})$ $(\land (\neg (\land (\land n_{261} \ n_{255}) \ (\land n_{237} \ n_{232}))) \ i_{28})$ $\rightarrow (\oplus (\land (\land (\land (\land i_{28} \ n_{261}) \ n_{237}) \ n_{255}) \ n_{232}) \ i_{28})$ $(\oplus (\land (\oplus n_{325} (\neg (\land n_{254} n_{207}))) n_{210}) i_{63})$ $\rightarrow (\oplus (\oplus (\land (\land n_{210} \ n_{254}) \ n_{207}) (\land (\neg n_{325}) \ n_{210})) \ i_{63})$ $(\oplus n_{198} (\neg (\land n_{194} (\land n_{174} (\land n_{114} n_{87})))))$ $\rightarrow (\oplus (\neg (\land (\land n_{114} n_{194}) n_{87}) n_{174})) n_{198})$ $(\land (\land n_{207} (\land n_{204} n_{192})) (\neg (\land n_{110} n_{184})))$ $\rightarrow (\land (\land n_{192} (\oplus n_{207} (\neg n_{204}))) (\oplus n_{207} (\land n_{110} (\land n_{184} n_{207}))))$ $(\land (\neg (\land (\land (\land n_{171} \ n_{134}) \ n_{132}) \ n_{112})) \ n_{130})$ $\rightarrow (\lor (\land n_{130} (\neg (\land n_{132} n_{171}))) (\land n_{130} (\neg (\land n_{134} n_{112}))))$ $(\lor (\land x_{17} (\neg n_{95})) (\land n_{107} (\land x_{17} x_{15})))$ $\rightarrow (\oplus (\land n_{107} (\land x_{15} (\land x_{17} n_{95}))) (\land x_{17} (\neg n_{95})))$ $(\land (\neg (\oplus (\oplus n_{102} (\land n_{87} n_{59})) n_{71})) i_{16})$ $\rightarrow (\oplus (\land n_{87} (\land i_{16} n_{59})) (\land i_{16} (\oplus n_{102} (\neg n_{71}))))$ $(\land (\neg (\land (\neg norm_4) (\neg (\land n_{156} n_{155})))) b_3)$ $\rightarrow (\oplus (\land norm_4 (\land n_{156} (\land b_3 n_{155}))) (\land b_3 (\oplus norm_4 (\land n_{156} n_{155}))))$ $(\land (\neg (\land (\land n_{459} \ n_{457}) \ (\land n_{730} \ n_{733}))) \ pi_{020})$ $\rightarrow (\lor (\oplus pi_{020} (\land n_{730} (\land pi_{020} n_{457}))) (\oplus pi_{020} (\land n_{733} (\land pi_{020} n_{459}))))$ $(\land (\neg (\land n_{1285} (\land (\land n_{436} n_{437}) n_{452}))) pi_{082})$ $\rightarrow (\lor (\oplus pi_{082} (\land n_{1285} (\land pi_{082} n_{437}))) (\oplus pi_{082} (\land n_{452} (\land pi_{082} n_{436}))))$ $(\land (\neg (\land n_{366} (\neg (\land n_{361} (\neg norm_6))))) n_{332})$ $\rightarrow (\land (\land n_{332} (\lor n_{361} (\neg n_{366}))) (\neg (\land n_{366} norm_6)))$ $(\land n_{263} (\oplus n_{245} (\neg (\land (\land n_{240} n_{230}) n_{226})))))$ $\rightarrow (\oplus (\land (\land n_{226} \ n_{240}) \ (\land n_{230} \ n_{263})) \ (\land n_{263} \ (\neg n_{245})))$ $(\land (\neg (\land norm_3 (\neg norm_8))) (\neg (\land n_{694} norm_8)))$ $\rightarrow (\oplus (\lor norm_8 norm_3) (\lor n_{694} (\neg norm_8)))$

$$\begin{array}{c} (\land (\neg (\land norm_{3} i_{1})) (\neg (\land (\neg i_{1}) i_{0}))) \\ \rightarrow (\oplus (\land i_{1} norm_{3}) (\lor i_{1} (\neg i_{0}))) \\ (\land (\oplus (\oplus na_{324} n_{291}) (\neg (\land n_{254} norm_{5}))) n_{215}) \\ \rightarrow (\oplus (\land norm_{5} (\land n_{215} n_{254})) (\land n_{215} (\oplus n_{291} (\neg n_{324})))) \\ (\land (\neg (\land (\land n_{135} (\neg n_{132})) (\neg n_{128}))) norm_{6}) \\ \rightarrow (\oplus (\land (\lor n_{132} n_{128}) (\land n_{135} norm_{6})) (\land (\neg n_{135} norm_{6})) \\ (\land (\neg (\land (\land n_{135} (\neg n_{132})) (\neg n_{128}))) norm_{6}) \\ \rightarrow (\oplus (\land (\lor n_{132} n_{128}) (\land n_{135} norm_{6})) (\land (\neg n_{135} norm_{6})) \\ (\land (\neg (\land (\land n_{135} (\neg op_{0}))) op_{4})) (\neg op_{1})) \\ \rightarrow (\oplus (\land (\lor op_{1} op_{0}) (\land op_{3} op_{4})) (\oplus op_{1} (\neg (\land op_{3} op_{4})))) \\ (\land (\neg (\land (\land (\land n_{10} (\neg o_{2}))) (\neg op_{1})) \\ \rightarrow (\oplus (\land oc_{2} (\land in_{1} (\neg oc_{1}))) (\lor (\land oc_{1} ct_{0}) (\land oc_{2} in_{1})))) \\ (\land (\neg (\land (\land (\land in_{10} (\neg oc_{2}))) oc_{3})) oc_{1}) \\ \rightarrow (\oplus (\oplus oc_{3} (\lor oc_{2} (\oplus oc_{1} oc_{3}))) (\oplus oc_{1} (\land oc_{3} (\neg in_{0})))) \\ (\land (\neg (\land (\land n_{5162} norm_{4}) (\land n_{5157} norm_{10}))) n_{264}) \\ \rightarrow (\land (\oplus (\land n_{5162} norm_{4}) (\land n_{5157} norm_{10}))) n_{264}) \\ \rightarrow (\lor (\land (\land n_{5162} norm_{4}) (\land n_{5157} norm_{10}))) n_{264}) \\ \rightarrow (\lor (\land (\land (\land n_{5166} n_{305}) n_{277} n_{263}) n_{305}) \\ (\neg (\land (\land (\land n_{306} n_{305}) (\land n_{277} n_{263})) n_{272}) n_{268}) \\ (\neg (\land (\land (\land n_{306} n_{305}) (\land n_{277} n_{263})) n_{272}) n_{268}) \\ (\neg (\land (\land (\land n_{306} n_{305}) n_{277} n_{272}) n_{268}) n_{305}) \\ (\neg (\land (\land (\land (\land n_{306} n_{305}) n_{277}) n_{273}) n_{268}) n_{305}) \\ (\neg (\land (\land (\land n_{306} n_{305}) n_{277}) n_{263}) n_{272}) n_{268}) \\ \rightarrow (\land (\land (\land n_{243} n_{248} n_{242}) n_{219}) n_{219}) n_{219}) \\ (\land (\land (\land (\land n_{306} n_{305}) (\land n_{277} n_{263})) n_{272}) n_{268}) \\ \rightarrow (\land (\land (\land n_{306} n_{305}) (\land n_{277} n_{263})) n_{272}) n_{268}) \\ (\land (\land (\land (\land n_{306} n_{305}) (\land n_{277} n_{263})) n_{272}) n_{268}) \\ \rightarrow (\land (\land (\land (\land n_{306} n_{277}) n_{268}) (\land (n_{263} n_{305})) n_{272}) n_{268}) \\ (\land (\land (\land (\land n_{306} n_{277}) n_{268}) (\land n_{263} n_{305})) n_{272}) n_{27}) n_{20}) \\ (\neg (\land (\land (\land (\land n_{306} n_{277}) n_{268}) (\land n_{277} n_{20})) n_{272}) n_{20})$$

$$\rightarrow (\oplus (\land (\land (\land n_{20} n_{296}) n_{285}) n_{271}) (\lor (\neg n_{296}) n_{201}) \\ (\neg (\land (\oplus (\lor (\land (\land n_{37} n_{267})) n_{254}) n_{267}) n_{46})) \\ (\land (\land (\land (\neg pi_{044}) (\neg pi_{042})) (\neg pi_{040})) (\neg pi_{038})) \\ \rightarrow (\oplus (\lor (\neg (\land (\land n_{46} n_{37}) n_{267})) n_{254}) (\land (\neg n_{267}) n_{46})) \\ (\land (\land (\land (\neg pi_{044}) (\neg pi_{042})) (\neg pi_{040})) (\neg pi_{038})) \\ \rightarrow (\neg (\lor (\lor pi_{038} pi_{042}) (\lor pi_{044} pi_{040}))) \\ (\neg (\land (\oplus (\lor (\neg (\land n_{821} n_{1032}) n_{1028}) n_{821}) n_{1041})) \\ (\neg (\land (\oplus (\lor (\land (\land n_{821} n_{1032}) n_{1041})) n_{1028}) (\land (\neg n_{821}) n_{1041})) \\ (\neg (\land (\oplus (\lor (\land (\land n_{825} n_{1533})) n_{1529}) n_{825}) n_{1539})) \\ (\land (\oplus (\oplus (\land (\land n_{825} n_{1539}) n_{1533})) n_{1529}) (\land (\neg n_{825}) n_{1539})) \\ (\land (\neg (\oplus (\oplus (\land (\lor pi_{058} pi_{116}) pi_{026})) pi_{058})) pi_{094}) \\ \rightarrow (\land (\land (\oplus pi_{026} pi_{058}) pi_{094}) (\lor pi_{116} pi_{058})) \\ (\land (\neg (\land n_{852} (\land (\land n_{848} n_{825}) pi_{026}))) (\neg n_{840})) \\ \rightarrow (\neg (\lor (\land (\land (\land n_{25} pi_{026}) n_{852}) n_{848}) n_{840})) \\ (\neg (\land (\land (\land n_{852} (\land (\land n_{848} n_{825}) pi_{026}))) (\neg n_{840})) \\ \rightarrow (\neg (\lor (\land (\land (\land n_{25} pi_{026}) n_{352}) n_{458}) n_{840})) \\ (\neg (\land (\land (\land n_{252} op_{11})) (\lor op_{33} op_{4})) op_{2}) \\ (\neg (\lor (\land (\land (\land n_{252} op_{11})) (\lor op_{33} op_{4})) op_{2}) \\ (\neg (\lor (\land (\land (\neg op_{2} op_{11})) (\lor op_{3} op_{4})) op_{2}) \\ (\neg (\lor (\land (\oplus n_{217} n_{168})) (\land (\land n_{227} n_{322}) i_{24})) n_{686}) \\ \rightarrow (\oplus (\oplus (\oplus (\land (\land n_{135} n_{132}) n_{129}) n_{686}) n_{690}) \\ (\land (\land (\land (n_{135} n_{133}) n_{129})) norm_{6} n_{47})) \\ \rightarrow (\lor (\land (\land n_{135} n_{133}) n_{129})) norm_{6} n_{47})) \\ \rightarrow (\lor (\land (\land n_{135} (\neg n_{133}) n_{129})) norm_{6} n_{47})) \\ \rightarrow (\oplus (\land (\land n_{135} (\neg n_{132}) (\neg n_{128})) n_{281}) \\ \land (\oplus (\land (\oplus n_{269} n_{248}) (\land n_{281} n_{213})) n_{281}) \\ \land (\oplus (\land (\oplus n_{269} n_{248}) (\land n_{281} n_{213})) (\land n_{281} n_{286}))) \\ (\land n_{251} (\neg (\oplus (\land nrom_{5} n_{143})) (\land nrom_{5} i_{25}))))) \\ \rightarrow (\oplus (\land (\land (\oplus i_{25} n_{143}) n_{251}) norm_{5}) (\land (\neg n_{143}) n_{251})))$$

$$(\land (\oplus (\oplus n_{359} (\oplus n_{272} (\land n_{253} n_{215}))) n_{186}) n_{157})$$

$$\rightarrow (\oplus (\land (\oplus (\oplus n_{186} n_{272}) n_{359}) n_{157}) (\land (\land n_{157} n_{215}) n_{253}))$$

$$(\land (\neg (\land (\land norm_4 i_6) i_7)) (\neg (\land i_6 i_5)))$$

$$\rightarrow (\oplus (\lor (\lor (\neg i_6) i_5) (\land norm_4 i_7)) i_6)$$

$$(\land (\oplus (\land (\land i_6 i_8) i_7) i_4) (\land i_7 i_6))$$

$$\rightarrow (\land (\land (\oplus i_8 i_4) i_6) i_7)$$

$$(\land (\land n_{121} (\neg (\land (\neg n_{115}) n_{34}))) (\land norm_6 i_3))$$

$$\rightarrow (\land (\land (\land i_3 n_{121}) norm_6) (\lor (\neg n_{34}) n_{115}))$$

$$(\land (\neg (\oplus n_{245} (\neg (\land (\land n_{240} n_{230}) n_{226})))) n_{236})$$

$$\rightarrow (\oplus (\land (\land (\land n_{230} n_{236}) n_{240}) n_{226}) (\land n_{236} n_{245}))$$

$$(\land (\land (\land n_{132} (\land n_{95} n_{80})) (\land n_{114} n_{99})))$$

$$\rightarrow (\land (\land (\land n_{158} (\land (\neg n_{148}) (\neg n_{130} n_{95})) n_{80})$$

$$(\land (\land (\land n_{158} (\land (\neg n_{148}) (\neg n_{130}))) n_{98}) n_{65})$$

$$\rightarrow (\land (\land (\neg (\neg n_{180} n_{148})) (\land n_{65} n_{98})) n_{158})$$

$$(\land (\land (\land n_{158} (\land (\neg n_{148}) (\neg n_{136}))) n_{98}) n_{65})$$

$$\rightarrow (\land (\land (\land n_{192} n_{165}) (\land n_{152} (\land n_{116} n_{103}))))$$

$$\rightarrow (\oplus (\land (\land (n_{199} n_{192}) n_{165}) (\land n_{116} n_{103}))))$$

$$\rightarrow (\land (\land (\land n_{199} n_{192}) n_{103}) (\land n_{116} n_{152})) n_{165})$$

$$(\land (\land (\land n_{180} n_{86}) n_{74}) (\land n_{114} n_{173}) n_{142})$$

$$(\neg (\lor (\land n_{173} n_{142}) (\land n_{114} (\land n_{86} n_{74}))))$$

$$\rightarrow (\oplus (\land n_{107} (\land x_{15} (\land x_{17} n_{95}))) (\lor n_{077} (\land x_{17} x_{15}))))$$

$$\rightarrow (\oplus (\land norm_5 (\lor i_{21} (\neg n_{272}))) (\lor norm_5 (\land n_{272} (\neg n_{120}))))$$

$$(\land (\land (\neg m_{349} (\land n_{374} (\land n_{46} n_{356}))) (\lor n_{46} (\neg n_{374})))$$

$$(\land (\oplus (\land norm_4 (\land 4_{45} n_{356}))) (\lor n_{46} (\neg n_{374})))$$

$$(\oplus (\land norm_4 (\land 4_{45} n_{356}))) (\lor n_{46} (\neg n_{374})))$$

$$(\Rightarrow (\oplus (\land norm_4 (\land 4_{45} n_{356}))) (\lor (\wedge (\land (\land n_{744} n_{457}) n_{461}) p_{1002})))$$

 $\rightarrow (\lor (\neg (\lor n_{741} (\land pi_{002} n_{457}))) (\neg (\lor n_{741} (\land n_{734} n_{461}))))$ $(\land (\land (\land (\neg pi_{044}) (\neg pi_{042})) (\neg pi_{040})) (\neg pi_{038}))$ $\rightarrow (\land (\neg (\lor pi_{038} pi_{044})) (\neg (\lor pi_{042} pi_{040})))$ $(\land (\neg (\land (\land (\neg pi_{052}) (\neg pi_{051})) (\neg pi_{039}))) pi_{027})$ $\rightarrow (\oplus (\land pi_{027} (\oplus pi_{051} (\lor pi_{052} pi_{039}))) (\land (\lor pi_{052} pi_{039}) (\land pi_{027} pi_{051})))$ $(\land (\land (\neg n_{955}) n_{825}) (\neg (\land (\land n_{868} n_{821}) pi_{116})))$ $\rightarrow (\land (\oplus n_{955} (\lor n_{825} (\oplus pi_{116} (\neg n_{868})))) (\oplus n_{825} (\land n_{868} (\land pi_{116} (\land n_{821})))))$ $n_{825}))))))$ $(\land (\land (\neg (\land n_{753} n_{751})) n_{748}) (\neg (\land n_{744} pi_{082})))$ $\rightarrow (\land (\oplus n_{748} (\land n_{751} (\land n_{753} n_{748}))) (\oplus n_{748} (\land n_{744} (\land pi_{082} n_{748}))))$ $(\land (\land i_4 i_3) (\land (\land i_5 i_4) (\land i_6 i_5)))$ $\rightarrow (\land (\land i_5 i_6) (\land i_4 i_3))$ $(\land (\land i_{24} i_{23}) (\land (\land i_{26} i_{25}) (\land i_{25} i_{24})))$ $\rightarrow (\land (\land i_{24} \ i_{25}) \ (\land i_{26} \ i_{23}))$ $(\land (\land (\land (\land n_{80} n_{95}) (\land n_{104} n_{116})) n_{131}) n_{118})$ $\rightarrow (\land (\land n_{118} (\land n_{80} n_{131})) (\land n_{116} (\land n_{104} n_{95})))$ $(\land (\land n_{156} \ n_{138}) \ (\neg (\land (\neg (\land n_{151} \ n_{65})) \ i_{18})))$ $\rightarrow (\land (\land n_{156} (\land n_{138} (\lor n_{65} (\neg i_{18})))) (\oplus (\land n_{156} n_{138}) (\land i_{18} (\neg n_{151}))))$ $(\land (\land n_{150} (\land n_{65} (\land n_{158} n_{98}))) (\oplus n_{65} norm_5))$ $\rightarrow (\land (\land n_{150} (\land n_{158} (\land n_{65} n_{98}))) (\oplus n_{98} norm_5))$ $(\land n_{184} (\land (\land (\land n_{178} n_{103}) (\land n_{116} n_{152})) n_{165}))$ $\rightarrow (\land (\land (\land (\land n_{178} \ n_{184}) \ n_{165}) (\land n_{103} \ n_{116})) \ n_{152})$ $(\land (\neg (\land (\neg i_3) (\neg i_2))) (\neg (\land i_4 i_3)))$ $\rightarrow (\oplus (\land i_3 i_4) (\lor i_3 i_2))$ $(\land (\neg (\land (\neg i_1) (\neg i_0))) (\neg (\land i_2 i_1)))$ $\rightarrow (\oplus (\lor (\oplus i_2 i_0) i_1) i_2)$ $(\land (\neg (\oplus i_{15} (\neg i_7))) (\land (\land i_{17} i_9) norm_6))$ $\rightarrow (\land (\land i_{17} norm_6) (\land i_9 (\oplus i_{15} i_7)))$ $(\land (\neg (\land (\land n_{120} (\neg n_{111})) (\neg n_{105}))) (\neg n_{93}))$

$$\rightarrow (\oplus (\vee n_{93} (\neg (\vee n_{111} n_{105}))) (\vee (\vee n_{93} n_{111}) (\vee n_{120} n_{105}))) \\ (\land n_{180} (\land (\land n_{173} n_{142}) (\land n_{114} (\land n_{86} n_{74})))) \\ \rightarrow (\land (\land n_{86} (\land n_{173} n_{180})) (\land n_{74} (\land n_{114} n_{142}))) \\ (\land n_{144} (\land n_{136} (\land (\land n_{120} n_{104}) (\land n_{89} n_{75})))) \\ \rightarrow (\land (\land n_{75} (\land n_{120} n_{144})) (\land n_{89} (\land n_{104} n_{136}))) \\ (\land (\neg (\vee (\neg (\vee op_4 op_1))) (\oplus op_3 op_4))) op_2) \\ \rightarrow (\land (\land n_{75} (\land n_{120} n_{144}) (\land n_{89} (\land op_1 (\neg op_4)))) \\ (\neg (\land (\neg (\land (\land (\neg op_4) (\neg op_3)) op_1)) op_2)) \\ \rightarrow (\oplus (\land (\vee op_3 op_4) (\land op_2 op_1)) (\vee op_1 (\neg op_2))) \\ (\land (\neg (\land (\land (\land n_{96} n_{279} op_4))) (\lor op_1 (\neg op_2))) \\ (\land n_{437} (\neg (\land (\neg (\land n_{1037} (\neg n_{1018})) n_{437}) \\ (\land (\neg (\land (\land n_{1036} n_{279} op_4))) (\oplus n_{22} (\land n_{306} (\land n_{22} n_{279}))))) \\ (\land (\neg (\land (\neg (\land n_{1037} (\neg n_{1018}))) n_{23})) (\neg n_{983})) \\ \rightarrow (\land (\neg (\vee n_{983} (\land n_{23} (\neg n_{1037})))) (\neg (\land n_{1018} n_{23}))) \\ (\land (\neg (\land (\neg (\land n_{264} n_{188})) (\lor norm_6) n_{188})) n_{264}) \\ \rightarrow (\oplus (\neg (\land n_{264} n_{188})) (\lor norm_6 (\land n_{203} (\neg n_{264}))))) \\ (\land (\neg (\land (\land n_{150} (\neg (\land (\neg n_{140} n_{130})) n_{36}))) n_{21}) \\ \rightarrow (\land (\vee ((\land (\neg n_{110} n_{100}) n_{140}) n_{36}) (\neg n_{150})) n_{21}) \\ (\land (\neg (\land (\land (\neg p_{1109} (\neg p_{1095})) (\neg p_{110})) \\ (\land (\neg (\land (\land (\neg p_{1109} p_{1095})) (\neg p_{110})) n_{40}) \\ (\land ((\land (\land (\neg p_{1109} p_{1095})) (\neg p_{116})) p_{110}) \\ (\land ((\land (\land (\neg p_{1109} (\neg p_{1095})) (\neg p_{110})) n_{41}) \\ \rightarrow ((\vee ((\land (\neg p_{1109} p_{1029}) p_{105})) (\neg n_{245}))) n_{44}) \\ \rightarrow ((\vee (\land (\land n_{44} op_0) n_{254}) norm_6) (\land n_{44} n_{245})) \\ (\neg (\land ((\land (\neg n_{244} n_{00}) n_{254}) norm_6) (\land n_{244} n_{955}))) \\ (\neg (\land ((\land (\neg n_{244} n_{010}) n_{396}) (\neg (n_{245})))) n_{44}) \\ \rightarrow (\land ((\vee ((\land n_{244} n_{00}) n_{254}) norm_6) (\land n_{44} n_{245})) \\ (\neg (\land ((\land (\neg n_{244} n_{00}) n_{254}) norm_6) (\land n_{44} n_{245})) \\ (\neg (\land ((\land (\neg n_{244} n_{00}) n_{254}) norm_6) (\land n_{44} n_{245})) \\ (\neg (\land ((\land (\neg n_{244} n_{00}) n_{254}) norm_6) (\land n_{44} n_{255})) (\neg (\land ((\land (\neg n_{244} n_{00}) n_{256}) (\neg (\land n_{244} n_{255}))) (\neg ((\land ((\land n_{244} n_{0$$

$$(\neg (\land (\neg (\land s_{0} (\neg a_{109}))) (\neg (\land (\neg s_{0} norm_{8})))) \rightarrow (\oplus (\lor (\oplus norm_{8} a_{109}) s_{0} a_{109}) (\land (\neg (\land (\neg s_{0}) (\neg a_{108}))) (\neg (\land s_{0} (\neg a_{107})))) \rightarrow (\oplus (\land (\oplus a_{107} a_{108}) s_{0}) a_{108}) (\land (\land (\neg (\land norm_{4} n_{333})) n_{150}) (\neg (\land (\neg norm_{4}) n_{336}))) \rightarrow (\land (\oplus (\lor (\oplus (\neg n_{336}) n_{333}) norm_{4}) n_{333}) n_{150}) (\land (\land (\land n_{251} (\oplus n_{247} (\neg n_{237}))) n_{218}) (\land n_{214} n_{207})) \rightarrow (\land (\land (\land (\land n_{251} n_{218}) n_{207}) (\oplus (\neg n_{247}) n_{237})) n_{214}) (\land (\land n_{251} (\oplus n_{247} (\neg (\land (\land n_{235} n_{232}) n_{222})))) n_{218}) \rightarrow (\land (\land (\oplus (\neg (\land (\land n_{235} n_{222}) n_{232})) n_{247}) n_{218}) n_{251}) (\land n_{80} (\land (\neg (\oplus (\oplus (\oplus n_{48} n_{45}) i_{2}) i_{10})) n_{53})) \rightarrow (\land (\oplus (\oplus (\oplus n_{199} (\land n_{188} n_{152})) (\land (n_{153} n_{80}))) (\land (\neg (\oplus (\oplus n_{199} (\land n_{188} n_{152})) (\land (n_{175} n_{152}))) n_{138}) \rightarrow (\oplus (\land (\oplus n_{188} n_{175}) (\land n_{138} n_{152})) (\land (\neg n_{199}) n_{138})) (\land (\neg (\land (\neg (\land (\oplus i_{4} i_{8}) i_{7}) (\lor i_{4} i_{9}))) i_{8}) (\neg (\land (\neg norm_{3}) (\neg (\land (\land (\land n_{308} n_{272} n_{268}) n_{263}))))) \rightarrow (\lor norm_{3} (\land n_{263} (\land n_{308} (\land n_{272} n_{268})))) (\land (\neg (\land (\neg (\land n_{121} (\neg (\land n_{116} (\neg n_{114}))))) x_{23} x_{24}) \rightarrow (\land (\land m_{232} (\neg (n_{114} n_{121}))) (\land x_{24} (\lor n_{116} (\neg n_{121})))) (\land (\land (\land (\land n_{272} (\land n_{305} (\land n_{277} n_{268}))))) (\land (\land (\land (\land n_{272} (\land n_{305} n_{306})) (\land n_{263} (\land n_{277} n_{268}))))) (\land (\land (\land (\land n_{127} (\land n_{272} n_{306}))) (\land (\land n_{305} (\land n_{268} n_{263}))))) (\land (\land (\land (\land n_{114} n_{121}))) (\land x_{24} (\lor n_{116} (\neg n_{121}))))) (\land (\land (\land (\land n_{114} n_{121})) (\land n_{263} (\land n_{268} n_{263}))))) (\land (\land (\land (\land n_{112} n_{40} n_{169} n_{142}) (\land n_{162} (\oplus n_{122} n_{112})))) (\land (\land (\land (\land n_{112} n_{149} n_{142}) (\land n_{135} (\oplus n_{122} n_{112})))) (\land (\land (\land (\land n_{112} n_{149} n_{142}) (\land n_{169} n_{132} n_{112}))))$$

 $\rightarrow (\lor (\land (\oplus n_{207} n_{197}) (\land n_{37} n_{58})) (\oplus n_{37} (\land norm_4 (\land n_{37} n_{207}))))$ $(\land n_{68} (\neg (\lor (\land norm_4 (\land n_{46} b_3)) (\land n_{46} n_{60})))))$ $\rightarrow (\land (\oplus n_{46} (\lor n_{60} (\oplus n_{46} n_{68}))) (\oplus n_{68} (\land norm_4 (\land n_{46} b_3))))$ $(\land (\land (\neg (\land norm_4 n_{86})) n_{132}) (\neg (\land norm_{11} (\neg n_{123}))))$ $\rightarrow (\land (\lor n_{123} (\neg norm_{11})) (\oplus n_{132} (\land norm_4 (\land n_{86} n_{132}))))$ $(\neg (\lor (\neg (\lor n_{741} \ norm_4)) (\neg (\lor n_{741} \ (\land n_{734} \ n_{461})))))$ $\rightarrow (\lor n_{741} (\land n_{734} (\land n_{461} norm_4)))$ $(\neg (\land (\land n_{821} (\lor n_{820} (\neg n_{1610}))) (\neg (\land n_{1610} n_{1604}))))$ $\rightarrow (\oplus (\land n_{1604} (\land n_{1610} (\land n_{821} n_{820}))) (\lor (\neg n_{821}) (\land n_{1610} (\neg n_{820}))))$ $(\land (\neg (\oplus (\land n_{481} (\land n_{462} (\land pi_{082} n_{457}))) pi_{082})) n_{468})$ $\rightarrow (\land (\oplus n_{468} (\land (\neg n_{481}) (\land n_{468} pi_{082}))) (\oplus pi_{082} (\neg (\land n_{462} (\land n_{457} pi_{082})))))$ $(\land (\neg (\lor (\land n_{856} (\land n_{818} n_{820})) (\land n_{818} norm_6))) n_{819})$ $\rightarrow (\land (\oplus n_{819} (\land norm_6 (\land n_{819} n_{818}))) (\oplus n_{819} (\land n_{856} (\land n_{820} n_{818}))))$ $(\land (\neg (\lor (\neg (\lor pi_{058} pi_{116})) (\oplus pi_{026} (\neg pi_{058})))) pi_{094})$ $\rightarrow (\land (\land pi_{094} (\oplus pi_{026} pi_{058})) (\lor pi_{116} (\oplus pi_{094} pi_{026})))$ $(\land (\neg (\land (\neg (\land n_{950} n_{916})) (\neg (\land n_{904} n_{846})))) n_{820})$ $\rightarrow (\lor (\land n_{950} (\land n_{820} n_{916})) (\land n_{846} (\land n_{820} n_{904})))$ $(\land (\land n_{65} \ norm_2) \ (\land n_{150} \ (\neg (\land n_{177} \ (\land n_{171} \ n_{166})))))$ $\rightarrow (\land (\land n_{150} (\land n_{65} norm_2)) (\oplus n_{65} (\land n_{166} (\land n_{171} n_{177}))))$ $(\neg (\land (\land (\land n_{104} (\land n_{110} n_{108})) (\land n_{95} n_{80})) n_{111}))$ $\rightarrow (\lor (\oplus n_{104} (\lor n_{80} (\oplus n_{108} n_{104}))) (\neg (\land n_{95} (\land n_{111} (\land n_{108} n_{110})))))$ $(\land n_{149} (\neg (\land (\neg (\land n_{133} n_{130})) (\neg (\land n_{127} n_{117})))))$ $\rightarrow (\oplus (\land (\land n_{117} \ n_{130}) \ (\land n_{149} \ (\land n_{127} \ n_{133}))) \ (\land n_{149} \ (\oplus \ (\land n_{117} \ n_{127}) \ (\land n_{149} \ (\land n_{149} \ n_{149} \ (\land n_{149} \ n_{149} \ (\land n_{149} \ n_{149} \ n_{149} \ n_{149} \ (\land n_{149} \ n_{149}$ $n_{133} n_{130}))))$ $(\land (\land (\neg n_{90}) (\neg (\land norm_5 n_{58}))) (\land n_{55} (\neg n_{48})))$ $\rightarrow (\land (\neg (\lor n_{48} n_{90})) (\oplus n_{55} (\land norm_5 (\land n_{55} n_{58}))))$ $(\neg (\land (\land n_{58} (\lor n_{63} (\neg n_{84}))) (\neg (\land n_{84} norm_7))))$ $\rightarrow (\oplus (\land norm_7 (\land n_{63} (\land n_{84} n_{58}))) (\lor (\neg n_{58}) (\land n_{84} (\neg n_{63}))))$ $(\land (\land n_{110} \ n_{108}) \ (\neg (\land (\neg (\land (\neg n_{101}) \ op_1)) \ n_{53})))$

$$\rightarrow (\land (\land n_{108} (\lor op_{1} (\oplus n_{53} n_{110}))) (\oplus n_{110} (\land n_{101} (\land n_{53} n_{110})))) (\land (\neg (n_{983} (\land n_{23} (\neg n_{1037})))) (\neg (\land n_{1018} n_{23}))) \rightarrow (\land (\neg n_{983}) (\lor (\neg n_{23}) (\land n_{1037} (\neg n_{1018})))) (\land (\neg (\land (\neg (\land in_{1} in_{0})) oc_{3})) (\neg (\land in_{0} oc_{0}))) \rightarrow (\oplus (\land (\lor oc_{0} in_{1}) (\land in_{0} oc_{3})) (\oplus oc_{3} (\neg (\land oc_{0} in_{0})))) (\land n_{199} (\neg (\oplus (\land n_{195} (\oplus norm_{5} n_{189}))) (\oplus norm_{5} n_{189})))) \rightarrow (\oplus (\land norm_{5} (\land n_{199} (\neg n_{195}))) (\land n_{199} (\lor n_{195} (\neg n_{189})))) (\land (\land (\land n_{251} (\oplus n_{247} (\neg norm_{5}))) n_{218}) (\land n_{214} n_{207})) \rightarrow (\land (\oplus n_{247} (\oplus n_{251} norm_{5})) (\land n_{207} (\land n_{214} (\land n_{251} n_{218})))) (\neg (\land (\neg (\land (\neg s_{0} (\neg a_{30}))) (\neg (\land s_{0} norm_{8}))))) (\oplus (\land a_{78} (\neg s_{0})) (\land (\land s_{0} a_{77})) (\land (\oplus (\land s_{0} (\neg a_{77}))) (\neg (\land (\neg s_{0} norm_{8}))))) (\oplus (\land a_{78} (\neg s_{0})) (\land s_{0} a_{77})) (\land (\neg (\land norm_{3} (\land b_{4} s_{3}))) (\neg (\land (\neg b_{6}) (\neg b_{4}))))) (\oplus (\land norm_{3} (\land b_{4} b_{3}))) (\neg (\land (\neg b_{6}) (\neg b_{4}))))) (\oplus (\land norm_{3} (\land b_{4} b_{3}))) (\neg (\land (\neg b_{6}) (\neg b_{4}))))) (\oplus (\land (\neg (n_{346}) (\neg (\land (\neg n_{342}) (\neg n_{334}))))) n_{305}))) (\oplus ((\land (\neg n_{346}) (\neg (\land (\neg n_{341}) (\neg norm_{3})))) n_{305}))) (\oplus ((\land (\neg n_{346}) (\neg (\land (\neg n_{341}) (\neg norm_{3})))) n_{305}))) (\oplus ((\land (\neg n_{360}) n_{400}) (\lor (\land (\neg n_{301}) n_{305}))) (\oplus ((\land (\neg n_{360}) (\neg (n_{11}) (\neg oc_{2}))))) (\land ((\neg (\land n_{303}) (\neg (n_{11}) (\neg oc_{2}))))) (\oplus ((\land ((\oplus (m_{15} a_{16}) s_{0})) a_{16}) (\land ((\oplus (\oplus (n_{101})))) (\neg (n_{105})))) (\oplus (() ((\oplus (m_{203} (\neg (\oplus n_{200} n_{197}))) (\oplus (n_{200} n_{197}))) n_{188})) (\land ((\oplus (\oplus (\neg n_{300}) n_{185})))$$

$$\begin{array}{l} (\land n_{388} (\land n_{359} (\oplus (\oplus n_{271} (\land n_{262} n_{215})) (\land n_{253} n_{215})))) \\ \rightarrow (\land (\oplus (\land (\oplus n_{253} n_{262}) n_{215}) n_{271}) (\land n_{359} n_{388})) \\ (\land (\land (\land n_{52} (\neg n_{47})) (\neg (\land (\land n_{30} n_{26}) n_{23}))) n_{22}) \\ \rightarrow (\land (\neg (\lor (\land (\land n_{23} n_{26}) n_{30}) n_{47})) (\land n_{22} n_{52})) \\ (\land (\land (\neg (\land (\land (\neg n_{9}) i_{3})) i_{7}) (\neg (\land i_{9} (\neg i_{3})))) \\ \rightarrow (\land (\oplus (\neg (\land (\land (\oplus n_{133} n_{127}) n_{104}) (\land n_{22} n_{52}))) \\ (\land (\land (\neg (\neg (\land (\land n_{102} n_{133}) n_{116}) n_{104}) n_{96})) n_{133})) \\ \rightarrow (\oplus (\land (\land (\oplus n_{133} n_{127}) n_{104}) (\land n_{116} n_{96})) n_{133}) \\ \rightarrow (\oplus (\land (\land (\land n_{102} n_{131}) n_{55}) n_{151}) (\neg (\lor n_{115} n_{164}))) \\ (\land (\land (\land (\land n_{102} n_{131}) n_{55}) n_{151}) (\neg (\lor n_{115} n_{164}))) \\ (\land (\lor (\land n_{177} (\land n_{92} (\neg (\land n_{93} n_{127})))) (\land n_{211} \land n_{177} n_{182}))) \\ (\land (\land n_{23} (\neg (n_{114} n_{121}))) (\land n_{24} (\lor n_{116} (\neg n_{121})))) \\ (\land (\land n_{23} (\neg (n_{114} (\land n_{112} n_{23}))) (\land n_{211} (\land n_{177} n_{182}))) \\ (\land (\land n_{23} (\land n_{114} (\land n_{116} n_{121})) (\land n_{121} (\neg n_{16}))) n_{23}) \\ \rightarrow (\oplus (\land n_{114} (\land n_{126} n_{120})) (\land n_{24} (\lor n_{116} (\neg n_{121})))) \\ (\land (\neg (\oplus (\land n_{114} (\land n_{116} n_{121})) (\land n_{24} (\neg (n_{116} (\neg n_{121}))))) \\ (\land (\neg (\neg (n_{175} (\oplus (\land n_{126} (\land n_{92} n_{33})) (\land n_{92} (\neg n_{93}))))) \\ (\land (\neg (\land n_{252} (\neg (\land n_{248} (\neg (n_{243} (\neg n_{241})))))) (\land n_{252} (\neg (\land n_{243} (\neg n_{243}))))) \\ (\land (\land (\land n_{220} (\lor n_{248} (\land n_{243} n_{248})) (\land n_{248} (\neg n_{243}))))) \\ (\land (\land (\land (\neg (\oplus m_{241} (\land n_{243} (\land n_{248} n_{252}))) (\land n_{252} (\neg (n_{252}))))) \\ (\land (\land (\land (\neg (\oplus m_{94} n_{873}) p_{1097})) n_{852}) (\neg (\land n_{977} n_{873})))))) \\ (\land (\land (\neg (\land (n_{994} n_{873}) p_{1097})) n_{852}) (\neg (\land n_{974} n_{873})))))) \\ (\land (\land (\neg n_{1001} n_{892}) (\neg (\land (\neg (\neg n_{995}) n_{873}))))) \\ (\land (\land (\neg n_{1001} n_{892}) (\neg (\land (\neg (\neg n_{1001} (\land p_{1097} n_{873})))))) \\ (\land (\land (\neg n_{1001} n_{892}) (\neg (\land (\neg (\neg (\neg n_{1077} n_{1629}))))) \\ (\land (\land (\neg n_{1001} n_{892}) (\neg (\land (\neg (\neg (\neg (\neg n_{1077} n_{1029})))))) \\ (\land (\land (\neg n_{1001} n_{892}) (\neg (\land (\neg (\neg (\neg (\neg (\neg (\neg n_{1$$

$$\begin{array}{l} \rightarrow (\land (\lor (\neg n_{1184}) norm_5) (\land n_{1192} (\lor n_{1172} (\neg n_{446})))) \\ (\land (\land n_{1469} (\neg (\land n_{1463} (\neg (\land (\neg n_{1179}) pi_{082}))))) (\neg n_{1458})) \\ \rightarrow (\land (\neg (\lor n_{1458} (\land n_{1463} n_{1179}))) (\land n_{1469} (\lor pi_{082} (\neg n_{1463})))) \\ (\land (\neg (\oplus (\land n_{1452} (\land n_{1449} n_{468})) (\land n_{1449} (\neg n_{468})))) n_{447}) \\ \rightarrow (\oplus (\land n_{1452} (\land n_{1449} (\land n_{447} n_{468}))) (\land n_{447} (\lor n_{468} (\neg n_{1449})))) \\ (\land (\land (\land n_{452} (\land n_{1449} (\land n_{477} n_{468}))) (\land n_{447} (\lor n_{466} (\neg n_{1449})))) \\ (\land (\land (\land n_{453} (\land n_{461} n_{478})) (\land n_{440} (\land n_{457} n_{658}))) n_{449}) \\ \rightarrow (\land (\land n_{461} (\land n_{658} (\land n_{478} n_{455}))) (\land n_{449} (\land n_{440} n_{457}))) \\ (\neg (\land n_{1539} (\neg (\land (\neg (\land n_{1533} (\neg (\land n_{1528} n_{818})))) n_{825}))))) \\ (\land (\land n_{1539} (\neg (\land (\neg (\land n_{1533} (\neg (\land n_{1538}) (\land n_{825} (\neg n_{1533}))))) \\ (\land (\neg (\land n_{1539} (\neg (\land (\neg (\land n_{1528} (\land n_{825} n_{818}))) (\land (\land n_{825} (\neg n_{1533}))))) \\ (\land (\land n_{1539} (\neg (\lor (\land n_{1528} (\land n_{825} n_{818}))) (\land n_{825} (\neg n_{1533}))))) \\ (\land (\land (\land (\neg (\land n_{1533} (\oplus n_{825} n_{1539})))) (\ominus n_{1529} (\land n_{1528} (\land n_{825} n_{818}))))) \\ (\land (\land (\land (\neg (\land n_{1533} (\oplus n_{825} n_{1539})))) (\Rightarrow (n_{1529} (\land n_{1528} (\land n_{825} n_{818}))))) \\ (\land (\land (\land (\neg (\land n_{1533} (\oplus n_{825} n_{1539})))) (\Rightarrow (n_{1529} (\land n_{1528} (\land n_{825} n_{818}))))) \\ (\land (\land (\land (\neg (\land n_{1533} (\oplus n_{825} n_{1539})))) (\Rightarrow (\land n_{1529} (\land n_{1528} (\land n_{825} n_{818}))))) \\ (\land (\land (\land (\neg n_{1533} (\oplus n_{825} n_{1539})))) (\Rightarrow (\land n_{1529} (\land n_{1629} n_{161})))) \\ (\land (\land (\land (\neg n_{109} i_{22}) (\neg n_{233}) n_{109}) (\neg (\land n_{171} n_{0771}n_{0771})))) \\ (\land (\land (\land (\neg n_{109} i_{22}) (\neg n_{233}) n_{109}) (\neg (\land n_{233} n_{111}))) \\ \rightarrow (\oplus (\land (\land n_{114} n_{107} n_{106}) (\land n_{104} (\land n_{138} n_{139}))))) \\ (\land (\land (\land n_{118} (\land n_{03} n_{139}) (\land n_{120} (\land n_{144} n_{171} n_{114} n_{127})))) \\ (\land (\land (\land n_{118} (\land n_{013} n_{139}) (\land n_{120} (\land n_{144} n_{131} n_{118}))))) \\ (\land (\land (\land n_{118} (\land n_{139} n_{133}) n_{179}) (\land n_{144} (\land n_{131} n_{118}))))) \\ (\land (\land (\land n_{118} (\land n_{135} n_{139}) n_{149})) (\land n_{149} (\land n_{$$

$$(\land (\land n_{216} n_{202}) (\land (\land n_{173} n_{142}) (\land n_{114} (\land n_{86} n_{74})))) \rightarrow (\land (\land n_{74} (\land n_{173} (\land n_{86} n_{216}))) (\land (\land n_{86} n_{114}) (\land n_{142} n_{202}))) (\land (\land n_{147} i_{23}) (\land n_{136} (\land (\land n_{120} n_{104}) (\land n_{89} n_{75})))) \rightarrow (\land (\land (\land n_{89} n_{120}) (\land n_{136} n_{147})) (\land (\land n_{75} n_{104}) (\land n_{147} i_{23}))) (\land (\neg (\land (\land (\neg op_4) (\neg op_3)) op_1)) (\neg (\land op_4 op_3))) \rightarrow (\lor (\neg (\land (\land (\neg op_4) op_1)) (\oplus op_3 op_4)) (\land (\neg (\land (\land (\neg n_{1138}) (\land n_{22} n_{292})) (\land n_{22} n_{1158}))) n_{1065}) \rightarrow (\land (\oplus n_{1065} (\land n_{1158} (\land n_{1065} n_{22}))) (\lor n_{1138} (\neg (\land n_{29} n_{22})))) (\land (\neg (\land (\neg (\land (\neg t_1) oc_3)) oc_0)) (\neg (\land norm_8 t_1))) \rightarrow (\oplus (\land t_1 (\lor t_2))) (\neg (\land (\land t_2 (\neg t_{00})))) (\land (\neg (\land (n_1 (\neg t_2)))) (\neg (\land (\land t_2 (\neg t_{00})))) (\land (\neg (\land (n_1 (\neg t_{22}))) (\neg (\land (\land t_2 (\neg t_{00})))) (\land (\neg (\land (\neg (\land (\land n_{740} n_{719}) (\neg n_{698}))) n_{22})) (\neg n_{660})) \rightarrow (\land (\neg (\land (\neg (\land (\neg (\land norm_8 n_{148})) n_{29})) n_{17})) n_{22})) (\land (\neg (\land (\neg (\land (\neg (\land norm_8 n_{148})) n_{29})) n_{17})) n_{22})) (\land (\neg (\oplus (\land n_{203} (\neg (\oplus n_{200} n_{197}))) (\oplus n_{202} (\land n_{719} n_{29}))))) (\land (\neg (\oplus (\land n_{206} (\neg n_{197})) (\land n_{188} (\neg n_{203})))) (\land (\neg (\land (\land (\land n_{3648} (\land n_{469} n_{3652}))))) (\land (\land n_{3648}) (\land n_{3646}) (\land n_{3653} (\land n_{646})))) n_{469}) \rightarrow (\oplus (n_{469} (\land n_{3642}) (\land n_{3648}) (\neg n_{3648}) (\neg n_{3646})))) n_{469}) \rightarrow (\oplus (\land n_{5514} (\land n_{264} n_{5517})))) (\land (\lor (n_{5519} n_{5514}) (\land n_{5521} (\land n_{5517})))) (\land (\land (\neg (\land norm_4 n_{279}))) (\neg (\land norm_{11} s_1)))) (\land s_3 n_{265})) \rightarrow (\land (\oplus (\otimes s_3 (\land norm_4 (\land s_3 n_{279}))) (\oplus (\land norm_{11} s_1)))) (\land s_3 n_{265}))))) (\land (\neg (\land (\land (\land s_3 n_{279}))) (\ominus (\land norm_{11} s_1)))) (\land s_5 n_{205})))))$$

$$\rightarrow (\oplus (\vee (\oplus (\vee b_2 \ b_5) (\neg b_3)) \ b_5)$$

$$(\land (\neg (\land (\neg n_{741}) (\neg (\land (\land (\land n_{461} \ n_{457}) \ n_{734}) \ p_{1002})))) \ p_{1082})$$

$$\rightarrow (\land (\vee (\land (\land (\land p_{1002} \ n_{461}) \ n_{457}) \ n_{734}) \ n_{741}) \ p_{1082})$$

$$(\land (\neg (\land n_{883} (\neg (\land n_{880} (\neg n_{863}))))) (\neg (\land (\neg n_{855}) \ n_{820})))$$

$$\rightarrow (\land (\oplus (\vee (\neg (\land n_{883} \ n_{880})) \ n_{863}) \ n_{883}) (\vee (\neg n_{820}) \ n_{855}))$$

$$(\land (\neg (\land (\land (\neg p_{110}) (\neg p_{1055})) (\neg p_{1096}))) (\neg (\land p_{116} \ p_{1055})))$$

$$\rightarrow (\oplus (\vee (\oplus (\vee p_{110} \ p_{1096}) \ p_{116}) \ p_{1055}) \ p_{116})$$

$$(\land (\neg (\land (\land (\neg p_{110}) (\neg p_{1055})) (\neg p_{1096})) \ n_{25}))) \ n_{29}))$$

$$\rightarrow (\oplus (\vee (\oplus (\vee p_{110} \ p_{1096}) \ p_{116}) \ p_{1055}) \ p_{116})$$

$$(\neg (\land (\neg (\land n_{1041} (\neg (\land (\neg (\land n_{950} (\neg n_{950}))) \ n_{25})))) \ n_{29}))$$

$$\rightarrow (\oplus (\land (\neg (\land n_{1041} (\neg (\land (\neg (n_{105} \ n_{1041}) \ n_{10}))) \ n_{21})) \ n_{21}))$$

$$(\land (\neg (\land (\land n_{1041} (\neg (\land (\neg (n_{210} \ n_{1041}) \ (\neg (n_{210}))) \ n_{21}))) \ n_{21}))$$

$$\rightarrow (\lor (\land (\land (\neg (n_{11} (\neg (n_{210}))) \ n_{21}))) \ (\land (\neg (\neg (n_{210}) \ n_{210}))) \ n_{21})) \ n_{21})$$

$$(\land (\land (\land (\neg (n_{111} (\neg (n_{210}))) \ (\land (\neg (n_{210} \ n_{210})))) \ n_{21})) \ n_{21}) \ n_{21} \ n_{22} \ n_{21} \ n_{22} \ n_{21} \ n_{22} \ n_{21} \ n_{22} \ n_{22}$$

$$\begin{array}{l} \rightarrow (\oplus (\wedge n_{114} (\wedge x_{23} (\wedge n_{116} (\wedge n_{179} n_{121})))) (\vee (\neg n_{179}) (\wedge x_{23} (\vee n_{116} (\neg n_{121})))) \\ (\wedge (\neg (\wedge (\wedge x_{23} (\vee n_{116} (\neg n_{121}))) (\neg (\wedge n_{121} n_{114})))) n_{179}) \\ \rightarrow (\wedge (\wedge (\wedge x_{23} (\vee n_{121} (\neg x_{23}))) (\vee n_{114} (\neg (\wedge n_{116} x_{23})))) \\ (\wedge (\neg (\wedge (\wedge n_{179} (\vee n_{23})) (\vee n_{23})) (\vee (\wedge n_{5} (\neg n_{33})))) \\ (\wedge (\neg (\wedge (\wedge norm_3 (\wedge (\neg b_7) (\neg b_2)))) (\neg (\wedge b_7 (\neg b_4)))) \\ \rightarrow (\oplus (\wedge norm_3 (\neg (\vee b_7 b_2))) (\vee b_4 (\neg b_7))) \\ (\wedge (\neg (\vee (\wedge norm_3 (\neg (\vee b_7 b_2))) (\vee b_4 (\neg b_7)))) \\ (\wedge (\neg (\vee (\wedge n_{20} (\vee n_{46} (\neg n_{285}))) (\wedge n_{285} (\neg n_{46})))) n_{20}) \\ \rightarrow (\oplus (\wedge n_{20} (\vee n_{46} (\neg n_{23}))) (\vee n_{216} (\neg n_{267} n_{285})))) \\ (\wedge (\neg (\wedge (\wedge n_{69} (\vee n_{23} (\neg n_{21}))) (\vee n_{41} (\neg n_{21})))) n_{20}) \\ \rightarrow (\oplus (\wedge n_{69} (\wedge n_{41} (\wedge n_{20} (\wedge n_{23} n_{21})))) (\wedge n_{20} (\vee n_{21} (\neg n_{69})))) \\ (\wedge (\oplus (\wedge n_{349} (\wedge n_{374} (\wedge n_{46} n_{356})) (\vee n_{46} (\neg n_{374})))) n_{19}) \\ \rightarrow (\oplus (\wedge n_{349} (\wedge n_{374} (\wedge n_{356} (\wedge n_{19} n_{46})))) (\wedge n_{19} (\vee n_{46} (\neg n_{374}))))) \\ (\wedge (\neg (\wedge (\neg n_{252} (\neg (\wedge (\neg (\neg (\neg (\wedge n_{1027} (\wedge n_{820} n_{1013}))))) \\ (\wedge (\wedge (\wedge n_{101} (\vee n_{1025} (\neg n_{820}))) (\vee (n_{104} (\wedge n_{1013} (\wedge n_{1025} (\wedge n_{820} n_{1013}))))) \\ (\wedge ((\wedge n_{1025} (\vee n_{1018} (\neg n_{20}))) (\vee (n_{1044} (\wedge n_{1013} (\wedge n_{1025} (\wedge n_{820} n_{1013}))))) \\ (\wedge (n_{883} (\neg (\wedge (\wedge n_{877} p_{1025}) n_{869})) (\neg (\wedge norm_8 p_{1027} n_{833}))) \\ (\wedge (\wedge n_{1254} n_{1242} n_{1242}) (\vee n_{1244} (\wedge n_{383} n_{384}))) (\oplus n_{384} (\wedge n_{1254} (\oplus n_{384} (\vee n_{1247} n_{1242}))))) \\ \end{pmatrix} (\oplus (\oplus (\wedge (\vee n_{1277} n_{1242}) (\vee n_{1245} (\wedge n_{387} n_{383}))) (\wedge norm_8 (\wedge n_{1254} (\oplus n_{384} (\vee n_{1247} n_{1242})))))) \\ (\oplus (\oplus (\wedge (\vee n_{1277} n_{1242}) (\vee n_{1247} (\neg n_{1242})))) n_{434} (\wedge n_{1254} (\oplus n_{384} (\vee n_{1247} n_{1242})))))) \\ (\oplus (\oplus (\wedge (\vee n_{1277} n_{1242}) (\vee n_{1254} (\wedge n_{387} n_{384})))) (\oplus (\neg n_{1247} n_{1242})))) n_{4384} (\wedge n_{1247} n_{1242}))))))$$

$$\begin{array}{l} (\land (\land n_{1442} n_{1435}) (\neg (\land (\neg (\land (\neg (\land n_{1427} n_{735})) n_{1426})) p_{1082}))) \\ \rightarrow (\land (\land (\land n_{1442} n_{1435}) (\lor n_{1426} (\neg p_{1082}))) (\land n_{1435} (\neg (\land n_{735} (\land p_{1082} n_{1427}))))) \\ (\neg (\land (\land n_{1442} n_{1435}) (\lor (\neg (\land n_{837} (\neg (\land n_{831} p_{100})))) n_{821})))) \\ \rightarrow (\lor (\land n_{821} (\land n_{831} (\land n_{837} p_{100}))) (\lor norm_3 (\land n_{821} (\neg n_{837})))) \\ (\land (\land (\neg (\land (\neg (\lor (\lor p_{1039} p_{1051})) (\land p_{116} (\neg p_{1052})))) norm_8) p_{1026}) \\ \rightarrow (\land (\land p_{1026} norm_8) (\lor (\lor p_{1052} p_{1039}) (\lor p_{1051} (\neg p_{116})))) \\ (\land (\land (\neg (\land (\land n_{449} (\land n_{444} n_{440})) (\oplus n_{444} (\oplus n_{440} n_{1206})))) p_{1082}) \\ \rightarrow (\ominus (\land n_{1206} (\land n_{449} (\land n_{440} (\land p_{1082} n_{444})))) p_{1082}) \\ (\land (\land n_{252} (\oplus (\oplus n_{244} n_{253}) (\neg (\land (\land n_{240} (\neg n_{229})) (\neg n_{225}))))) \\ (\land (\land n_{152} (\oplus (\oplus n_{244} n_{253}) (\neg (\land (\land n_{240} (\neg n_{229})) (\neg n_{225}))))) \\ (\land (\land n_{155} (\neg (\land n_{104} n_{96}))) (\neg (\land (\neg (\land n_{103} n_{96})) i_{19}))) \\ (\land (\land n_{115} (\neg (\land n_{104} n_{96}))) (\neg (\land (\neg (\land n_{103} n_{96})) i_{19})))) \\ (\land (\land n_{115} (\neg (\land n_{104} n_{96}))) (\neg (\land (\land n_{156} n_{150} n_{203})) \\ (\land (\land (\land n_{115} (\neg (\land n_{104} n_{96})))) (\neg (\land n_{156} n_{150} n_{203})) \\ (\land (\land (\land n_{115} (\neg (\land n_{104} n_{96})))) (\neg (\land n_{156} n_{150} n_{203})) \\ (\land (\land (\land n_{115} (\neg (\land n_{104} n_{96})))) (\neg (\land n_{157} (\land n_{166} n_{159}))))) \\ \rightarrow (\land (\oplus (\land n_{115} (\neg (\land n_{166} n_{159})))) (\neg (\land n_{156} n_{150} n_{203})) \\ (\land (\neg (\land n_{133} (\neg (\land n_{166} n_{105})))) (\neg (\land n_{166} n_{158} n_{159}))))) \\ \land (\ominus (\land (\land n_{137} (\neg (\land n_{166} n_{139} n_{138})))) (\neg (\land n_{161} (\land n_{139} n_{138}))))) \\ (\land (\neg (\land n_{145} (\neg (\land n_{139} n_{139})))) (\neg (\land n_{141} (\land n_{139} n_{138}))))) \\ (\land (\neg (\land n_{121} (\land n_{297} n_{297}))) (\lor (\land n_{293} (\neg (\land n_{281} n_{129}))))) \\ (\land (\neg (\land n_{135} (\neg n_{132})) (\neg (\land n_{127} (\neg n_{193})))) (\land (\land n_{147})) \\ (\oplus (\land n_{120} (\neg (\land n_{110} n_{108}))) (\neg (\land n_{104} (\neg (\land n_{85} (\land n_{97} n_{104})))))) \\ (\land (\land n_{120} (\neg (\land n_{110} n_{108}))) (\neg (\land n_{104} (\neg (\land n_{85} (\land n_{97} n_{104})))))) \\ (\land (\land (\land n_{29} (\lor$$
$$\begin{array}{l} \rightarrow (\oplus (\land oc_{3} (\neg (\lor in_{1} oc_{0}))) (\neg (\lor oc_{3} (\land in_{1} oc_{2})))) \\ (\land (\neg (\lor (\neg (\lor oc_{0} ct_{0})) (\oplus in_{1} (\land oc_{0} (\neg cc_{2}))))) in_{0} \\ \rightarrow (\oplus (\land (\land in_{0} ct_{0}) (\neg (\lor in_{1} oc_{0}))) (\land in_{0} (\land oc_{0} (\oplus in_{1} oc_{2})))) \\ (\land (\neg (\land (\neg (\land n_{1115} (\neg n_{1110}))) in_{1})) (\neg (\land (\neg n_{1103}) n_{38}))) \\ \rightarrow (\land (\oplus in_{1} (\lor n_{1110} (\neg (\land n_{1115} in_{1})))) (\oplus (\land n_{1110} in_{1}) (\lor n_{1103} (\neg n_{38})))) \\ (\land (\neg (\land (\neg (\land (\land (\neg n_{453}) oc_{1}) (\neg n_{451}))) (\neg norm_{9}))) n_{24}) \\ \rightarrow (\lor (\land (\land (\neg (\land (\land (\neg n_{453}) oc_{1}) (\neg n_{451}))) (\land n_{24} norm_{9})) \\ (\land (\land (\land (\land (\land (\land (\neg n_{452} oc_{1}) (\neg (\lor (\land n_{451} n_{453}))) (\land n_{24} norm_{9})) \\ (\land (\land (\land (ac_{22} oc_{1}) (\neg (\lor (\land n_{451} n_{453}))) (\land n_{24} norm_{9})) \\ (\land (\land (\land (ac_{22} (\neg n_{663})) (\neg (\land (\land (\land n_{38} (\neg (\land n_{667} (\neg n_{663})))) n_{38})))) \\ \rightarrow (\land (\land (\land (\land (ac_{22} ac_{76})) (\oplus (\land (\land (\neg n_{335} (\neg (\land n_{38} n_{667}))))) \\ (\land (\land (\land (\land n_{262} (\neg (ac_{777})) (\land n_{3777} (\land n_{3777})) (\land n_{3777} (\land n_{3777})) n_{44})) n_{928}) \\ \rightarrow (\land (\land n_{928} (\neg (\land n_{3779}) (\neg n_{3777})) (\land n_{3777} (\neg n_{3777})) (\lor n_{3777} (\land n_{3777})) (\lor n_{3777}) n_{3779}) (\lor n_{3772})) \\ (\land (\land (\land (\land n_{1599} (\neg (n_{1777} (\lor n_{3777} n_{3777}))) (\land n_{3777} n_{3719}) (\lor n_{3777} n_{3779}) (\lor n_{3777}) (\land n_{469} n_{3155}))))) \\ \rightarrow (\oplus (\oplus n_{469} (\neg (\lor n_{1137} (\lor n_{1115} n_{1094})))) (\lor (\lor (n_{1137} n_{1115}) (\lor n_{1094})) \land n_{335}))))) \\ \rightarrow (\oplus (\land (\land (\land n_{159} (\neg (n_{511} n_{293})) n_{340}) h_{4}) n_{335}) n_{355}) \\ (\land (\neg (\land (\land (n_{1596} (\neg (n_{511} n_{1094}))) (\land (\land (\neg (n_{10} n_{201})))) \\ \rightarrow (\lor (\oplus (\land (\land (n_{29} n_{293})) n_{340}) h_{4}) n_{335}) n_{355}) \\ (\land (\neg (\land (\land (n_{10} n_{201})) (\neg (\land (\land (n_{293} n_{294})) n_{293}) \\ \land (\lor (\land (\land (n_{10} n_{201})) (\neg (\land (\neg (n_{293} n_{294}))) n_{29})) \\ \rightarrow (\lor (\oplus (\land (n_{293} n_{295}))) (\land (\land (\neg (n_{293} n_{294})) n_{29})) \\ (\land (\land (\land (n_{10} n_{201})) (\neg (\land (\neg (n_{293} n_{294}))) n_{29})) \\ (\land ((\land (\land (n_{10} n_{293}) n_{255})) (\land ((\land (n_{10} n_{203})))) \\ (\land ((\land (\land (n_{10} n_{293}) n_{255})) (\land ((\land (n_{293} n_{2$$

 $(\land (\neg (\land (\land s_0 a_{14}) (\neg s_1))) (\neg (\land (\land (\neg s_0) a_{13}) s_1)))$ $\rightarrow (\lor (\oplus (\neg (\land s_1 \ a_{13})) (\land s_0 \ a_{14})) (\land s_1 \ s_0))$ $(\land (\neg (\land (\land (\neg s_0) a_{40}) (\neg s_1))) (\neg (\land (\land s_0 a_{37}) s_1)))$ $\rightarrow (\lor (\oplus (\neg (\land (\oplus a_{37} a_{40}) s_0)) a_{40}) (\oplus s_1 s_0))$ $(\oplus (\land (\oplus n_{359} (\oplus n_{271} (\land n_{262} n_{215}))) n_{186}) (\land (\land n_{186} n_{215}) n_{253}))$ $\rightarrow (\oplus (\land (\land (\oplus n_{253} \ n_{262}) \ n_{186}) \ n_{215}) (\land (\oplus n_{359} \ n_{271}) \ n_{186}))$ $(\land (\land (\neg (\land (\neg i_6) i_4)) i_5) (\neg (\land (\land norm_8 i_6) (\neg i_4))))$ $\rightarrow (\oplus (\land (\land (\oplus i_4 i_6) i_5) (\lor norm_8 i_4)) i_5)$ $(\land (\neg (\land (\neg (\land i_9 i_5)) (\land i_8 i_7))) (\neg (\land (\neg i_7) i_5)))$ $\rightarrow (\oplus (\lor (\neg (\land i_7 i_8)) (\land i_5 i_9)) (\land (\neg i_7) i_5))$ $(\land (\neg (\neg (\lor (\oplus i_7 i_8) i_9))) (\neg (\land (\neg (\land i_8 i_7)) i_5)))$ $\rightarrow (\land (\oplus (\lor (\oplus i_8 i_7) (\oplus i_5 i_9)) i_5) (\lor (\neg i_5) i_7))$ $(\land (\oplus (\oplus i_{38} i_6) (\land i_{37} i_5)) (\land (\oplus i_{37} i_5) (\land i_{36} i_4)))$ $\rightarrow (\land (\land (\oplus i_{37} i_5) (\oplus i_6 i_{38})) (\land i_4 i_{36}))$ $(\land (\neg (\oplus norm_3 (\neg (\land i_{33} i_1)))) (\land (\neg (\oplus i_{33} (\neg i_1))) norm_{13}))$ \rightarrow (\land (\land (\oplus i_{33} i_1) norm₃) norm₁₃) $(\land (\land (\land x_{23} (\neg (\land n_{114} n_{121}))) (\land x_{24} (\lor n_{116} (\neg n_{121})))) x_{25})$ $\rightarrow (\land (\lor (\neg n_{121}) (\land n_{116} (\neg n_{114}))) (\land x_{25} (\land x_{23} x_{24})))$ $(\land (\neg (\oplus (\land n_{107} (\land x_{17} (\land x_{15} n_{95}))) (\land x_{17} (\neg n_{95})))) n_{94})$ $\rightarrow (\oplus (\land n_{107} (\land n_{94} (\land x_{15} (\land x_{17} n_{95})))) (\land n_{94} (\lor n_{95} (\neg x_{17}))))$ $(\land (\neg (\oplus (\land n_{349} (\land n_{374} (\land n_{46} n_{356}))) (\land n_{374} (\neg n_{46})))) n_{19})$ $\rightarrow (\oplus (\land n_{349} (\land n_{374} (\land n_{356} (\land n_{19} n_{46})))) (\land n_{19} (\lor n_{46} (\neg n_{374}))))$ $(\land (\land (\land (\neg (\lor pi_{038} pi_{044})) (\neg (\lor pi_{042} pi_{040}))) (\neg pi_{050})) (\neg pi_{046}))$ $\rightarrow (\land (\neg (\lor pi_{040} (\lor pi_{042} pi_{044}))) (\neg (\lor pi_{046} (\lor pi_{038} pi_{050}))))$ $(\land n_{1603} (\neg (\oplus (\land n_{1590} (\land n_{1587} (\land n_{825} n_{1596}))) (\land n_{825} (\neg n_{1596})))))$ $\rightarrow (\oplus (\land (\land n_{1590} \ n_{1587}) \ (\land n_{1603} \ (\land n_{1596} \ n_{825}))) \ (\land n_{1603} \ (\lor n_{1596} \ (\neg n_{825}))))$ $(\land (\neg (\oplus (\land n_{1163} (\land n_{1165} (\land n_{468} pi_{082}))) (\land n_{468} (\neg pi_{082})))) n_{1162})$ $\rightarrow (\oplus (\land (\land n_{1162} \ n_{1165}) \ (\land n_{1163} \ (\land n_{468} \ pi_{082}))) \ (\land n_{1162} \ (\lor pi_{082} \ (\neg n_{468}))))$ $(\land (\land n_{1230} \ n_{384}) \ (\neg (\land (\neg (\land (\neg n_{1220}) \ n_{468})) \ n_{1218})) \ n_{444})))$

$$\rightarrow (\land (\land n_{1230} (\land n_{384} (\lor n_{1218} (\neg n_{444})))) (\land n_{384} (\lor n_{1220} (\neg (\land n_{444} n_{468})))))$$

 $(\land (\neg (\oplus (\land (\land pi_{082} \ n_{665}) \ (\land n_{656} \ n_{684})) \ (\land n_{684} \ (\neg pi_{082})))) \ n_{384})$ $\rightarrow (\oplus (\land (\land n_{656} \ n_{665}) \ (\land n_{684} \ (\land n_{384} \ pi_{082}))) \ (\land n_{384} \ (\lor pi_{082} \ (\neg n_{684}))))$ $(\land (\neg (\oplus (\land n_{675} (\land n_{451} (\land pi_{082} n_{681}))) (\land n_{681} (\neg pi_{082})))) n_{672})$ $\rightarrow (\oplus (\land (\land n_{672} \ n_{451}) \ (\land n_{675} \ (\land n_{681} \ pi_{082}))) \ (\land n_{672} \ (\lor pi_{082} \ (\neg n_{681}))))$ $(\land (\neg (\oplus (\oplus n_{244} n_{238}) (\neg (\land (\land n_{240} (\neg n_{229})) (\neg n_{225}))))) n_{236})$ $\rightarrow (\oplus (\land (\lor n_{229} \ n_{225}) \ (\land n_{236} \ n_{240})) \ (\land n_{236} \ (\oplus \ n_{238} \ (\oplus \ n_{244} \ n_{240}))))$ $(\land (\land (\neg (\land n_{727} (\land (\neg n_{715}) norm_6))) (\neg (\land (\neg n_{721}) n_{719}))) norm_6)$ $\rightarrow (\land (\land (\lor (\neg n_{727}) n_{715}) (\lor (\neg n_{719}) n_{721})) norm_6)$ $(\land (\neg (\land (\neg (\land n_{171} \ n_{166})) \ (\neg \ norm_6))) \ (\neg (\land (\land n_{171} \ n_{177}) \ n_{166})))$ $\rightarrow (\oplus (\land n_{166} (\land n_{171} n_{177})) (\lor (\land n_{166} n_{171}) norm_6))$ $(\land (\land (\land n_{213} \ n_{166}) \ n_{211}) (\land (\neg (\land n_{203} \ n_{150})) (\neg (\land n_{171} \ n_{166}))))$ $\rightarrow (\land (\land n_{166} (\land (\neg n_{171}) (\land n_{213} n_{211}))) (\oplus n_{171} (\lor (\neg n_{203}) (\oplus n_{166} n_{150}))))$ $(\neg (\land (\neg (\land n_{133} (\neg (\land n_{116} n_{105})))) (\neg (\land n_{127} (\land n_{116} n_{105})))))$ $\rightarrow (\oplus (\land (\land (\oplus n_{127} \ n_{133}) \ n_{105}) \ n_{116}) \ n_{133})$ $(\land (\oplus (\land n_{233} (\land n_{109} i_{22})) (\land n_{109} (\neg i_{22}))) (\neg (\land n_{233} n_{111})))$ $\rightarrow (\oplus (\land n_{233} (\lor n_{111} (\neg n_{109}))) (\lor n_{233} (\land n_{109} (\neg i_{22}))))$ $(\land (\oplus (\oplus i_{14} i_6) (\land i_{13} i_5)) (\land (\oplus i_{13} i_5) (\land i_{12} i_4)))$ $\rightarrow (\land (\land i_{12} (\oplus i_{13} i_5)) (\land i_4 (\oplus i_6 i_{14})))$ $(\land (\neg (\land (\land op_4 op_3) op_0)) (\neg (\land (\land (\neg op_4) (\neg op_3)) op_1)))$ $\rightarrow (\lor (\oplus op_3 (\land op_4 op_0)) (\oplus op_3 (\lor op_4 (\neg op_1))))$ $(\land (\neg (\land (\land (\neg opext_0) op_4) opext_1)) (\land (\neg (\land (\neg opext_1) op_4)) op_3))$ $\rightarrow (\oplus (\land (\land op_3 \ opext_1) \ (\land op_4 \ opext_0)) \ (\land op_3 \ (\neg \ op_4)))$ $(\land (\neg (\lor (\oplus op_3 (\land op_4 op_0)) (\oplus op_3 (\lor op_4 (\neg op_1))))) op_2)$ $\rightarrow (\land (\oplus op_1 (\land op_3 (\oplus op_0 op_1))) (\land op_2 (\oplus op_3 (\neg op_4))))$ $(\neg (\land (\land (\neg norm_4) (\neg (\land (\neg (\land (\neg n_{672}) n_{24})) n_{44}))) (\neg n_{670})))$ $\rightarrow (\lor (\land n_{44} (\lor n_{672} (\neg n_{24}))) (\lor n_{670} norm_4))$ $(\land (\neg (\lor (\land oc_3 (\neg in_0)) (\oplus oc_2 (\neg (\land ct_0 in_0))))) (\neg in_1))$

$$\begin{array}{l} \rightarrow (\land (\oplus in_{1} (\lor in_{0} (\neg oc_{3}))) (\oplus (\land ct_{0} in_{0}) (\land oc_{2} (\neg in_{1})))) \\ (\land (\neg (\land (\neg norm_{4}) (\neg (\land (\neg (n_{513}) (\neg n_{476}))) n_{22})))) \\ \rightarrow (\lor (\land n_{29} norm_{4}) (\land (\lor n_{476} n_{513}) (\land n_{29} n_{22}))) \\ (\land (\land (\neg (\land (\neg in_{1}) (\neg in_{0}))) ct_{0}) (\neg (\land (\land in_{0} oc_{0} in_{1}))) \\ \rightarrow (\oplus (\land (\land in_{1} in_{0}) (\land oc_{0} ct_{0})) (\land ct_{0} (\lor in_{1} in_{0}))) \\ (\land (\neg (\land (\neg (\lor n_{112} (\land n_{24} (\neg n_{54})))) (\neg (\land n_{24} n_{46})))) n_{23}) \\ \rightarrow (\oplus (\land n_{23} (\oplus n_{54} (\lor n_{24} (\oplus n_{54} n_{112})))) (\land (\lor n_{112} n_{46}) (\land n_{23} (\land n_{54} n_{24})))) \\ (\land (\neg (\land (\land (\neg s_{0}) a_{89}) (\neg s_{1}))) (\neg (\land (\land s_{0} a_{86}) s_{1}))) \\ \rightarrow (\lor (\oplus s_{1} (\land s_{0} a_{86})) (\oplus s_{1} (\lor s_{0} (\neg a_{89}))))) \\ (\land (\neg (\land (\land s_{0} a_{14}) (\neg s_{1}))) (\neg (\land (\land (\neg s_{0}) a_{13}) s_{1}))) \\ \rightarrow (\lor (\oplus s_{0} (\neg (\land s_{1} a_{13}))) (\oplus s_{0} (\land a_{14} (\neg s_{1}))))) \\ (\land (\neg (\land (\land in_{0} ct_{1}) in_{1})) (\neg (\land (\land (\neg in_{0} (\neg in_{3}) s_{1})))) \\ \rightarrow (\lor (\oplus s_{0} (\neg (\land s_{1} a_{13}))) (\oplus s_{0} (\land a_{14} (\neg s_{1}))))) \\ \rightarrow (\lor (\oplus s_{0} (\neg (\land s_{1} a_{13}))) (\oplus s_{0} (\land a_{14} (\neg s_{1}))))) \\ \rightarrow (\lor (\oplus (\lor (\land (\neg n_{369}) (\land n_{365} n_{325})) (\neg (\land n_{365} n_{325}))))) \\ \rightarrow (\lor (\land (\neg n_{369}) (\land n_{36} n_{326}) (\neg (\land n_{365} n_{325})))) \\ \rightarrow (\oplus (\land (\land (\neg n_{369} n_{273}) (\land (\land n_{203} n_{202}) n_{201})) n_{188}) (\oplus (\land n_{203} n_{202} n_{201}))) \\ \rightarrow (\oplus (\land (\land (\neg n_{189} n_{203}) n_{202}) (\lor n_{188} n_{201}) n_{212}) \\ (\oplus (\land n_{149} (\neg (\land (\neg n_{188} n_{203}) n_{202}) (\lor n_{188} n_{201}) n_{212}) \\ (\oplus (\land n_{126} (\land n_{203} (\land n_{22} (\neg n_{174}))))) (\land n_{22} (\land (\land n_{209} n_{114} n_{182}))) \\ \rightarrow (\lor (\land n_{182} (\land n_{209} (\land n_{14} n_{183}))) (\land n_{22} (\land n_{209} n_{114} n_{182}))) \\ \rightarrow (\lor (\land n_{182} (\land n_{209} (\land n_{114} n_{183}))) (\land n_{183} (\land (\land n_{209} n_{114} n_{182}))) \\ (\land (\neg (\land (\land (\land n_{182} (\neg n_{15} n_{175}))) (\neg ((\neg (\neg n_{151} n_{21}))))) n_{1}))) \\ (\oplus (\oplus (\land n_{222} (\lor n_{151} (\land n_{199} n_{21}))) (\land (\land (\land (\neg n_{163} (\land n_{209} n_{114} n_{182})))) \\ (\land (\neg (\land (\land (\land n_{182} (\neg n_{153} n_{175}))) (\neg (\land (\neg (\neg n_{163} (\land n_{192} n_{175})))$$

$$\begin{split} \rightarrow (\oplus (\land n_{856} (\land n_{885} (\land n_{818} (\land n_{820} n_{819})))) (\land n_{819} (\lor n_{885} (\neg n_{818})))) \\ (\land n_{971} (\neg (\land (\land n_{956} (\lor n_{939} (\neg p_{1028}))) (\neg (\land p_{1028} (\oplus n_{939} n_{936}))))) \\ \rightarrow (\oplus (\land (\land n_{936} n_{958}) (\land n_{971} (\land n_{939} p_{1028}))) (\land n_{971} (\lor p_{1028} (\neg n_{958})))) \\ (\land (\land (\neg n_{477}) (\neg (\land n_{149} (\land n_{146} n_{132})))) (\neg (\land (\land n_{146} n_{145} n_{132}))) \\ \rightarrow (\lor (\land (\land n_{477} (\lor n_{145} n_{149}))) (\neg (\lor n_{477} (\land n_{132} n_{146})))) \\ (\oplus (\land n_{138} (\land n_{103} (\land n_{96} n_{141}))) (\lor (\neg n_{145}) (\land n_{138} (\land n_{103} n_{96})))) \\ \rightarrow (\oplus (\neg n_{145}) (\land n_{96} (\land n_{138} (\land n_{103} (\oplus n_{145} n_{141}))))) \\ (\land (\ominus (\land (\land (\neg c_1) oc_3) (\land in_0 (\neg oc_0)))) (\neg (\land (\neg in_0) c_1)))) \\ \rightarrow (\oplus (\neg (n_{45}) (\land n_{96} (\neg n_{10}) (\neg (\neg oc_0)))) (\neg (\land (\neg c_{10}) c_{1})))) \\ \rightarrow (\lor (\oplus c_1 (\lor oc_2) (\oplus in_0 in_1)) (\lor (\neg in_0) (\land oc_2 (\neg c_{11})))) \\ (\land (\neg (\land (\land c_1 in_0) (\land c_2 (\neg c_1))) (\land oc_1 (\land in_1 (\neg c_2))))) \\ (\land (\land (\neg (\land in_1 in_0)) (\neg c_1)) (\land (\neg (\land in_1) oc_0) (\neg oc_2))) \\ \rightarrow (\land (\land (\land in_1 in_0)) (\neg c_1)) (\land (\land (\neg in_1) (\neg c_0)) (\neg oc_2))) \\ \rightarrow (\land (\land (\land in_1 in_1)) (\neg (\land (\land (\neg in_1) (\neg c_0)) (\neg oc_2))) \\ \rightarrow (\land (\land (\neg (\land n_{345} n_{359} (\oplus n_{272} n_{254}))) (\land (\land (\neg (\land n_{345} n_{326}))))) \\ \rightarrow (\land (\land n_{345} (\land n_{326} n_{333}))) (\oplus n_{359} (\oplus n_{272} n_{254})) n_{186})) \\ \rightarrow (\land (\land n_{146} (\neg n_{379})) (\oplus n_{359} (\oplus n_{272} n_{254}))) n_{186})) \\ \rightarrow (\land (\land n_{146} (\neg n_{345} n_{326} n_{333}))) (\oplus n_{150} (\land n_{345} n_{326}))))) \\ \land (\land (\neg (\land (\land n_{345} n_{326} n_{333}))) (\oplus n_{150} (\land n_{336} (\neg (\land n_{345} n_{326})))))) \\ \land (\land (\oplus (\land n_{345} (\land n_{326} n_{333}))) (\oplus n_{150} (\land n_{336} (\neg (\land n_{345} n_{326})))))) \\ \land (\land (\neg (\land (\land n_{345} n_{326} n_{333}))) (\oplus n_{150} (\land n_{336} (\neg (\land n_{345} n_{326})))))) \\ \land (\land ((\neg (\land (\land (\neg n_{345} n_{326} n_{333}))) (\oplus n_{150} (\land n_{345} n_{326})))))) \\ \land (\land ((\neg (\land (\land (\neg n_{345} n_{326} n_{333}))) (\oplus n_{150} (\land n_{336} (\neg (\land n_{345} n_{326}))))))) \\ \land (\land ((\neg (\land (\land n_{345} n_{326} n_{333}))) (\oplus n_{150} (\land n_{336} (\neg (\land n_{345} n_{326})))))))) \\ \land (\land ((\neg (\land (\land n_{345} n_$$

 $(\land (\land norm_{2} (\neg (\oplus (\land (\neg n_{215}) (\neg n_{167})) (\neg (\land (\neg n_{216}) (\neg n_{119}))))) n_{225}) \\ \rightarrow (\land (\oplus (\lor n_{167} n_{215}) (\lor n_{216} n_{119})) (\land norm_{2} n_{225})) \\ (\neg (\land (\oplus n_{135} (\neg (\land (\land n_{63} n_{130}) norm_{6}))) (\neg (\land n_{127} (\neg (\land norm_{6} n_{63})))))) \\ \rightarrow (\lor (\oplus (\land (\land (\oplus n_{130} n_{127}) n_{63}) norm_{6}) n_{127}) (\neg n_{135})) \\ (\land (\neg (\oplus (\lor (\neg norm_{5}) (\lor (\neg i_{7}) i_{4})) i_{4})) (\neg (\land (\land i_{7} i_{4}) norm_{5}))) \\ \rightarrow (\oplus (\land norm_{5} i_{7}) i_{4}) \\ (\land (\land (\neg (\land (\neg i_{7}) i_{6})) (\neg (\land (\neg i_{5}) i_{4}))) (\neg (\land (\neg i_{6}) i_{5}))) \\ \rightarrow (\land (\oplus (\lor (\oplus i_{6} i_{4}) (\neg i_{5})) i_{4}) (\lor (\neg (\land (\neg i_{21}) i_{20})))) \\ \rightarrow (\land (\oplus (\lor (\oplus i_{6} i_{4}) (\neg i_{5})) i_{4}) (\lor (\neg (\neg i_{21}) i_{20})))) \\ \rightarrow (\land (\oplus (\neg (\lor (\oplus i_{20} i_{22}) i_{21})) i_{22}) (\lor (\neg i_{22}) i_{23})) \\ (\land (\neg (\oplus (\land n_{241} (\land n_{243} (\land n_{248} n_{252}))) (\land n_{252} (\lor n_{243} (\neg n_{248}))))) n_{220}) \\ \rightarrow (\oplus (\land n_{241} (\land n_{243} (\land n_{243} n_{243})))) (\land n_{220} (\lor (\neg n_{252}) (\land n_{248} (\neg n_{243}))))) \\ (\neg (\land (\neg (\land (\neg (\land n_{343} (\neg (\land (\neg (\land n_{337} (\neg n_{156}))) b_{3})))) b_{4})) n_{335})) \\ \rightarrow (\lor (\lor (\lor (\neg (\land n_{343})) (\land (\land (\neg (\land n_{343} (\land (\land (\neg n_{343}))) (\land b_{3} (\land b_{4} (\lor n_{156} (\neg n_{337})))))))$

$$\rightarrow (\lor (\lor (\neg h_{335}) (\land b_4 (\neg h_{343}))) (\land b_3 (\land b_4 (\lor h_{156} (\neg h_{337})))) \\ (\land (\neg (\oplus (\land b_5 (\neg (\lor b_6 b_3))) (\lor b_4 (\lor b_9 (\neg b_6))))) (\neg b_2)) \\ \rightarrow (\oplus (\lor (\lor b_2 b_3) (\lor b_6 (\neg b_5))) (\lor (\lor b_2 b_4) (\lor b_9 (\neg b_6)))) \\ (\land (\neg (\land (\land b_5 (\neg b_3)) (\neg b_6))) (\neg (\land (\land (\neg b_9) b_6) (\neg b_4)))) \\ \rightarrow (\oplus (\land b_5 (\neg (\lor b_6 b_3))) (\lor b_4 (\lor b_9 (\neg b_6))))$$

 $(\land (\neg (\oplus (\land n_{256} (\land n_{267} (\land n_{46} n_{285}))) (\land n_{285} (\lor n_{267} (\neg n_{46}))))) n_{20})$ $\rightarrow (\oplus (\land n_{256} (\land n_{285} (\land n_{267} (\land n_{20} n_{46})))) (\land n_{20} (\lor (\neg n_{285}) (\land n_{46} (\neg n_{267})))))$ $(\land n_{267})))))$

$$(\neg (\land (\land (\neg n_{93}) n_{76}) (\neg (\land (\neg (\land (\land n_{68} (\neg n_{64})) (\neg n_{44}))) n_{20}))))) \rightarrow (\lor (\land n_{20} (\oplus n_{44} (\lor n_{64} (\neg n_{68})))) (\lor (\land n_{20} n_{44}) (\lor n_{93} (\neg n_{76})))))$$

 $(\land n_{1616} (\neg (\oplus (\land n_{1604} (\land n_{821} (\land n_{1610} n_{820}))) (\land n_{821} (\lor n_{820} (\neg n_{1610}))))))$ $\rightarrow (\oplus (\land n_{1604} (\land n_{1616} (\land n_{1610} (\land n_{820} n_{821})))) (\land n_{1616} (\lor (\neg n_{821}) (\land n_{1610} (\neg n_{820})))))$

 $(\land n_{1041} (\neg (\oplus (\land n_{1026} (\land n_{821} (\land n_{1032} n_{825}))) (\land n_{821} (\lor n_{825} (\neg n_{1032}))))))$ $\rightarrow (\oplus (\land n_{1026} (\land n_{1032} (\land n_{1041} (\land n_{825} n_{821})))) (\land n_{1041} (\lor (\neg n_{821}) (\land n_{1032}))))))$ $(\neg n_{825})))))$

 $(\land n_{1025} (\neg (\oplus (\land n_{1004} (\land n_{1013} (\land n_{820} n_{1018}))) (\land n_{820} (\lor n_{1013} (\neg n_{1018})))))))$ $\rightarrow (\oplus (\oplus n_{1004} (\land n_{1025} (\lor n_{1018} (\neg n_{820})))) (\lor n_{1004} (\land n_{1013} (\land n_{1025} (\land n_{10$ $n_{1018} n_{820})))))$ $(\land (\land (\neg (\land pi_{021} \ pi_{008})) \ (\neg \ pi_{013})) \ (\neg (\land (\neg (\land (\neg \ pi_{021}) \ (\neg \ pi_{008}))) \ pi_{007})))$ $\rightarrow (\oplus (\lor pi_{013} (\land pi_{021} (\oplus pi_{007} pi_{008}))) (\lor pi_{013} (\neg (\land pi_{007} pi_{008}))))$ $(\land (\neg (\land (\neg (\land (\neg (\land norm_7 (\neg pi_{096}))) pi_{097})) (\land (\neg pi_{100}) (\neg pi_{095})))) norm_7)$ $\rightarrow (\lor (\land pi_{097} (\land pi_{096} norm_7)) (\land norm_7 (\lor pi_{100} pi_{095})))$ $(\land (\land (\neg (\land (\neg pi_{110}) (\neg pi_{096}))) pi_{097})) (\land (\neg pi_{100}) (\neg pi_{095}))) pi_{097})$ $\rightarrow (\land (\neg (\lor pi_{096} (\lor pi_{100} pi_{095}))) (\land pi_{097} (\neg (\lor pi_{110} pi_{095}))))$ $(\land (\neg (\oplus (\land n_{483} (\land n_{1218} (\land n_{468} pi_{082}))) (\land n_{1218} (\lor pi_{082} (\neg n_{468}))))) n_{444})$ $\rightarrow (\oplus (\land n_{483} (\land n_{444} (\land n_{1218} (\land pi_{082} n_{468})))) (\land n_{444} (\lor (\neg n_{1218}) (\land n_{468}))))$ $(\neg pi_{082})))))$ $(\land (\neg (\land (\land i_{14} i_{13}) (\land i_{13} i_{12}))) (\neg (\land (\land i_{12} i_{11}) (\land i_{13} i_{12}))))$ $\rightarrow (\lor (\neg (\lor i_{11} \ i_{14})) \ (\neg (\land i_{12} \ i_{13})))$ $(\land (\neg (\land (\land i_{14} i_{13}) (\land i_{15} i_{14}))) (\neg (\land (\land i_{16} i_{15}) (\land i_{15} i_{14}))))$ $\rightarrow (\neg (\land (\lor i_{16} i_{13}) (\land i_{14} i_{15})))$ $(\land (\land (\land (\land (\neg i_{17}) (\neg i_{16})) (\neg i_{18})) (\land (\neg i_{15}) (\neg i_{12}))) (\neg i_{19}))$ $\rightarrow (\land (\neg (\lor i_{18} (\lor i_{16} i_{15}))) (\neg (\lor i_{19} (\lor i_{17} i_{12}))))$ $(\land norm_1 (\neg (\land (\neg (\land n_{133} (\neg (\land n_{116} n_{105})))) (\neg (\land n_{127} (\land n_{116} n_{105})))))))$ $\rightarrow (\land (\oplus (\land (\oplus n_{127} \ n_{133}) \ n_{105}) \ n_{116}) \ n_{133}) \ norm_1)$ $(\neg (\oplus (\land n_{138} (\land n_{103} (\land n_{96} n_{141}))) (\lor (\neg n_{145}) (\land n_{138} (\land n_{103} n_{96})))))$ $\rightarrow (\oplus (\land n_{96} (\land n_{138} (\land n_{103} n_{141}))) (\oplus n_{145} (\land n_{96} (\land n_{138} (\land n_{103} n_{145})))))$ $(\land (\neg (\oplus norm_3 (\neg (\land i_9 i_1)))) (\land (\neg (\oplus i_9 (\neg i_1))) (\land i_8 i_0)))$ $\rightarrow (\land (\land i_8 (\oplus i_9 i_1)) (\land i_0 norm_3))$ $(\land (\land (\neg (\oplus (\land (\neg n_{218}) (\oplus n_{168} \ n_{120})) (\land (\neg n_{168}) (\neg n_{120})))) \ norm_{10}) \ i_{27})$ $\rightarrow (\land (\land i_{27} \ norm_{10}) \ (\oplus \ n_{218} \ (\land (\oplus \ n_{120} \ n_{218}) \ (\oplus \ n_{168} \ n_{218}))))$ $(\land (\neg (\land n_{118} (\oplus (\land (\land op_3 opext_1) (\land op_4 opext_0)) (\land op_3 (\neg op_4))))) n_{53})$ $\rightarrow (\lor (\oplus n_{53} (\land n_{118} (\land n_{53} op_3))) (\land (\land n_{53} op_4) (\neg (\land opext_0 opext_1))))$

 $(\land (\neg (\oplus (\land oc_3 (\neg (\lor in_1 oc_0))) (\neg (\lor oc_3 (\land in_1 oc_2))))) (\neg oc_1))$ $\rightarrow (\oplus (\land (\land in_1 \ oc_2) \ (\neg (\lor oc_1 \ oc_3))) \ (\land (\lor oc_0 \ in_1) \ (\land oc_3 \ (\neg \ oc_1))))$ $(\land n_{1063} (\oplus (\land n_{1042} (\land n_{1055} (\land n_{22} n_{29}))) (\lor (\neg n_{22}) (\land n_{1055} (\neg n_{29})))))$ $\rightarrow (\oplus (\land n_{1042} (\land n_{29} (\land n_{1055} (\land n_{22} n_{1063})))) (\land n_{1063} (\lor (\neg n_{22}) (\land n_{1055} n_{1055})))) (\land n_{1063} (\lor (\neg n_{22}) (\land n_{1055} n_{1055})))) (\land n_{1063} (\lor (\neg n_{22}) n_{1065}))))$ $(\neg n_{29})))))$ $(\land (\neg (\land (\neg (\land s_0 a_{76})) s_1)) (\neg (\land (\neg (\land (\neg s_0) a_{79})) (\neg s_1))))$ $\rightarrow (\oplus (\land s_1 (\land s_0 a_{76})) (\land a_{79} (\neg (\lor s_1 s_0))))$ $(\land (\neg (\land (\neg (\land (\neg s_0) a_{95})) s_1)) (\neg (\land (\neg (\land s_0 a_{96})) (\neg s_1))))$ $\rightarrow (\oplus (\land s_1 (\lor s_0 a_{95})) (\land s_0 (\lor s_1 a_{96})))$ $(\land (\land n_{450} (\neg (\oplus norm_4 (\neg norm_{10})))) (\oplus (\land (\neg (\oplus norm_4 (\neg norm_{10}))))))$ $norm_{10}) (\neg norm_{10})))$ $\rightarrow (\land (\oplus norm_{10} norm_4) n_{450})$ $(\land (\land n_{135} (\neg (\land (\land n_{63} n_{130}) (\neg n_{77})))) (\neg (\land n_{127} (\neg (\land (\neg n_{77}) n_{63})))))$ $\rightarrow (\land (\oplus (\lor (\neg (\land (\oplus n_{130} \ n_{127}) \ n_{63})) \ n_{77}) \ n_{127}) \ n_{135})$ $(\land (\neg (\land (\neg (\land i_7 i_6)) i_9)) (\neg (\land (\land (\neg i_9) i_7) (\neg (\land i_6 i_5)))))$ $\rightarrow (\oplus (\neg (\land (\lor i_5 i_9) (\land i_6 i_7))) (\lor i_9 i_7))$ $(\land (\neg (\land n_{133} (\neg (\land n_{116} (\land n_{104} n_{96}))))) (\neg (\land (\land n_{127} n_{96}) (\land n_{104} n_{116}))))$ $\rightarrow (\oplus (\neg (\land (\land (\land (\oplus n_{127} \ n_{133}) \ n_{116}) \ n_{104}) \ n_{96})) \ n_{133})$ $(\land (\land n_{203} (\land n_{149} n_{137})) (\neg (\land (\neg (\land n_{171} n_{166})) (\neg (\land n_{158} (\land n_{149} n_{137})))))))$ $\rightarrow (\land (\lor (\land n_{171} \ n_{166}) \ n_{158}) (\land (\land n_{137} \ n_{149}) \ n_{203}))$ $(\land (\neg (\land (\land (\neg x_{13}) (\neg x_{12})) (\neg (\land (\neg (\land (\neg x_{10}) (\neg x_9))) x_{11})))) x_{14})$ $\rightarrow (\lor (\land (\lor x_9 x_{10}) (\land x_{14} x_{11})) (\land x_{14} (\lor x_{12} x_{13})))$ $(\land (\neg (\land (\land b_5 (\neg b_4)) b_3)) (\neg (\land (\land (\neg b_6) b_0) (\land b_4 (\neg b_3)))))$ $\rightarrow (\oplus (\land (\land b_4 \ b_0) \ (\neg (\lor b_3 \ b_6))) \ (\lor b_4 \ (\neg (\land b_3 \ b_5)))))$ $(\neg (\land (\neg (\land (\neg (\land n_{376} (\neg (\land (\neg (\land (\neg n_{367}) n_{332})) n_{301})))) pi_{054})) (\neg n_{326})))$ $\rightarrow (\land (\lor n_{326} (\land pi_{054} (\lor n_{301} (\neg n_{376})))) (\lor n_{326} (\lor n_{367} (\neg (\land n_{332} n_{376})))))$ $(\neg (\oplus (\neg (\lor pi_{039} (\lor pi_{052} (\lor pi_{051} pi_{027})))) (\lor pi_{026} (\neg (\lor pi_{039} (\lor pi_{052}$

 $\rightarrow (\oplus (\neg (\lor (\lor pi_{051} \ pi_{052}) \ (\lor \ pi_{039} \ pi_{026}))) \ (\oplus \ pi_{026} \ (\lor (\lor \ pi_{051} \ pi_{052}) \ (\lor$

 $pi_{039} pi_{027})))))$

 $(\land (\neg (\oplus (\land (\neg norm_5) i_{22}) (\land norm_5 i_6))) (\oplus (\land (\neg norm_5) i_{23}) (\land norm_5 i_7)))$

 $\rightarrow (\oplus (\land norm_5 (\lor i_6 (\neg i_7))) (\lor norm_5 (\land i_{23} (\neg i_{22}))))$

 $(\land (\oplus (\land n_{166} (\land n_{171} n_{177})) (\lor (\land n_{166} n_{171}) norm_6)) (\neg (\land (\land n_{171} n_{166})))$

 $norm_6))))$

 $\rightarrow (\land (\neg (\land n_{166} (\land n_{171} n_{177}))) (\oplus norm_6 (\land n_{171} n_{166})))$ $(\land (\neg (\land n_{145} (\neg (\land (\land n_{103} n_{96}) n_{138})))) (\neg (\land n_{141} (\land (\land n_{103} n_{96}) n_{138}))))$ $\rightarrow (\oplus (\land n_{138} (\land n_{103} (\land n_{96} n_{141}))) (\lor (\neg n_{145}) (\land n_{138} (\land n_{103} n_{96}))))$ $(\land (\land n_{135} (\neg (\land (\land n_{63} n_{130}) (\neg n_{77})))) (\neg (\land n_{127} (\neg (\land (\neg n_{77}) n_{63})))))$ $\rightarrow (\oplus (\land (\land n_{127} \ n_{135}) \ (\lor \ n_{77} \ (\neg \ n_{63}))) \ (\land \ n_{135} \ (\lor \ n_{77} \ (\neg \ (\land \ n_{63} \ n_{130})))))$ $(\land (\land (\oplus (\land (\neg n_{218}) (\oplus n_{168} n_{120})) (\land (\neg n_{168}) (\neg n_{120}))) (\oplus n_{218} n_{170})) i_{33})$ $\rightarrow (\land (\oplus (\lor n_{168} \ n_{218}) \ (\lor \ n_{120} \ (\oplus \ n_{168} \ n_{170}))) \ (\land \ i_{33} \ (\oplus \ n_{218} \ n_{170})))$ $(\neg (\land (\land (\oplus (\land (\neg n_{218}) (\oplus n_{168} n_{120})) (\land (\neg n_{168}) (\neg n_{120}))) (\neg n_{219})) i_{33}))$ $\rightarrow (\lor (\oplus n_{218} (\land (\oplus n_{120} n_{218}) (\oplus n_{168} n_{218}))) (\oplus (\land n_{168} n_{218}) (\lor n_{219} (\neg$ $(i_{33})))))$ $(\land (\neg (\land (\neg op_4) op_3)) (\oplus (\land (\lor op_3 op_0) (\land op_1 op_4)) (\land op_1 (\neg op_4))))$ $\rightarrow (\oplus (\land op_1 (\neg (\lor op_4 op_3))) (\land (\lor op_0 op_3) (\land op_4 op_1)))$ $(\land (\neg (\land (\land ct_0 (\neg oc_2)) (\land (\neg in_1) (\neg oc_1)))) (\neg (\land (\land in_1 norm_{11}) oc_1)))$ $\rightarrow (\lor (\oplus in_1 (\lor oc_1 (\oplus oc_2 (\neg ct_0)))) (\oplus (\lor in_1 oc_2) (\land oc_1 norm_{11})))$ $(\land (\land (\neg (\land (\neg in_1) oc_1)) (\neg ct_2)) (\neg (\land (\neg (\land (\neg in_1) oc_0)) (\neg oc_1))))$ $\rightarrow (\land (\neg (\lor ct_2 (\oplus oc_1 in_1))) (\oplus ct_2 (\lor in_1 oc_0)))$ $(\land (\neg (\land (\land ct_0 \ norm_4) \ (\land in_1 \ in_0))) \ (\neg (\land (\land (\neg in_1) \ (\neg ct_0)) \ (\neg in_0))))$ $\rightarrow (\lor (\land ct_0 (\oplus in_1 \ norm_4)) (\lor (\oplus in_0 \ ct_0) (\oplus in_1 \ ct_0)))$ $(\land (\neg (\lor (\oplus (\lor oc_2 oc_0) (\lor in_0 in_1)) (\lor (\oplus oc_2 oc_0) (\oplus in_0 in_1)))) ct_0)$ $\rightarrow (\land (\land ct_0 (\oplus in_0 (\neg in_1))) (\neg (\lor (\oplus oc_0 in_1) (\oplus oc_2 in_0))))$ $(\oplus (\land (\oplus n_{150} (\land n_{345} (\land n_{326} n_{333}))) (\oplus n_{150} (\land n_{336} (\neg (\land n_{345} n_{326}))))) n_{335})$ $\rightarrow (\oplus (\land n_{345} (\land n_{326} (\land n_{150} (\oplus n_{333} n_{336})))) (\oplus n_{335} (\land n_{150} (\neg n_{336}))))$

 $(\land (\neg (\land (\land (\neg ct_1) ct_0) (\land in_1 in_0))) (\neg (\land (\land (\neg in_1) (\neg ct_0)) (\neg in_0))))$

 $\rightarrow (\lor (\lor (\oplus in_0 ct_0) (\oplus in_1 ct_0)) (\land ct_0 ct_1))$ $(\land (\land (\oplus n_{313} n_{273}) norm_4) (\oplus (\land (\oplus n_{313} n_{273}) norm_4) (\land (\neg n_{313}) (\neg n_{313}))))$ $(n_{273})))) n_{322})$ $\rightarrow (\land (\land norm_4 \ n_{322}) \ (\neg (\lor n_{313} \ n_{273})))$ $(\land (\land (\land n_{387} (\oplus norm_4 (\neg norm_{10}))) (\oplus (\land (\neg (\oplus norm_4 (\neg norm_{10}))))))$ $norm_{10}) (\neg norm_{10}))) i_{26})$ $\rightarrow (\land (\neg (\lor norm_4 norm_{10})) (\land i_{26} n_{387}))$ $(\land (\neg (\land (\neg (\land i_8 i_7)) i_5)) (\oplus (\lor (\lor (\neg i_6) i_5) (\land (\neg i_8) i_7)) i_6))$ $\rightarrow (\oplus (\neg (\lor (\land (\oplus i_6 i_8) i_7) (\oplus i_5 i_8)))) i_8)$ $(\land (\oplus (\land (\lor (\neg i_7) i_6) (\land (\neg i_9) i_8)) i_8) (\neg (\land (\land i_9 i_7) (\neg i_6))))$ $\rightarrow (\land (\oplus (\land (\neg i_6) i_7) i_9) i_8)$ $(\land (\neg (\land (\neg (\oplus (\land n_{1176} (\land n_{468} pi_{082})) (\land n_{468} (\neg pi_{082}))))) (\neg (\land n_{1376}))))$ $pi_{082})))) n_{437})$ $\rightarrow (\oplus (\land (\lor n_{1176} \ n_{1376}) \ (\land n_{437} \ (\land n_{468} \ pi_{082}))) \ (\land (\lor n_{468} \ n_{1376}) \ (\land n_{437} \ (\oplus n_{437} \ (\land n_{437} \ (\oplus n_{437} \ (\land n_{437} \ (\oplus n_{437} \ (\land n_$ $n_{468} pi_{082})))))$ $(\land (\neg (\land (\land i_{11} \ i_{10}) \ (\land i_9 \ i_8))) \ (\neg (\land (\land i_{12} \ i_{11}) \ (\land (\land i_{11} \ i_{10}) \ i_9))))$ $\rightarrow (\lor (\land i_{11} (\neg (\land i_9 i_{10}))) (\neg (\land i_{11} (\lor i_{12} i_8))))$ $(\land (\neg (\land (\neg (\land (\neg i_{62}) (\neg i_{30}))) (\neg (\land (\neg i_{61}) (\neg i_{29}))))) (\neg (\land i_{61} i_{29})))$ $\rightarrow (\oplus (\lor (\oplus (\lor i_{30} i_{62}) i_{29}) (\oplus (\neg i_{29}) i_{61})) i_{61})$ $(\neg (\land (\land n_{135} (\neg (\land (\land n_{63} n_{130}) (\neg n_{77})))) (\neg (\land n_{127} (\neg (\land (\neg n_{77}) n_{63}))))))$ $\rightarrow (\oplus (\land (\land n_{63} \ n_{135}) \ (\neg (\lor n_{77} \ n_{130}))) \ (\lor (\lor n_{127} \ (\neg \ n_{135})) \ (\land n_{63} \ (\neg \ n_{77}))))$

 $(\land (\land (\neg (\land n_{342} (\land n_{313} n_{303}))) (\neg (\land (\land n_{362} n_{352}) n_{318}))) (\land (\neg n_{2640}) (\neg n_{2638})))$

 $\begin{array}{l} \rightarrow (\land (\neg (\lor n_{2640} (\land n_{313} (\land n_{303} n_{342})))) (\neg (\lor n_{2638} (\land n_{352} (\land n_{362} n_{318}))))) \\ (\land (\neg (\oplus (\lor (\neg (\land op_3 op_4)) (\lor op_1 op_0)) op_1)) (\neg (\land (\neg (\land op_4 (\neg op_3))) op_1))) \\ \rightarrow (\land (\land (\oplus op_1 op_3) op_4) (\lor (\neg op_0) op_1)) \end{array}$

 $(\land (\land (\oplus (\oplus n_{313} \ n_{273}) \ norm_4) \ (\neg (\oplus (\land (\oplus n_{313} \ n_{273}) \ norm_4) \ (\land (\neg n_{313}) \ (\neg n_{273}))))) \ n_{322})$

 $\rightarrow (\land (\land (\oplus (\oplus norm_4 \ n_{313}) \ n_{273}) \ n_{322}) \ (\lor \ n_{313} \ n_{273}))$

 $(\land (\land (\oplus norm_3 (\oplus n_{168} n_{120})) (\neg (\oplus (\land norm_3 (\oplus n_{168} n_{120})) (\land (\neg n_{168}) (\neg n_{168}))))))$ $n_{120}))))) n_{226})$ $\rightarrow (\land (\land (\oplus (\oplus norm_3 n_{168}) n_{120}) n_{226}) (\lor n_{168} n_{120}))$ $(\land (\land (\oplus n_{216} (\neg n_{119})) (\neg (\oplus (\land (\neg n_{215}) (\neg n_{167})) (\neg (\land (\neg n_{216}) (\neg$ $n_{119})))))) n_{225})$ $\rightarrow (\land (\land (\oplus (\neg n_{216}) \ n_{119}) \ n_{225}) (\oplus (\lor n_{167} \ n_{215}) \ n_{119}))$ $(\land (\land (\land n_{373} (\neg (\oplus norm_5 (\neg norm_{12})))) (\oplus (\land (\neg (\oplus norm_5 (\neg norm_{12}))))))$ $norm_{12}) (\neg norm_{12}))) i_{24})$ $\rightarrow (\land (\land (\oplus norm_5 norm_{12}) i_{24}) n_{373})$ $(\land (\neg (\land (\oplus (\land (\lor y_{12} y_{11}) (\lor y_9 y_{10})) (\lor y_9 (\lor y_{10} (\neg y_{12})))) (\neg y_{13}))) y_{14})$ $\rightarrow (\lor (\land y_{14} (\lor y_{12} y_{13})) (\land (\lor y_{10} y_{9}) (\land y_{14} y_{11})))$ $(\land (\neg (\land (\neg norm_4) (\neg (\land n_{171} n_{166})))) (\neg (\land norm_4 (\oplus n_{158} (\land n_{166} (\land n_{171} n_{166}))))))))$ $\rightarrow (\land (\oplus (\land n_{171} \ n_{166}) \ norm_4) \ (\oplus \ n_{158} \ (\neg (\land n_{166} \ (\land n_{158} \ n_{171})))))$ $(\land (\neg (\oplus (\land i_3 i_4) (\lor i_3 i_2))) (\land (\neg (\land (\neg i_2) i_1)) (\neg (\land (\neg i_1) i_0))))$ $\rightarrow (\land (\oplus i_2 (\neg (\lor i_1 (\oplus i_0 i_2)))) (\oplus i_4 (\neg (\lor i_3 (\oplus i_2 i_4)))))$ $(\land (\neg (\land (\land opext_0 op_1) (\neg opext_1)) (\land op_4 op_3))) (\neg (\land (\land (\neg op_4) op_3) (\neg$ $op_1))))$ $\rightarrow (\lor (\land op_1 (\lor opext_1 (\neg opext_0))) (\lor (\neg op_3) (\oplus op_1 op_4)))$ $(\land (\neg (\land (\land ct_0 (\neg oc_2)) in_0)) (\neg (\land (\neg (\oplus (\land in_0 (\neg ct_0)) (\lor in_0 oc_3))) oc_2)))$ $\rightarrow (\lor (\land oc_3 (\neg in_0)) (\oplus oc_2 (\neg (\land ct_0 in_0))))$ $(\land (\neg (\land (\neg (\land (\neg (\land ct_1 (\neg oc_2))) oc_3)) (\land (\neg in_1) (\neg oc_1)))) (\neg (\land in_1 oc_2)))$ $\rightarrow (\lor (\land oc_3 (\oplus in_1 (\lor oc_2 (\neg ct_1)))) (\oplus oc_2 (\lor in_1 (\oplus oc_2 oc_1))))$ $(\land (\land (\oplus (\neg n_{217}) (\oplus n_{168} \ n_{120})) (\oplus (\land (\neg n_{217}) (\oplus n_{168} \ n_{120})) (\land (\neg n_{168}) (\neg n_{168})))$ $(n_{120})))) n_{226})$ $\rightarrow (\land (\neg (\lor n_{217} \ n_{168})) (\land (\neg n_{120}) \ n_{226}))$ $(\land (\oplus (\land (\oplus n_{313} \ n_{273}) \ (\oplus n_{216} \ n_{192})) \ (\land (\neg n_{313}) \ (\neg n_{273}))) \ (\land (\oplus n_{313} \ n_{273}))$ $(\oplus n_{216} n_{192})))$

 $\rightarrow (\land (\oplus n_{192} \ n_{216}) \ (\oplus n_{313} \ n_{273}))$

$$(\land (\land (\land (i_{11} i_{10}) (\land i_{9} i_{8})) (\land i_{8} i_{7})) (\land (\land i_{12} i_{11}) (\land (\land i_{11} i_{10}) i_{9}))) \rightarrow (\land (\land i_{9} (\land i_{10} i_{7})) (\land i_{11} (\land i_{12} i_{8}))) (\land (\land (i_{14} i_{13}) (\land i_{13} i_{12})) (\land (\land (\land i_{12} i_{11}) (\land i_{13} i_{12}))) (\land (\land i_{11} i_{10}) i_{9}))) \rightarrow (\land (\land i_{9} (\land i_{10} i_{14})) (\land i_{11} (\land i_{13} i_{12}))) (\land (\land (\land i_{6} i_{7}) (\land i_{5} i_{4}))) (\neg (\land (\land i_{4} i_{3}) (\land (\land i_{5} i_{4}) (\land i_{6} i_{5}))))) \rightarrow (\lor (\land i_{6} (\neg (\land i_{5} i_{4}))) (\neg (\land (i_{6} (\lor i_{3} i_{7})))) (\land (\land (\land n_{202} n_{199}) (\land n_{149} n_{137})) (\neg (\land (\lor (\neg n_{165}) (\neg norm_{9})) (\neg (\land n_{158} (\land n_{149} n_{137}))))) \rightarrow (\land (\lor n_{158} (\land n_{165} norm_{9})) (\land n_{137} (\land n_{202} (\land n_{149} n_{199})))) (\land n_{203} (\oplus (\land (\land n_{117} n_{130}) (\land n_{149} (\land n_{127} n_{133}))) (\land n_{149} (\oplus (\land n_{117} n_{127}))) (\land n_{203} (\oplus (\land (\land n_{117} n_{130}) (\land n_{149} (\land n_{137} n_{133})))) \rightarrow (\land (\land (\land n_{149} n_{203}) (\lor (\land n_{130} n_{133}) (\land n_{117} n_{127})))) (\land (\neg (\land (\land n_{149} n_{203}) (\lor (\land n_{130} n_{133}) (\land n_{117} n_{127})))) (\land (\neg (\land (\land n_{149} n_{203}) (\lor (\land n_{130} n_{133}) (\land n_{117} n_{127})))) (\land (\neg (\land (\land n_{149} n_{203}) (\lor (\land n_{130} n_{133}) (\land n_{117} n_{127})))) (\land (\neg (\land (\land n_{149} n_{203}) (\lor (\land n_{130} n_{133}) (\land n_{117} n_{127})))) (\land (\neg (\land (\land n_{149} n_{203}) (\lor (\land n_{130} n_{133}) (\land n_{117} n_{127})))) (\land (\neg (\land (\land n_{384} n_{369}) n_{387}) (\land n_{384} n_{369})) n_{387}) (\land n_{384} n_{369})) (\neg (\land (\land n_{393} (\oplus (\land n_{384} n_{369}) n_{387}) (\land n_{384} n_{369}))) (\land (\land (\land n_{393} (\oplus (\land n_{384} n_{369}) n_{387}) n_{393}) (\oplus (\land (\land n_{59} (\land n_{59}) (\land i_{16} (\oplus n_{102} (\neg n_{171}))))) (\land (\land (\land n_{12} (\land n_{59} (\oplus n_{152} n_{152})) (\land (n_{16} (\land n_{188} (\land i_{23} n_{152})) (\land i_{23} ((\oplus n_{176} n_{199}) (\oplus i_{23} n_{156}))))) (\land (\land (\land (n_{12} (\land n_{120} n_{277})) (\land (\land (\land n_{121} (\land n_{120} (\neg n_{297})))))) (\land (\land (\land (n_{121} (\land n_{120} n_{277}))) (\land n_{267} (\land n_{121} (\land n_{120} (\neg n_{297}))))))) (\land ((\land (\land (\neg (\land (\neg (n_{12} (\land n_{121} n_{120}))))) ($$

 $(\oplus (\land n_{199} (\neg (\oplus (\land n_{195} (\oplus norm_6 n_{189})) (\oplus norm_6 n_{189})))) (\oplus (\land n_{195} (\oplus$ $norm_6 n_{189})) (\oplus norm_6 n_{189})))$ $\rightarrow (\oplus (\land (\neg n_{199}) (\lor n_{189} \ n_{195})) (\lor (\lor n_{195} \ n_{199}) \ norm_6))$ $(\land (\neg (\land (\land (\land (\neg pi_{052}) (\neg pi_{039})) (\neg pi_{051})) (\neg pi_{027}))) (\neg (\oplus (\lor (\lor pi_{026}))) (\neg pi_{027})))$ $pi_{051}) (\lor pi_{052} pi_{039})) pi_{026})))$ $\rightarrow (\oplus (\land (\lor (\lor pi_{051} pi_{052}) pi_{039}) (\oplus pi_{026} pi_{027})) pi_{027})$ $(\land (\land (\neg (\land (\neg (\land i_7 i_6)) (\neg i_3))) i_4) (\neg (\land (\land (\oplus (\neg i_2) i_6) i_3) (\lor (\neg i_6) i_7))))$ $\rightarrow (\land (\lor (\land (\oplus i_7 i_3) i_6) (\land (\oplus i_6 i_2) i_3)) i_4)$ $(\land (\neg (\land (\land (\land (\neg pi_{052}) (\neg pi_{039})) (\neg pi_{051})) (\neg pi_{027}))) (\neg (\oplus pi_{026} (\lor (\lor$ $pi_{026} pi_{051}) (\lor pi_{052} pi_{039})))))$ $\rightarrow (\oplus (\neg (\lor pi_{039} (\lor pi_{052} (\lor pi_{051} pi_{027})))) (\lor pi_{026} (\neg (\lor pi_{039} (\lor pi_{052}$ $pi_{051}))))))$ $(\land (\land (\land (\land i_{14} i_{13}) (\land i_{15} i_{14})) (\land (\land i_{12} i_{11}) (\land i_{13} i_{12}))) (\land (\land i_{16} i_{15}) (\land i_{15})$ $(i_{14})))$ $\rightarrow (\land (\land i_{14} (\land i_{16} i_{12})) (\land i_{15} (\land i_{11} i_{13})))$ $(\land (\land (\land (\land i_{14} i_{13}) (\land i_{15} i_{14})) (\land (\land i_{17} i_{16}) (\land i_{16} i_{15}))) (\land (\land i_{18} i_{17}) (\land i_{17} i_{16}) (\land i_{16} i_{15})))$ $i_{16})))$ $\rightarrow (\land (\land i_{16} (\land i_{18} i_{15})) (\land i_{17} (\land i_{14} i_{13})))$ $(op_1))) (op_3)))$ $\rightarrow (\land (\neg (\lor op_2 (\land op_0 (\oplus op_3 op_4)))) (\oplus (\lor op_3 op_2) (\lor op_1 (\neg op_4))))$ $(\land (\neg (\oplus (\land ct_1 (\lor oc_0 (\neg in_1))) (\lor ct_1 (\lor oc_3 (\neg oc_0))))) (\neg (\land (\neg (\land in_1 ct_1))))$ $(\neg in_0))))$ $\rightarrow (\oplus (\land (\land in_0 \ oc_0) \ (\neg (\lor ct_1 \ oc_3))) \ (\land ct_1 \ (\oplus in_0 \ (\land in_1 \ (\oplus in_0 \ oc_0)))))$ $(\land (\neg (\land (\land in_1 oc_1) ct_0)) (\neg (\land (\neg (\land (\neg ct_0) (\neg oc_1))) oc_2)) (\land (\neg in_1))) (\land (\neg in_1))) (\land (\neg in_1)) (\land (\neg in_1)) (\land (\neg in_1)) (\land (\neg in_1))) (\land (\neg in_1)) ((\land in_1))$ $(\neg ct_1)))))$ $\rightarrow (\oplus (\land in_1 (\land oc_1 ct_0)) (\lor (\lor ct_1 in_1) (\land oc_2 (\lor oc_1 ct_0))))$ $(\oplus (\land (\neg (\oplus (\oplus (\land i_{16} i_8) i_7) i_{15})) (\land (\oplus i_{16} i_8) (\land i_{17} i_9))) (\land (\oplus i_{16} i_8) (\land i_{17}))$ $i_{9})))$

 $\rightarrow (\land (\land (\oplus i_{16} i_8) (\oplus i_{15} i_7)) (\land i_9 i_{17}))$

 $(\oplus (\land n_{305} (\oplus (\land (\oplus n_{264} n_{257}) (\neg n_{254})) (\neg n_{254}))) (\land n_{305} (\neg (\oplus (\land (\oplus n_{264} n_{257}) (\neg n_{254})) (\neg n_{254})))))$

 $\rightarrow n_{305}$

 $(\oplus (\oplus (\wedge (\oplus n_{188} \ n_{175}) \ (\wedge \ n_{138} \ n_{152})) \ (\wedge \ n_{138} \ (\neg \ n_{199}))) \ (\wedge (\oplus \ (\oplus \ n_{199} \ (\wedge n_{188} \ n_{152})) \ (\wedge \ n_{175} \ n_{152})) \ i_{18}))$

 $\rightarrow (\oplus (\land (\oplus n_{188} \ n_{175}) \ (\land n_{152} \ (\oplus \ i_{18} \ n_{138}))) \ (\oplus \ n_{138} \ (\land \ n_{199} \ (\oplus \ i_{18} \ n_{138}))))$ $(\land (\neg (\oplus (\land (\lor \ op_1 \ op_0) \ (\land \ op_3 \ op_4)) \ (\oplus \ op_1 \ (\neg (\land \ op_3 \ op_4)))))) \ (\neg (\land (\neg (\land \ op_4 \ (\neg \ op_3))) \ op_1)))$

 $\rightarrow (\land (\land op_4 (\oplus op_1 op_3)) (\oplus op_4 (\land op_3 op_0)))$

 $(\land (\neg (\land (\neg oc_3) oc_1)) (\neg (\oplus (\land (\land in_1 oc_2) (\neg (\lor oc_1 oc_3))) (\land (\lor oc_0 in_1) (\land oc_3 (\neg oc_1))))))$

 $\rightarrow (\oplus (\oplus oc_3 (\lor (\lor oc_1 oc_3) (\land in_1 oc_2))) (\lor (\lor oc_1 (\neg oc_3)) (\neg (\lor oc_0 in_1)))) \\ (\land (\neg (\oplus (\lor pi_{013} (\land pi_{021} (\oplus pi_{007} pi_{008}))) (\lor pi_{013} (\neg (\land pi_{007} pi_{008}))))) (\neg (\land (\land (\neg pi_{021}) (\neg pi_{003})) (\neg pi_{007}))))$

 $\rightarrow (\oplus (\land (\oplus pi_{007} \ pi_{021}) \ (\oplus \ pi_{008} \ pi_{013})) \ (\lor (\land pi_{007} \ pi_{021}) \ (\land \ pi_{008} \ pi_{013})))$

 $(\land (\neg (\land (\land op_3 (\neg op_0)) op_4) opext_0)) (\neg (\land (\oplus (\land (\land op_3 opext_1) (\land op_4 opext_0)) (\land op_3 (\neg op_4))) op_0)))$

 $\rightarrow (\lor (\oplus op_0 (\neg (\land op_4 (\oplus op_0 opext_0)))) (\neg (\land op_3 (\lor opext_1 (\oplus op_0 op_4))))) \\ (\land (\land (\neg (\land (\oplus op_3 op_4) op_2)) op_1) (\neg (\land (\oplus (\land (\lor op_3 op_0) (\land op_1 op_4)) (\land op_1 (\neg op_4))) (\neg op_2))))$

 $\rightarrow (\land (\lor op_2 (\neg (\lor op_3 op_0))) (\land op_1 (\oplus op_2 (\oplus op_3 op_4))))$

 $\rightarrow (\land (\lor op_2 (\land op_0 op_3)) (\lor (\land op_1 opext_0) (\lor op_2 (\neg op_4))))$

 $(\land (\neg (\oplus (\oplus n_{379} (\land n_{359} (\oplus n_{272} n_{254}))) (\land (\oplus n_{359} (\oplus n_{272} n_{254})) n_{186}))) (\land (\oplus (\oplus n_{359} (\oplus n_{272} n_{254})) n_{186}) n_{157}))$

 $\rightarrow (\land (\oplus \ n_{254} \ (\oplus \ n_{272} \ (\oplus \ n_{359} \ n_{186}))) \ (\land \ n_{157} \ (\oplus \ n_{379} \ (\neg \ (\land \ n_{359} \ n_{186})))))$

 $(\land (\oplus (\land (\oplus n_{312} n_{288}) (\oplus n_{167} n_{143})) norm_6) (\land (\oplus n_{312} n_{288}) (\oplus n_{167} n_{143}))$

 $(n_{143})) (\land (\oplus (\oplus n_{312}, n_{288}) (\oplus n_{167}, n_{143})) norm_6))$ $\rightarrow (\land (\oplus (\oplus (\oplus n_{288} n_{143}) n_{312}) n_{167}) norm_6)$ $(\land (\land (\land norm_3 norm_7) (\neg (\land (\neg (\land b_4 b_1)) (\neg b_0)))) (\neg (\land (\neg (\land (\neg (\land b_8 (\neg (\land b_1 (\land b_1 (\land b_2 ()$ $(b_4))) (\neg (\land b_4 \ b_1))) \ b_0)))$ $\rightarrow (\land (\land norm_3 (\oplus b_0 (\land b_4 b_1))) (\land norm_7 (\lor b_4 (\oplus b_0 b_8))))$ $(\land (\neg (\oplus (\land (\neg (\oplus n_{148} n_{133})) i_{20}) (\land (\oplus n_{148} n_{133}) i_{4}))) (\neg (\oplus (\land (\neg (\oplus n_{148}) n_{133}) i_{4}))))$ $(n_{133})(i_{21}) (\land (\oplus n_{148} n_{133}) i_5))))$ $\rightarrow (\oplus (\land (\lor i_{21} \ i_{20}) \ (\oplus \ n_{148} \ (\neg \ n_{133}))) \ (\neg (\land (\oplus \ n_{148} \ n_{133}) \ (\lor \ i_5 \ i_4))))$ $(\land (\neg (\land (\neg (\lor op_2 (\land op_0 (\oplus op_3 op_4)))) (\oplus (\lor op_3 op_2) (\lor op_1 (\neg op_4))))) (\neg$ $(\land (\neg (\land op_4 (\neg op_3))) op_2)))$ $\rightarrow (\lor (\neg (\lor op_2 (\oplus op_3 (\lor op_1 (\neg op_4))))) (\land (\lor op_2 op_0) (\land op_4 (\neg op_3))))$ $(\oplus op_4 (\land op_3 op_0)))) (\neg op_2))))$ $\rightarrow (\land (\oplus op_3 (\lor op_1 (\land op_3 op_0))) (\oplus op_2 (\lor op_4 (\land op_2 op_3))))$ $(\neg (\land (\neg (\land (\land (\neg in_0) ct_0) (\land in_1 oc_2))) (\neg (\oplus (\land (\land in_0 ct_0) (\neg (\lor in_1 oc_0))))$ $(\land in_0 (\land oc_0 (\oplus in_1 oc_2)))))))$ $\rightarrow (\oplus (\land (\land oc_0 in_0) (\oplus oc_2 (\lor in_1 ct_0))) (\land (\lor oc_2 in_0) (\land ct_0 (\oplus in_1 in_0))))$ $(\neg (\land (\neg (\land (in_1 in_0) (\neg oc_3))) (\neg (\land (\neg (\oplus (\land (\lor oc_0 in_1) (\land in_0 oc_3)) (\oplus$ $oc_3 (\neg (\land oc_0 in_0)))) (\neg ct_1))))$ $\rightarrow (\lor (\land oc_0 (\land in_0 (\neg ct_1))) (\oplus oc_3 (\lor (\land ct_1 oc_3) (\land in_1 in_0))))$ $(\land (\neg (\land (\land (\neg pi_{021}) (\neg pi_{008})) (\land (\neg pi_{013}) (\neg pi_{007})))) (\neg (\oplus (\land (\oplus pi_{007}))))$ $pi_{021}) (\oplus pi_{008} pi_{013})) (\lor (\land pi_{007} pi_{021}) (\land pi_{008} pi_{013})))))$ $\rightarrow (\land (\oplus pi_{013} (\oplus pi_{007} (\lor pi_{008} pi_{021}))) (\oplus (\lor pi_{013} pi_{021}) (\lor pi_{008} pi_{007})))$ $(\oplus (\land (\oplus n_{313} n_{273}) (\oplus n_{216} n_{192})) (\land (\neg n_{313}) (\neg n_{273}))) (\land (\oplus n_{313}) (\neg n_{273})))$ $(n_{273}) (\oplus n_{216} n_{192}))) (\neg (\land (\oplus n_{313} n_{273}) (\oplus n_{216} n_{192}))))$ $\rightarrow 1$ $(\land (\neg (\oplus (\land n_{207} (\oplus n_{200} (\neg n_{193}))) (\land (\neg (\oplus n_{200} (\neg n_{193}))) n_{153}))) (\oplus (\land$

 $n_{234} (\oplus n_{200} (\neg n_{193}))) (\land (\neg (\oplus n_{200} (\neg n_{193}))) n_{180})))$

 $\rightarrow (\oplus (\land (\oplus n_{200} \ n_{193}) \ (\lor \ n_{153} \ (\neg \ n_{180}))) \ (\lor \ (\oplus \ n_{200} \ n_{193}) \ (\land \ n_{234} \ (\neg \ n_{207}))))$

$$(\land (\neg (\oplus (\land n_{207} (\oplus n_{200} (\neg n_{193}))) (\land (\neg (\oplus n_{200} (\neg n_{193}))) n_{153}))) (\neg (\oplus (\land n_{234} (\oplus n_{200} (\neg n_{193}))) (\land (\neg (\oplus n_{200} (\neg n_{193}))) n_{180}))))$$

 $\rightarrow (\oplus \ (\neg \ (\lor \ (\oplus \ n_{200} \ n_{193}) \ (\lor \ n_{234} \ n_{207}))) \ (\land \ (\oplus \ n_{200} \ n_{193}) \ (\neg \ (\lor \ n_{180} \ n_{153}))))$ $n_{214} (\oplus n_{200} (\neg n_{193}))) (\land (\neg (\oplus n_{200} (\neg n_{193}))) n_{189}))))$ $\rightarrow (\oplus (\neg (\lor (\oplus n_{200} \ n_{193}) (\lor n_{214} \ n_{222}))) (\land (\oplus n_{200} \ n_{193}) (\neg (\lor n_{189} \ n_{185}))))$ $(\land (\neg (\land (\land (\neg oc_3) (\neg oc_0))) (\neg oc_2)) (\oplus (\land oc_1 ct_0) (\lor ct_0 (\neg oc_3)))))$ $(\neg (\land (\land (\neg ct_0) oc_0) (\land (\neg oc_3) (\neg oc_1)))))$ $\rightarrow (\lor (\oplus oc_0 (\neg (\land oc_3 (\oplus ct_0 oc_0)))) (\oplus oc_2 (\land (\oplus oc_1 oc_2) (\oplus ct_0 oc_2))))$ $(\land (\neg (\oplus (\land n_{203} (\neg (\oplus (\land (\neg n_{199}) (\lor n_{189} n_{195})) (\lor (\lor n_{195} n_{199}) norm_{11}))))$ $(\oplus (\land (\neg n_{199}) (\lor n_{189} \ n_{195})) (\lor (\lor n_{195} \ n_{199}) \ norm_{11}))) n_{188})$ $\rightarrow (\land (\land n_{188} (\neg (\lor n_{203} n_{199}))) (\lor n_{195} (\oplus n_{189} (\neg norm_{11}))))$ $(\land (\neg (\oplus (\neg (\land (\oplus i_4 i_8) i_7) (\lor i_4 i_9))) i_8)) (\neg (\land (\land (\neg (\land i_9 i_7)) i_8) (\neg (\land$ $(\neg (\land (\neg i_9) (\neg i_7))) (\neg i_4))))))$ $\rightarrow (\land (\oplus (\land (\oplus i_9 i_7) i_8) i_4) (\lor (\neg i_4) i_7))$ $(\land (\neg (\oplus (\land n_{222} (\oplus n_{200} (\neg n_{193}))) (\neg (\land (\neg (\oplus n_{200} (\neg n_{193}))) n_{185})))) (\neg (\oplus n_{193}))) (\neg (\oplus n_{193})) (\neg (\oplus n_{193}))) (\neg (\oplus n_{193})) (\neg (\oplus n_{193}))) (\neg (\oplus n_{193})) (\neg (\oplus n_{193})) (\neg (\oplus n_{193}))) (\neg (\oplus n_{193})) (\neg (\oplus n_{193})) (\neg (\oplus n_{193}))) (\neg (\oplus n_{193})) (\neg (\oplus$ $(\land n_{214} (\oplus n_{200} (\neg n_{193}))) (\land (\neg (\oplus n_{200} (\neg n_{193}))) n_{189}))))$ $\rightarrow (\oplus (\land (\oplus n_{200} \ n_{193}) (\lor n_{189} \ (\neg \ n_{185}))) (\lor (\oplus n_{200} \ n_{193}) (\land n_{222} \ (\neg \ n_{214}))))$ $(\neg (\land (\neg (\land (\neg (\land (\neg pi_{116}) pi_{037})) (\neg (\land pi_{058} (\neg pi_{026}))))) (\neg (\land (\neg pi_{116}) pi_{037})) (\neg (\land pi_{058} (\neg pi_{026}))))))$ $pi_{116}(pi_{058})))) (\neg (\land (\land pi_{094} (\oplus pi_{026} pi_{058})) (\lor pi_{116} (\oplus pi_{094} pi_{026}))))))$ $\rightarrow (\oplus (\land (\oplus pi_{026} \ pi_{037}) \ (\neg (\lor pi_{116} \ pi_{058}))) \ (\land (\oplus pi_{026} \ pi_{058}) \ (\lor pi_{094} \ (\oplus pi_{016} \ pi_{016}$ $pi_{026} pi_{116}))))$ $(\land (\land (\neg (\oplus (\land (\lor op_1 op_0) (\land op_3 op_4)) (\oplus op_1 (\neg (\land op_3 op_4))))) (\neg op_2)) (\neg$ $(\oplus (\land op_1 (\neg (\lor op_4 op_3))) (\land (\lor op_0 op_3) (\land op_4 op_1)))))$ $\rightarrow (\land (\neg (\lor op_2 (\oplus op_1 (\oplus op_4 op_3)))) (\oplus (\land op_1 op_3) (\land op_4 (\neg op_0))))$ $norm_5 (\neg (\land (\neg n_{364}) (\neg n_{324}))))) (\land (\neg n_{364}) (\neg n_{324}))) (\neg (\land (\neg n_{364}) (\neg n_{364}))))$

$n_{324}))))))$

$$\rightarrow (\land (\neg norm_5) (\land n_{324} n_{364}))$$

 $(\land (\land (\oplus (\oplus norm_4 \ norm_9) \ norm_{14}) (\oplus (\land (\oplus norm_4 \ norm_9) \ norm_{14}) (\land$ $norm_4 \ norm_9))) (\oplus (\land (\oplus (\land (\oplus norm_4 \ norm_9) \ norm_{14}) (\land norm_4 \ norm_9)))$ $(\land (\oplus norm_4 \ norm_9) \ norm_{14})) (\neg (\land (\oplus norm_4 \ norm_9) \ norm_{14}))))$ $\rightarrow (\land (\land norm_{14} \ norm_4) \ norm_9)$ 요약

본 논문에서는 탐색기반 기법을 통해 동형암호 회로를 최적화하는 새로운 방법론 을 제시한다. 완전동형암호 기술은 제3자에게 개인정보의 가공 및 보관을 위탁할 수 있게 해주는 차세대 기술이지만, 동형암호 프로그램의 매우 큰 계산비용이라 는 한계점 때문에 널리 쓰이지 못하고 있는 실정이다. 동형암호 상용화를 위해 다양한 방식으로 동형암호 프로그램 최적화가 이루어지고 있지만, 수동으로 동형 암호 프로그램의 성능을 최적화하는 것은 대체로 높은 수준의 전문성을 필요로 하고, 충분한 전문성을 갖추고 있다고 하더라도 매우 고된 과정이다. 또한 동형 암호 프로그램을 자동으로 최적화하는 동형 컴파일러 기술들은 대부분 수동으로 고안된 간단한 최적화 규칙에 의존하고 있기 때문에, 만족할만한 최적화 성능이 나오지 않고 있는 실정이다. 우리는 프로그램 합성 기술을 통해 자동으로 동형암호 회로의 최적화 규칙을 발굴하고 식 다시쓰기 및 동일식 모두탐색 기술을 이용해 이러한 규칙들을 효율적으로 적용하는 방법론을 제시한다. 우리의 방법론은 동형 암호 회로 성능을 결정짓는 가장 중요한 요인인 곱셈깊이를 줄이는 것에 집중한다. 먼저 프로그램 합성을 통해 학습대상 동형암호 프로그램의 작은 일부분과 똑같은 실행의미를 가지면서 곱셈깊이는 더 작은 새로운 프로그램을 찾아내고, 이 부분 프로그램 쌍을 일반화하여 하나의 최적화 규칙으로 학습한다. 학습한 최적화 규 칙은 식 다시쓰기 기술을 기반으로 한 E-매칭을 통해 입력으로 들어오는 최적화 대상 프로그램에 유연하게 적용되며, 이때 최적화의 안전성이 보장된다는 것이 증 명되어있다. 또한 우리의 최적화 방법론은 동일식 모두탐색 기술을 사용해 주어진 제한시간 내에 최적화 규칙들을 적용할 수 있는 서로다른 모든 경우의 수를 탐색하 여 보다 최적에 가까운 결과를 찾아낸다. 널리 쓰이는 실제 동형암호 프로그램들에 대해 최적화를 적용해본 결과, 기존의 동형암호 최적화 방법론에 비해 최소 1.08 배에서 최대 3.17배까지 성능향상을 관측할 수 있었다.(기하평균 1.56배) 우리의 최적화 방법론은 기존의 수동 최적화 방법론을 통한 성능향상을 해치지 않으면서 추가로 적용가능하다는 강점이 있다.

주요어: 동형암호 회로, 프로그램 합성, 식 다시쓰기, 동일식 모두탐색, 최적화, 탐 색기반 기법 **학번**: 2015-22908