Light extraction efficiency of a normal bottom emission organic light emitting diode (OLED) is limited to approximately 20% as most of the emitted light is trapped in the waveguide and glass modes. An etchless simple method was developed to fabricate two-dimensional nanostructures on glass substrate directly by using ultraviolet (UV) curable polymer resin and UV nanoimprint lithography in order to improve output coupling efficiency of OLEDs. The enhancement of the light extraction was predicted by the three-dimensional finite difference time domain method. OLEDs integrated on nanoimprinted substrates enhanced electroluminescence intensity by up to 50% compared to the conventional device. © 2008 American Institute of Physics. [DOI: 10.1063/1.2939554]

**Ultraviolet nanoimprinted polymer nanostructure for organic light emitting diode application**

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Light extraction efficiency of a conventional organic light emitting diode (OLED) remains limited to approximately 20% and 80% of the emitted light is trapped in the waveguide and glass modes. Light extraction from OLEDs becomes the most serious factor limiting the overall efficiency of OLEDs for displays and solid state lighting. There have been many efforts to improve the output coupling efficiency of OLEDs by modifying the substrate surface. The nanoimprint lithography (NIL) process is an attractive method to replace the expensive photolithography and etching process for fabrication of nanoscale PCs. It has been applied to direct fabrication of a PC structure in a glass substrate by using a so called “hot embossing” technique, where high pressure of about 20 bars and high temperature of about 300 °C are required. A simple and cost-effective PC patterning method is still needed to achieve high throughput in the atmosphere.

In this study, an etchless UV-NIL process is examined for patterning of an organic PC structure onto an OLED substrate under atmospheric pressure and at room temperature. The UV-NIL process for the direct patterning of polymer pillars is depicted in Fig. 1. Surface of the stamp was coated by a self-assembled monolayer of trichloro-(1H,1H,2H,2H-perfluorooctyl) silane for easy separation. 60 μL of a UV curable resin was dispensed onto the Si stamp and a glass wafer was placed on it and finally cured by UV exposure for 3 min with an illumination power of 14.4 mW cm⁻². The UV curable resin is composed of two monomers, tri(propylene glycol) diacrylate (Aldrich) and tri(methylol) propane triacrylate (Aldrich). Subsequently, photoinitiator of 2,2-dimethoxy-2-phenylacetophenone (Aldrich) was added to the monomer mixture at 4 wt %. Rad 2200N (TEGO Chemi Service) of 10 wt % was blended as a releasing agent with the prepared mixture. Deposition of a SiNₓ layer on the polymer pillar array as the buffer layer completes the formation of PC structure on the glass substrates. The SiNₓ layer is transparent and has high refractive index (~2.02). It was deposited to make the PC surface planar and to give higher refractive index than that of polymer pillars. In order to achieve a flat surface, SiNₓ is deposited on the PC layer and two types of PCs with different SiNₓ layer thicknesses (500 and 800 nm) were prepared.

The 8 in. silicon wafer was used as the stamp materials after patterned by the deep UV lithography and reactive ion etching of 14.4 mW cm⁻². The UV curable resin is composed of two monomers, tri(propylene glycol) diacrylate (Aldrich) and tri(methylol) propane triacrylate (Aldrich). Subsequently, photoinitiator of 2,2-dimethoxy-2-phenylacetophenone (Aldrich) was added to the monomer mixture at 4 wt %. Rad 2200N (TEGO Chemi Service) of 10 wt % was blended as a releasing agent with the prepared mixture. Deposition of a SiNₓ layer on the polymer pillar array as the buffer layer completes the formation of PC structure on the glass substrates. The SiNₓ layer is transparent and has high refractive index (~2.02). It was deposited to make the PC surface planar and to give higher refractive index than that of polymer pillars. In order to achieve a flat surface, SiNₓ is deposited on the PC layer and two types of PCs with different SiNₓ layer thicknesses (500 and 800 nm) were prepared.

The 8 in. silicon wafer was used as the stamp materials after patterned by the deep UV lithography and reactive ion etching.

**FIG. 1.** Schematic of the UV-NIL procedure for the direct fabrication of a polymer pillar substrate.
etching. The silicon stamp pattern has a hexagonal lattice structure with repeated circular holes. The lattice constant of the holes is 530 nm and both the diameter and the height are 265 nm, which will produce the polymer pillar whose aspect ratio is 1:1. All propagating visible-wavelength light is in the nonguided mode region at that lattice constant and above the band gap of photonic band structure. Hence, greater light intensity can be achieved at oblique viewing angles in the visible region.\(^1\) Figure 2 shows the scanning electron microscope (SEM) image of the fabricated PC. SiN\(_x\) completely fills the gap between the polymer pillars without voids and the surface becomes smoother by depositing the buffer layer Figs. 2c and 2d.

In order to predict the performance of the proposed device, a three-dimensional finite difference time domain (FDTD) simulation was performed.\(^{14}\) The simulation structure included air, a glass substrate, a polymer resin, SiN\(_x\), indium tin oxide, organic layers, and cathode electrode layers. The polymeric PC was modeled using a circular rod shape and the measured geometric parameters. The simulation domain size was \(3.13 \times 2.71 \times 6.19 \, \mu \text{m}^3\) and the time step was 0.0333 fs. The periodic boundary conditions were used on the four vertical sides of the simulation domain in order to reduce the simulation time, and the array size and the two upper and lower horizontal sides were set to have perfectly matched layer boundary conditions. To model the cathode aluminum electrode, the auxiliary differential equations method was employed where the single-pole Drude model was assumed.\(^{14}\) Many Gaussian dipole sources were randomly distributed over the emission layer and the initial phase and the direction of dipole oscillation were randomly assigned at the beginning of the simulation. The center frequency of each Gaussian dipole source was set to be the value corresponding to the OLED peak emission wavelength of 511 nm and the time duration of the Gaussian pulse was determined on the order of femtosecond for the source to have a broad emission wavelength band of about 50 nm. The simulation result is shown in Fig. 3. The y axis is the time integration of the Poynting vector in the z direction over the top surface in air. The integrated z-Poynting vector is a measure of the light emission out of the glass into the air. The extraction efficiency remained nearly constant for various glass thickness, as shown in Fig. 3. Therefore, the air layer can be included in calculation structure without excessive time consumption. The predicted light extraction efficiency of the polymer PC was enhanced by nearly 70%.

Enhancement of the light extraction efficiency in the forward direction (glass side) and emission characteristics are also confirmed from the experiments. Relative integrated spectra of the light emitted from the device were measured using an integrating sphere (Labsphere Co., 6 in. diameter). Figure 4 shows the emission spectra of the device with relative EL intensities. The electroluminescence (EL) intensity of the PC device increases by more than 50% compared to the conventional device without a PC. Moreover, the enhancement of the light extraction from the PC-OLEDs is represented by means of the integrated light-output power versus the current density, as shown in Fig. 5. The EL intensity was increased by 54% compared to the conventional device. Thinner SiN\(_x\) buffer layer gives a little higher light extraction
efficiency because the thick SiN$_x$ film causes additional light absorption.

As shown in the emission images in Fig. 4, the conventional OLED emits light with a symmetric distribution in all in-plain directions, while the PC device produces a pattern with sixfold symmetry, reflecting the hexagonal structure of the PC layer. Moreover, the profiles are represented in terms of the luminance efficiency in the inset of Fig. 4. The maximum luminance efficiency of the PC device is $5.95 \text{ cd A}^{-1}$ at a tilted direction of $40^\circ$, which is an improvement of nearly 30% of the maximum luminance of the reference. The profile of the reference device is very similar to the Lambertian profile, whereas the PC device produces a radiation shape that is similar to butterfly wings whose area is about 1.5 times larger than the reference. The OLEDs integrated on PC structures not only give high luminance efficiency but also exhibit interesting emission patterns depending on the symmetry and dimensions of the pillars such as lattice constant, diameter, height, and arrangement. In conclusion, an etchless UV-NIL process is developed to fabricate organic PC structures as an OLED substrate with 50% enhancement of the device efficiency.